



# **RAMACHANDRA COLLEGE OF ENGINEERING** (Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada) NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

6.3.3 Average number of professional development / administrative training programs organized by the Institution for teaching and non teaching staff

Sl. No.	Year	Title of the professional development program organised for teaching staff	Title of the administrative training program organised for non-teaching staff	Dates (from-to)	No. of participants
<b>2018-19</b>					
1	2018-19	A 5 Day FDP on outcome based Education and accreditation		04-06-2018 to 08-06-2018	50
2	2018-19	Workshop on Online Permissions for Buildings		18-06-2019	1
3	2018-19	FDP on Perspectives of Research and Technical Paper Writing		02-03-2019	10
4	2018-19	Faculty Orientation Program on Theory and Lab Courses for Electrical and Electronic Engineering	Orientation Program on Lab Courses	02-11-2018 to 10-11-2018	27
5	2018-19	Lab Orientation Program for Electrical and Electronic Engineering		29-10-2018 to 01-11-2018	15
6	2018-19	Faculty Orientation on Theory Courses for Computer science and Engineering		01-11-2018 to 06-11-2018	25
7	2018-19	Faculty Orientation on Lab Courses for Computer science and Engineering		14-11-2018 to 16-11-2018	20
8	2018-19	Seminar on Advanced Finite Elements Analysis		27-7-2018 to 28-07-2018	18
9	2018-19	National workshop on HEP-1		04-07-18 to 08-07-18	20
10	2018-19	FDP ON HFSS		29-09-2018	21
11	2018-19	Workshop on MATLAB		19-7-18 to 23-7-18	25
12	2018-19	Faculty Orientation Program on Theory Courses	Orientation Program on Lab Courses	7-5-2018 to 13-5-2018	28
13	2018-19	Faculty Orientation Program on Theory Courses		5-11-2018 to 14-11-2018	26

2017-18					
14	2017-18	A 5 day Faculty Development Program		07-11-2017 to 11-11-2017	100
15	2017-18	FDP on how to do research and how to write a research paper		15-09-2017	55
16	2017-18	FDP on producing Quality Engineers		14-07-2017	75
17	2017-18	Workshop on Outcome Based Education		20-09-2017	150
18	2017-18	NA	Training on Python Programming	07-06-2018	6
19	2017-18	NA	Training on Unix Programming	04-06-2018	6
20	2017-18	Three Day Faculty Development Program on CREO Parametric Training		17-08-2017 to 19-08-2017	15
21	2017-18	Embedded Systems Workshop		07-08-2017	30
22	2017-18	Antenna design using HFSS		04-09-2017	28
23	2017-18	Faculty Orientation Program on Theory Courses		01-11-2017 to 04-11-2018	28
24	2017-18	Faculty Orientation Program on Theory Courses		23-4-2017 to 28-04-2017	24
25	2017-18	Seminar on Social and legal issues		10-08-2017 to 11-08-2017	10
26	2017-18	Awareness on Innovative Project Management		20-12-2017 to 21-12-2017	11
27	2017-18	Seminar on professional and social relations in society		15-02-2018 to 16-02-2018	14
2016-17					
28	2016-17	FDP on Theory Courses		2-05-2016 to 11-05-2016	20
29	2016-17	NA	Training on C Programming	09-05-2016	8
30	2016-17	NA	Training on Java Programming	11-11-2016	8
31	2016-17	International conference on Functional Materials, Characterization , Solid State Physics Power, Thermal and Combustion Energy		07-04-2017 to 08-04-2017	15
32	2016-17	Workshop on Applications Of Non Destructive Testing In Various Industries		08-02-2017 to 09-02-2017	15
33	2016-17	Three Day National Level Workshop on Computational Research Using Mat Lab		10-11-2016 to 12-11-2016	16
34	2016-17	One Day Workshop on Entrepreneurship Development Organized By NSIC Technical Service Centre		19-7-2016	20



35	2016-17	Faculty Orientation Program on Theory Courses	Lab orientation Program	2-5 to 11-5-2016	27
36	2016-17	Faculty Orientation Program on Theory Courses		17-10 to 28 -10-2016	21
37	2016-17	Workshop on Research Methodologies		03-02-2017 to 04-02-2017	25
38	2016-17	Embedded Systems Workshop		13-07-2016 to 15-7-2016	23
39	2016-17	Workshop on Individual and Team work, Leadership Capability		18-07-2016 to 19-07-2016	10
<b>2015-16</b>					
40	2015-16	NA	Training on C Programming	01-06-2015	8
41	2015-16	NA	Training on Java Programming	12-11-2015	8
42	2015-16	Faculty Orientation Program on Theory Courses	Lab orientation Program	14-5-2016 to 15-5-2016	20
43	2015-16	Faculty Orientation Program on Theory Courses		5-5-2015 to 15-5-2015	24
44	2015-16	Apps Development Using Android		04-03-2016 to 05-03-2016	8
45	2015-16	Workshop on 3D Animations		20-11-2015 to 21-11-2015	10
<b>2014-15</b>					
46	2014-15	NA	Training on DBMS	12-11-2014	4
47	2014-15	NA	Training on C Programming	04-06-2014	4
48	2014-15	NA	Lab orientation Program	19-05-2014 to 22-05-2014	20
49	2014-15	Faculty Orientation Program on Theory Courses		2-5-2014 to 12-5-2014	20
50	2014-15	Workshop on Software Engineering and Testing		23-01-2015 to 24-01-2015	10
51	2014-15	Workshop on Android Frame Work		24-10-2014 to 25-10-2014	6
52	2014-15	Workshop on Cyber Crime and Ethical Hacking		05-09-2014 to 06-09-2014	7

  
**PRINCIPAL**  
 Ramachandra College of Engineering  
 VATLUR (V), ELURU - 534 007  
 West Godavari District





# RAMACHANDRA COLLEGE OF ENGINEERING

NH-5 Bypass Road, Vatluru (V), Eluru - 534007, W. G. Dist., A.P.  
(Approved by AICTE, New Delhi, and Affiliated to JNT University, Kakinada)  
NAAC Accredited with B++, ISO 9001 : 2008 Certified

Organized by : Faculty Development Programme  
: S&H  
Format:9014/0  
Name of the Speaker : Dr.E.S.M.Suresh and Dr.V.Shanmugha Neethi  
Designation : Professor and Assistant Professor, NITTTR, Chennai  
Topic : Outcome based education and Accreditation  
Venue : IEDC Conference Hall  
Date & Time : From: 4<sup>th</sup> June, 2018 to 8<sup>th</sup> June, 2018 (9.00-4.00pm)  
Conducted for :

Branch	No of Faculty Attended
Civil	1
EEE	3
ME	7
ECE	5
CSE	15
MBA	5
S&H	14

**Total No of Students Attended**

50

## Profile of NITTTR

Dr.ESmSuresh and Dr V.Shanmugha Neethi Professor and Assistant professors in Civil and CSE departments of NITTTR, Chennai. The National Institute of Technical Teachers Training and Research (NITTTR) Chennai was established as an autonomous Institute by the Ministry of Human Resource Development, Government of India in the year 1964 to improve the quality of Engineering Education system in India and in the Southern Region in particular. Within this mandate, the institute takes initiatives to offer need based Human Resource Development programmes through appropriate modes and develop curricula and instructional resources. It also fosters research in the inter disciplinary area of Engineering Education and offers consultancy and extension services for the total development of Engineering Colleges, Polytechnic Colleges, Vocational institutions, Industry, Service sector and the Community at large. NITTTR Chennai has a rich experience in conducting pedagogical Faculty Development Programmes (FDPs) on various areas as well as Engineering specialization. The AICTE has recognized the short-term training programmes conducted by NITTTR for consideration for the purpose of movement to higher grades under the Career Advancement Scheme of the faculty members.

## Report

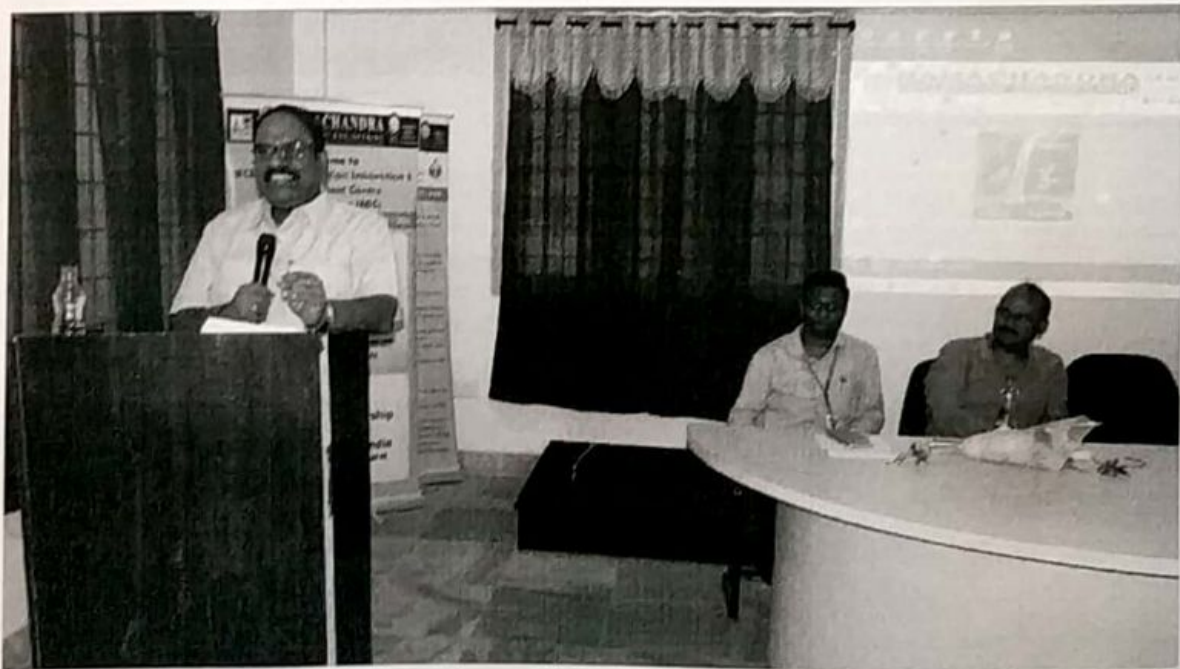
### Faculty Development Programme on "Outcome Based Education and Accreditation"

A five day Faculty Development Programme was organized by The National Institute of Technical Teachers Training and Research (NITTTR) Chennai from 4<sup>th</sup> to 8<sup>th</sup> June, 2018. 50 Faculty members had taken part in this programme. The programme was inaugurated with the lighting of lamp by Dr.E.S.M.Suresh, Professor in Civil Engineering, NITTTR, Chennai Honourable Secretary Mr.K.Venugopal, Secretary, RamaChandra College of Engineering and Dr.Sanjay Dola Principal had addressed the gathering. Faculty Skill Development Centre Naipunya was established for enhancing teaching skills. The Naipunya poster was launched by the Secretary, Principal and all Head of the Department. Dr.E.S.M.Suresh, Course Coordinator delivered lectures on NBA Accreditation and Teaching Learning Process. Another Resource person Dr.Shanmugha Neethi, NITTTR joined the FDP explaining various criterias in NBA Process. Several Group Discussion sessions were organized for practicing the new methodologies Resource persons trained the faculty in Computer lab to introduce new tools for developing Google class and delivery of study materials













**3. Feedback from Faculty:** Very much useful and asked to conduct every year.

**4. Remarks from Resource Person:**

Dr.ESM.Suresh asked the faculty to save 5% of their salary to enhance their career in various aspect by attending different programmes and Dr.V.ShanmughaNeethi invited the faculty to join in online courses under SWAYAM program of NITTTR.

*Dr. K.V. Padma Prasad*  
Signature of the Organizer

*[Signature]*  
Principal





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Report on "One Day Workshop Online Permissions for Buildings" organized by Municipal Corporation Eluru on 18<sup>th</sup> June 2018.

The One Day Workshop Online Permissions for Buildings was started at Municipal Corporation Eluru.

Organized by : Municipal Corporation Eluru.

Place of visit : Municipal Corporation Eluru

Date : 18<sup>th</sup> June 2018.



Students of IV B.TECH & Completed Final Years in CIVIL ENGINEERING DEPARTMENT total of 50 students accompanied with 1 staff members went to the Workshop and tour was started with our beloved HOD wishes, Principal and Management encouragement.

First we visited Municipal Corporation Office in Eluru, Town Planning Engineers & ACB gave Information about Online Permission of Buildings & Step Wise Explanation about Permissions

We are very glad that we have a good opportunity to learn about different things in the form of industrial visit. We are very thankful to the management, principal, department Hod and staff members. Some of the Work shop pictures were shown below.



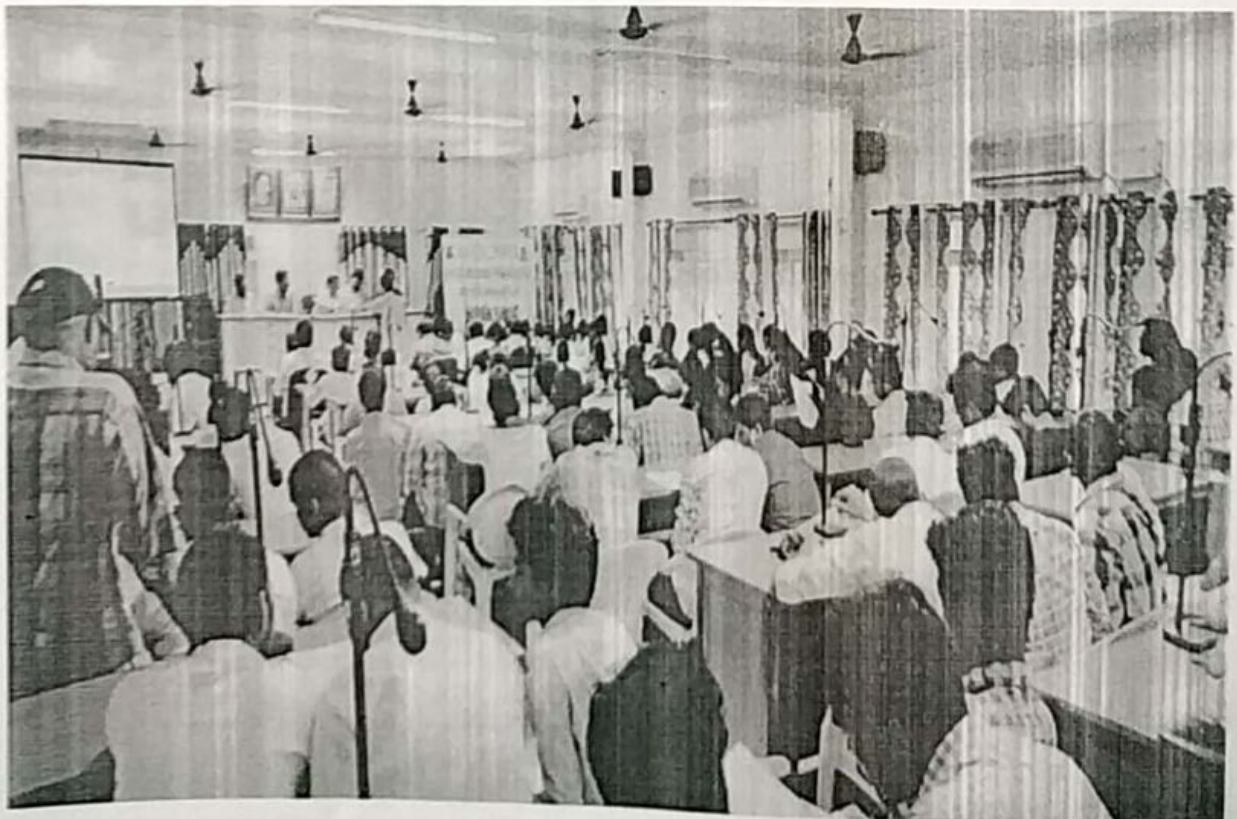


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During Work Shop Session







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End of the Workshop for Online Permissions for Buildings Session

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EEE

# PCB DESIGNING WORKSHOP



Purple Techno Solutions  
*Build ideas for better ideas*

## What is this program about?

In this course, our experts will take you through the process of making Printed Circuit Boards (PCBs) from scratch! You'll make a PCB prototype to function as a clap circuit - the system detects clap sound to power an LED bulb.

Here's what you'll learn by working on this project:

- Circuit design using software tools
- PCB fabrication as per design input
- Testing PCB after loading components

The aim of this workshop is to make the students learn the designing and manufacturing of a printed circuit board using open source PCB Wizard design software and with various active and passive components such as Regulators, Diodes, Resistors, Capacitors, Inductors, Switches, e.t.c.

## Course Highlights

- Design and Manufacture your own PCB
- Develop and Test your own Clap circuit
- Understand the steps and processes involved in PCB manufacturing
- Developing the schematics of circuit using industry grade software

## Kit Content

- |                            |                          |
|----------------------------|--------------------------|
| • Copper clad board        | • Sand paper             |
| • Ferric chloride solution | • Laser printer          |
| • Acetone                  | • Soldering gun          |
| • Operational Amplifier    | • Hand drillers          |
| • Resistors                | • Millimeter             |
| • Color LEDs               | • Other Mechanical Tools |
| • OHP transparent sheet    |                          |



**Note:** All the above components would be provided during the program to participants in groups of 3 but would be taken back at the end.

Participants registering in groups of 5 will get take-home kit for free. Take-home kit consists Of a Board designed by students

### Day 1

#### 1. Basic PCB Concepts

First of all they have given the concepts which will be very helpful for designing the PCB practically, using some power point presentations. In this theoretical explanation part they have explained about the PCB Wizard software and the use of software for further practical implementation in designing the PCB. They have also given a briefing about active and passive electronic components which they will be using in a PCB.

#### 2. Editing and Routing

Editing and Routing is the basic step and it is one of the important step for designing a PCB. Editing and Routing gives the circuit layout from one component to the other components. Soldering plays a key role in this step.

#### 3. Creation of Library and Components & Report Generation

This is a step done using PCB Wizard tools. In this step the components in the circuit and the respective libraries are selected in this software. So that the required circuit will be designed in the software and a print of the same will be taken on a sheet. The same print will be useful for the further process.

### Day 2

#### 4. Toner Transfer Method

This is the step where the designed circuit will be pasted on the wafer and this will be passed through a temperature of about 160 to 180 degrees so that the tracks of the circuit will be remained on the wafer. The tracks will be a conductive type.

#### 5. Drilling Technique

The board will be drilled with holes where the components have to be placed; the holes will be drilled in the board depending on the terminals available for the components in the design. The hole should be in the size so that the terminal has to be freely placed in the hole.

#### 6. Soldering Technique

The components that are placed in the board should be soldered to the track so that the circuit is connected as per the design. After this step the engraved PCB will be ready to use.



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**B++ Grade**

Format:9014/0

Report on 2 days workshop on "Hands on Design of PCB Design" organized by  
Department of Electrical and Electrical Engineering, Ramachandra College of Engineering,  
Eluru from 14<sup>th</sup> & 15<sup>th</sup> December, 2018

The 2-days workshop has been started with the inaugural Principal Dr.Dola Sanjay.S.

Organized by	: Department of EEE
Name of the Resource persons	: Mr. B.Suresh
Designation	: Entrepreneur, Purple Techno Solutions, Vijayawada
Topic	: 2 Days workshop on Hands on Design of PCB Design
Venue	: e-class room & R&D Projects Laboratory
Date & Time	: 14 <sup>th</sup> & 15 <sup>th</sup> December, 2018



# RAMACHANDRA COLLEGE OF ENGINEERING

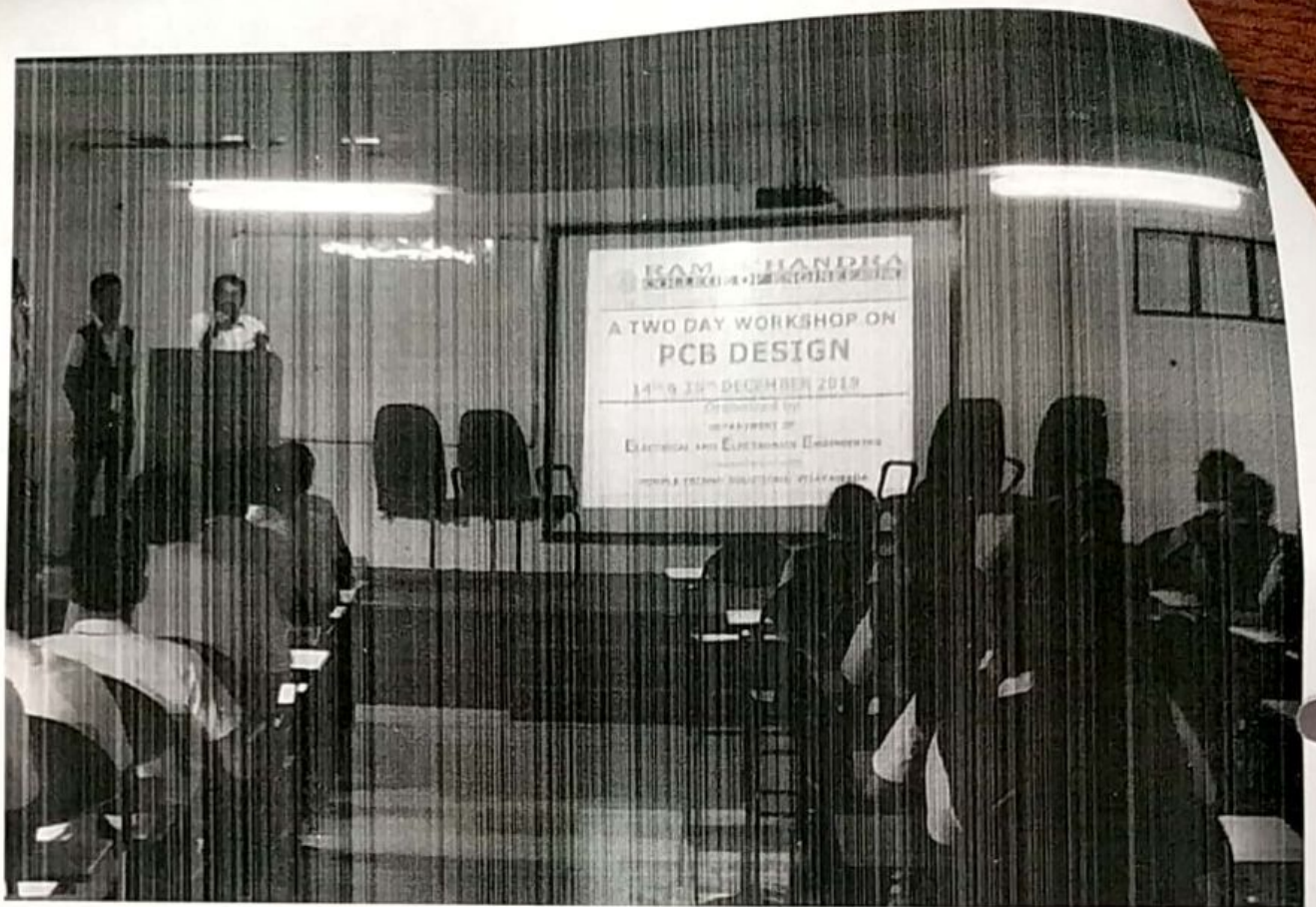
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NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

## A TWO DAYS WORKSHOP ON PCB DESIGN

14<sup>th</sup> & 15<sup>th</sup> DECEMBER 2018

Organized by  
DEPARTMENT OF  
**ELECTRICAL AND ELECTRONICS ENGINEERING**  
In association with  
**PURPLE TECHNO SOLUTIONS, VIJAYAWADA**





The program starts with the prayer song, followed by inviting the dignitaries on to the dais and our Principal addressed the gathering.

Our principal Dr.Dola Sanjay.S deeply explained the importance of improving the practical knowledge. And he appreciated the Department of EEE for arranging this kind of workshop to enrich the knowledge of the students. And also he narrated the importance of PCB's, which is the technology introduced to the students through this workshop.

And the Head of the Department, Prof.S.Jayalakshmi addressed the gathering and explain the main motto of the workshop is to bridge the gap between industry requirement and the student knowledge.





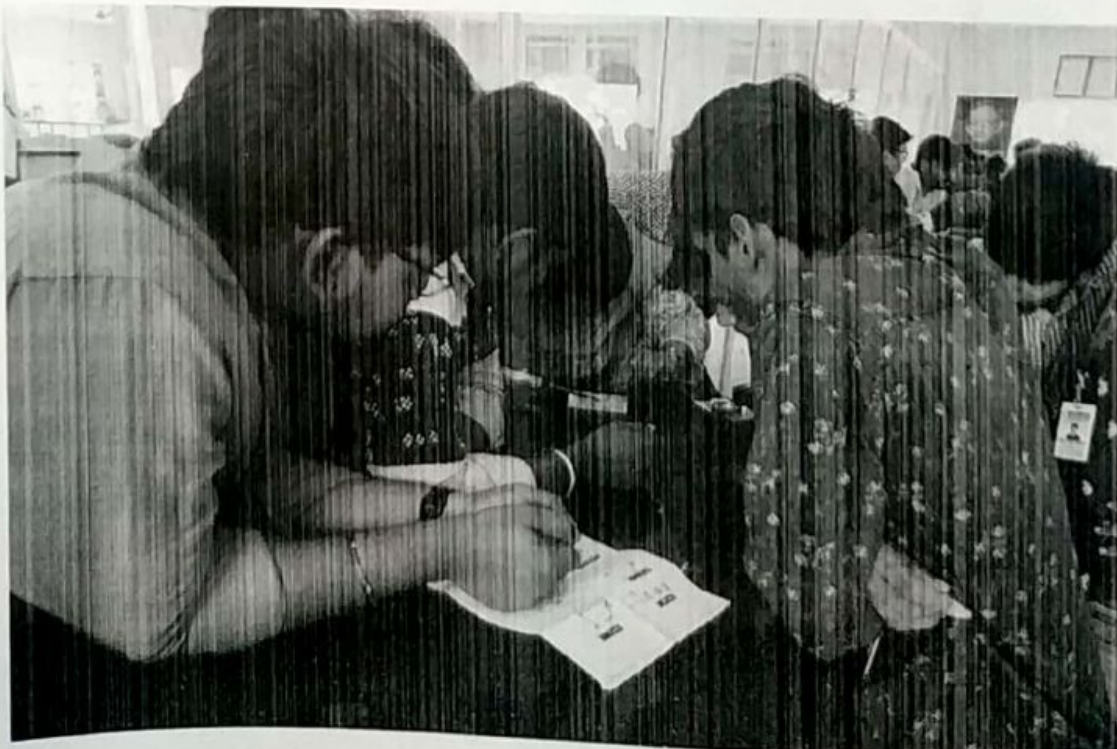
Then, the resource person Mr.Suresh and Mr.Khadhar Basha addressed the students and suggested to enhance the practical skills. And also he explained about the Internet of things. And the session starts,



**Topics covered:** Students learnt about

1. Circuit desing using software tools
2. PCB fabrication as per desing input
3. Testing PCB after loading components

The aim this workshop is to make the students learn the designing and manufacturing of a PCB using Open Source PCB Wizard desing software and with various active and passive components such as resistors, diodes, regulators, capacitors, inductors and switches etc.,





**Course Highlights:**

- ✓ Design and Manufacture own pcb
- ✓ Develop and test own clap circuit
- ✓ Understand the steps and process involved in PCB manufacturing
- ✓ Developing the Schematics of circuits using industry grade software

**Day-1:** Explaining the basics of PCB concepts, editing and routing gives the circuits lay out from one component to the other components. Soldering plays a key role in this step. Creation of library and components & Report generation. In this step the components in the circuit and respective libraries are selected in the software so that the required circuit will be designed in the software and a print of the same will be taken on a sheet. The same print will be useful for the further process.







**Day-2:** Explaining the toner transfer method: This is the step where the designed circuit will be pasted on the wafer and this will be passed through a temperature of about 160-180 degrees so that the tracks of the circuit will be remained on the wafer. The tracks will be a conductive type.

Drilling technique: the board will be drilled with holes where the components to be placed.

Soldering technique: The components that are placed in the board should be soldered to the track so that the circuit is connected as per the design. After this step the engraved PCB will be ready to use.

Followed by Demonstration and Valedictory





and in the **evening of Day-2**, our students explain the concepts of their project to Principal Dr.Dola Sanjay.S and he appreciated the students for their work for the past three days and gave his feedback about their projects. After this, with the valedictory program, the workshop session came to an end.

### 3. Feedback from students:

Students feel that this workshop really helped them to know the basic fundamental concepts of Microcontroller operation. By having the hands on experience, really they feel satisfied and they have the feel that they have learnt the things. And also they give the feedback that this kind of workshop has to be conducted at least once in a semester.

### 4. Remarks from Resource Person:

Resource persons gave the feedback that the students are stuff. And they have asked very meaningful doubts and they enjoy the session.

  
HOD 19/12/18

  
Principal 19/12/18



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## DEPARTMENT OF EEE REPORT ON LAB AND THEORY ORIENTATION PROGRAM

In the Department of EEE, lab orientation program has been conducted as per the following schedule and the report which consists of the list of experiments performed and the list of faculty members participated in the orientation program, has been enclosed.

Theory orientation program has been conducted and the list of faculty participated, attended and the topics delivered has <sup>been</sup> enclosed in the report.

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19/11/15

*Lab*  
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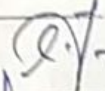
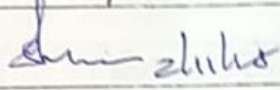
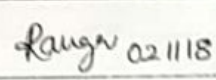

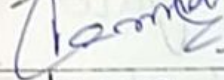

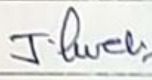
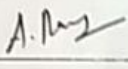



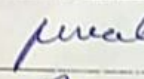
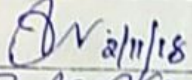
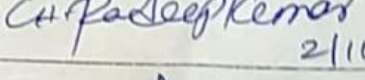


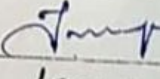
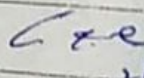


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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING ORIENTATION PROGRAM FOR THEORY A.Y.: 2018-19 EVEN SEMESTER

2/11/18

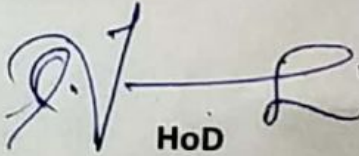
S.No	Name of the Faculty	Signature
1	Dr.S.Jaya Lakshmi	 2/11/18
2	Dr.S.Krishna Kumar	
3	Dr.Jarabala Ranga	 02/11/18
4	Mr.V.Satyanarayana	
5	Mr.S.S.Sarma	 2/11/18
6	Mr.B.Ashok Kumar	leave
7	Mr.K.R.K.Pandu	
8	Mr.J.Suresh	 2/11/18
9	Mr.A.Pavankumar	
10	Mr.G.Shinyvikram	
11	Mr.K.Sathish Kumar	 2/11/18
12	Mr.B.Narendra	
13	Mr.S.M.V.Durgarao	 2/11/18
14	Mr.P.Devadass	 2/11/18
15	Mr.Ch.Pradeepkumar	 2/11/18
16	Mr.T.Prathap	
17	Mr.V.N.S.R.Murthy	
18	Ms.M.Jayasree	 2/11/18
19	Mrs.B.Pathrisamma	leave
20	Mr. G.Mahesh	 2/11/18

21	Ms. Ch.Kowsthubha	absent 2/11/18
22	Ch. D.U. V.Sivarani	Leave
23	Mrs.G.Haripriya	Leave
24	Mr.N.Ramesh	Leave
25	A.Sri Sailaja	Leave
26	Mr.Ch.Phaniprasad	Leave
27	Ms.K.Sailakshmi	Leave

Topic:- Logic gates by Mr. S. S. Sarma

Topic:- Introduction to DCS by Dr. J. Ranga

Topic:- Shunt Compensation by Mr. G. Shiny Vikram

  
HoD



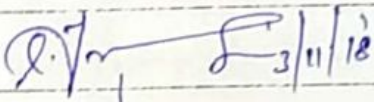
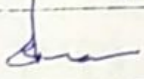
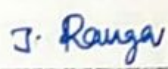
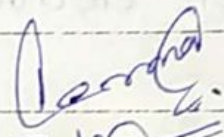

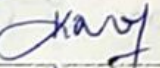
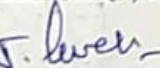
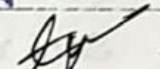
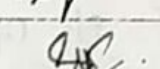
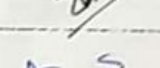






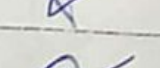

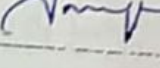



# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK, Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING ORIENTATION PROGRAM FOR THEORY A.Y.: 2018-19 EVEN SEMESTER

03/11/18

S.No	Name of the Faculty	Signature
1	Dr.S.Jaya Lakshmi	
2	Dr.S.Krishna Kumar	
3	Dr.Jarabala Ranga	
4	Mr.V.Satyanarayana	
5	Mr.S.S.Sarma	
6	Mr.B.Ashok Kumar	
7	Mr.K.R.K.Pandu	
8	Mr.J.Suresh	
9	Mr.A.Pavankumar	
10	Mr.G.Shinyvikram	
11	Mr.K.Sathish Kumar	
12	Mr.B.Narendra	
13	Mr.S.M.V.Durgarao	
14	Mr.P.Devadass	
15	Mr.Ch.Pradeepkumar	
16	Mr.T.Prathap	
17	Mr.V.N.S.R.Murthy	
18	Ms.M.Jayasree	
19	Mrs.B.Pathrisamma	
20	Mr.G.Mahesh	





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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING ORIENTATION PROGRAM FOR THEORY A.Y.: 2018-19 EVEN SEMESTER

05/11/18

S.No	Name of the Faculty	Signature
1	Dr.S.Jaya Lakshmi	
2	Dr.S.Krishna Kumar	leave
3	Dr.Jarabala Ranga	
4	Mr.V.Satyanarayana	
5	Mr.S.S.Sarma	
6	Mr.B.Ashok Kumar	
7	Mr.K.R.K.Pandu	
8	Mr.J.Suresh	
9	Mr.A.Pavankumar	
10	Mr.G.Shinyvikram	
11	Mr.K.Sathish Kumar	
12	Mr.B.Narendra	
13	Mr.S.M.V.Durgarao	leave
14	Mr.P.Devadass	
15	Mr.Ch.Pradeepkumar	
16	Mr.T.Prathap	
17	Mr.V.N.S.R.Murthy	
18	Ms.M.Jayasree	
19	Mrs.B.Pathrisamma	
20	Mr. G.Mahesh	



21	Ms. Ch.Kowsthubha	<i>Ch. Kowsthubha</i>
22	Ch. D.U. V.Sivarani	<i>Ch. D.U. V.Sivarani</i>
23	Mrs.G.Haripriya	<i>G. Haripriya</i>
24	Mr.N.Ramesh	<i>N. Ramesh</i>
25	A.Sri Sailaja	<i>A. Sri Sailaja</i>
26	Mr.Ch.Phaniprasad	<i>Ch. Phaniprasad</i>
27	Ms.K.Sailakshmi	<i>K. Sailakshmi</i>

Details of topic delivered during orientation program

NAME OF THE FACULTY	TOPIC	SIGNATURE
MY. K.SATHISH KUMAR	ARCHITECTURE OF 8086	<i>K. Sathish Kumar</i>
Mr. V. Satyanarayana	Graph theory	<i>V. Satyanarayana</i>
MY. K.R.K. Pandu	Block diagram of Electric drive	<i>K.R.K. Pandu</i>

*[Signature]*  
HoD



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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING ORIENTATION PROGRAM FOR THEORY A.Y.: 2018-19 EVEN SEMESTER

06/11/18

S.No	Name of the Faculty	Signature
1	Dr.S.Jaya Lakshmi	
2	Dr.S.Krishna Kumar	
3	Dr.Jarabala Ranga	
4	Mr.V.Satyanarayana	
5	Mr.S.S.Sarma	
6	Mr.B.Ashok Kumar	
7	Mr.K.R.K.Pandu	
8	Mr.J.Suresh	
9	Mr.A.Pavankumar	
10	Mr.G.Shinyvikram	
11	Mr.K.Sathish Kumar	
12	Mr.B.Narendra	
13	Mr.S.M.V.Durgarao	
14	Mr.P.Devadass	
15	Mr.Ch.Pradeepkumar	
16	Mr.T.Prathap	
17	Mr.V.N.S.R.Murthy	
18	Ms.M.Jayasree	
19	Mrs.B.Pathrisamma	
20	Mr.G.Mahesh	



21	Ms. Ch.Kowsthubha	<i>Ch. Kowsthubha</i>
22	Ch. D.U. V.Sivarani	<i>Ch. D.U. V.Sivarani</i>
23	Mrs.G.Haripriya	<i>G. Haripriya</i>
24	Mr.N.Ramesa	<i>N. Ramesa</i>
25	A.Sri Sailaja	<i>A. Sri Sailaja</i>
26	Mr.Ch.Phaniprasad	<i>Ch. Phaniprasad</i>
27	Ms.K.Sailakshmi	<i>K. Sailakshmi</i>

Details of topic delivered by faculty during orientation Program:

NAME OF THE FACULTY	TOPIC	SIGNATURE
Dr. S. Jayalakshmi	Signal flow graphs	<i>Dr. S. Jayalakshmi</i>
Dr. S. Krishna Kumar	Z-transforms	

*Dr. S. Jayalakshmi*  
HoD



**RAMAKRISHNA COLLEGE OF ENGINEERING AND TECHNOLOGY**  
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**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**  
**ORIENTATION PROGRAM FOR THEORY**  
**A.Y.: 2018-19 EVEN SEMESTER**

8/11/18

S.No	Name of the Faculty	Signature
1	Dr.S.Jaya Lakshmi	
2	Dr.S.Krishna Kumar	
3	Dr.Jarabala Ranga	
4	Mr.V.Satyanarayana	
5	Mr.S.S.Sarma	
6	Mr.B.Ashok Kumar	
7	Mr.K.R.K.Pandu	
8	Mr.J.Suresh	
9	Mr.A.Pavankumar	
10	Mr.G.Shinyvikram	
11	Mr.K.Sathish Kumar	
12	Mr.B.Narendra	
13	Mr.S.M.V.Durgarao	
14	Mr.P.Devadass	
15	Mr.Ch.Pradeepkumar	
16	Mr.T.Prathap	
17	Mr.V.N.S.R.Murthy	
18	Ms.M.Jayasree	
19	Mrs.B.Pathrisamma	
20	Mr. G.Mahesh	



21	Ms. Ch.Kowsthubha	ch.kowsthubha
22	Ch. D.U. V.Sivarani	ch.d.u.v.sivarani
23	Mrs.G.Haripriya	G.Haripriya
24	Mr.N.Ramesh	N.Ramesh
25	A.Sri Sailaja	A.Sri Sailaja
26	Mr.Ch.Phaniprasad	Ch.Phaniprasad
27	Ms.K.Sailakshmi	K.Sailakshmi

Details of topic delivered by faculty during orientation program

NAME OF THE FACULTY	TOPIC	SIGNATURE
T. PRATHAP	speed control of DC motors	T. Prathap
B. Narendra	Construction and principle of operation of Scotted reluctance motor	B. Narendra
A. Pavan Kumar	Distributions system	A. Pavan Kumar

HoD 8/11



# RAMACHANDRA COLLEGE OF ENGINEERING

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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING ORIENTATION PROGRAM FOR THEORY A.Y.: 2018-19 EVEN SEMESTER

09/11/2018

S.No	Name of the Faculty	Signature
1	Dr.S.Jaya Lakshmi	
2	Dr.S.Krishna Kumar	
3	Dr.Jarabala Ranga	
4	Mr.V.Satyanarayana	
5	Mr.S.S.Sarma	
6	Mr.B.Ashok Kumar	
7	Mr.K.R.K.Pandu	
8	Mr.J.Suresh	
9	Mr.A.Pavankumar	
10	Mr.G.Shinyvikram	
11	Mr.K.Sathish Kumar	
12	Mr.B.Narendra	
13	Mr.S.M.V.Durgarao	
14	Mr.P.Devadass	
15	Mr.Ch.Pradeepkumar	
16	Mr.T.Prathap	
17	Mr.V.N.S.R.Murthy	
18	Ms.M.Jayasree	
19	Mrs.B.Pathrisamma	
20	Mr. G.Mahesh	



21	Ms. Ch.Kowsthubha	<i>Ch. Kowsthubha</i>
22	Ch. D.U. V.Sivarani	<i>Ch. D.U. V.Sivarani</i>
23	Mrs.G.Haripriya	<i>G. Haripriya</i>
24	Mr.N.Ramesh	<i>N. Ramesh</i>
25	A.Sri Sailaja	<i>A. Sri Sailaja</i>
26	Mr.Ch.Phaniprasad	<i>Ch. Phaniprasad</i>
27	Ms.K.Sailakshmi	<i>K. Sailakshmi</i>

Details of topic delivered by faculty during Orientation program :-

NAME OF THE FACULTY	TOPIC	SIGNATURE
Ch. KTSR Kowsthubha	Engineers Responsibility towards Safety & Risk	<i>Ch. KTSR Kowsthubha</i>
M. Jayasree	Human Values & Ethics	<i>M. Jayasree</i>
P. Devadass	Classification of D.C. Generators	<i>P. Devadass</i>

*[Signature]*  
HoD





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## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING ORIENTATION PROGRAM FOR THEORY A.Y.: 2018-19 EVEN SEMESTER

10/11/18

S.No	Name of the Faculty	Signature
1	Dr.S.Jaya Lakshmi	
2	Dr.S.Krishna Kumar	
3	Dr.Jarabala Ranga	
4	Mr.V.Satyanarayana	
5	Mr.S.S.Sarma	
6	Mr.B.Ashok Kumar	
7	Mr.K.R.K.Pandu	
8	Mr.J.Suresh	
9	Mr.A.Pavankumar	
10	Mr.G.Shinyvikram	
11	Mr.K.Sathish Kumar	
12	Mr.B.Narendra	
13	Mr.S.M.V.Durgarao	
14	Mr.P.Devadass	
15	Mr.Ch.Pradeepkumar	
16	Mr.T.Prathap	
17	Mr.V.N.S.R.Murthy	
18	Ms.M.Jayasree	
19	Mrs.B.Pathrisamma	
20	Mr. G.Mahesh	



21	Ms. Ch. Kowsthubha	Ch. K.
22	Ch. D.U. V. Sivarani	Ch. D.U.
23	Mrs. G. Haripriya	G. Haripriya
24	Mr. N. Ramesh	N. Ramesh
25	A. Sri Sailaja	A. Sri
26	Mr. Ch. Phani Dasad	Ch. Phani
27	Ms. K. Sailakshmi	K. Sailakshmi

Details of topic delivered by faculty during orientation program

NAME OF THE FACULTY	TOPIC	SIGNATURE
V.V.S.R. Murthy	Mathematical Models & Control Syst	V.V.S.R. Murthy
G. Mahesh	Construction & working of PMMC	G. Mahesh
M.V. Durgadevi	Introduction to ECA	M.V. Durgadevi

*[Signature]*  
HoD 10/11



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## DEPARTMENT OF EEE REPORT ON LAB AND THEORY ORIENTATION PROGRAM

In the Department of EEE, lab orientation program has been conducted as per the following schedule and the report which consists of the list of experiments performed and the list of faculty members participated in the orientation program, has been enclosed.

Theory orientation program has been conducted and the list of faculty participated, attended and the topics delivered has <sup>been</sup> enclosed in the report.

*[Handwritten Signature]*  
15/11/15





# RAMACHANDRA

## COLLEGE OF ENGINEERING

APPROVED BY AICTE, NEW DELHI. AFFILIATED TO JNTUK, KAKINADA

### DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

#### LAB ORIENTATION REPORT

NAME OF THE LAB: ELECTRICAL MACHINES-I LAB  
 NAME OF THE FACULTY: J.SURESH

A.Y.: 2018-19  
 DEPARTMENT: EEE

CLASS/SEM: II-II  
 DATE: 29/10/2018

The following experiments of the ELECTRICAL MACHINES-I Lab have been demonstrated in the presence of the list of faculty given below.

S.no	Name of the Experiment	Regular/Add-on	Remarks
1	Magnetization characteristics of DC shunt generator. Determination of critical field resistance and critical speed.	Regular	
2	Brake test on DC shunt motor. Determination of performance curves.	Regular	
3	Hopkinson's test on DC shunt machines. Predetermination of efficiency	Regular	
4	Swinburne's test and Predetermination of efficiencies as Generator and Motor.	Regular	
5	Speed control of DC shunt motor by Field and armature Control.	Regular	
6	Separation of losses in DC shunts motor.	Regular	
7	OC& SC test on single phase transformer.	Regular	
8	Sumner's test on single phase transformer.	Regular	
9	Scott connection of transformers.	Regular	
10	Parallel operation of Single phase transformer.	Regular	
11	Field test on d.c machines	Add-on	
12	Brake test on D.C compound motor.	Add-on	

Name of the Faculty	Designation	Signature
Dr.J. RANGA	ASSOCIATE PROFESSOR	<i>[Signature]</i>
V.SATYANARAYANA	ASSOCIATE PROFESSOR	<i>[Signature]</i>
S.S.SARMA	ASSOCIATE PROFESSOR	<i>[Signature]</i>
B.ASHOK KUMAR	ASSOCIATE PROFESSOR	<i>[Signature]</i>
K.R.K.PANDU	ASSISTANT PROFESSOR	<i>[Signature]</i>
A.PAVAN KUMAR	ASSISTANT PROFESSOR	<i>[Signature]</i>
T.PRATHAP	ASSISTANT PROFESSOR	<i>[Signature]</i>

*[Signature]*  
 Signature of the Faculty

Name of the Faculty	Designation	Signature
P.DEVADAS	ASSISTANT PROFESSOR	<i>[Signature]</i>
S.M.V.DURGA RAO	ASSISTANT PROFESSOR	<i>[Signature]</i>
B.NARENDRA	ASSISTANT PROFESSOR	<i>[Signature]</i>
CH.PRADEEP KUMAR	ASSISTANT PROFESSOR	<i>[Signature]</i>
K.SATISH KUMAR	ASSISTANT PROFESSOR	<i>[Signature]</i>
M.JAYASREE	ASSISTANT PROFESSOR	<i>[Signature]</i>
CH.KOWSTHUBHA	ASSISTANT PROFESSOR	<i>[Signature]</i>

*[Signature]*  
 Lab In-charge

*[Signature]*  
 HoD EEE



**DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**  
**LAB ORIENTATION REPORT**

NAME OF THE LAB: POWER ELECTRONICS LAB  
 NAME OF THE FACULTY: B. NARENDRA

A.Y.: 2018-19  
 DEPARTMENT: EEE

CLASS/SEM: III-II  
 DATE: 01/11/2018

\*\*\*\*\*

The following experiments of the POWER ELECTRONICS Lab have been demonstrated in the presence of the list of faculty given below.

S.no	Name of the Experiment	Regular/Add-on	Remarks
1	Study of Characteristics of Thyristor, MOSFET & IGBT.	Regular	✓
2	Design and development of a firing circuit for Thyristor.	Regular	✓
3	Design and development of gate drive circuits for IGBT.	Regular	✓
4	Single -Phase Half controlled converter with R and RL load	Regular	✓
5	Single -Phase fully controlled bridge converter with R and RL loads	Regular	✓
6	Single -Phase AC Voltage Regulator with R and RL Loads	Regular	✓
7	Single -Phase square wave bridge inverter with R and RL Loads	Regular	✓
8	Three- Phase fully controlled converter with RL-load.	Regular	✓
9	Single -phase PWM inverter with sine triangle PWM technique.	Regular	✓
10	3-phase AC-AC voltage regulator with R-load.	Regular	✓
11	Single phase Series Inverter	Add-on	✓
12	Single phase Parallel Inverter	Add-on	✓

Name of the Faculty	Designation	Signature
J. RANGA	ASSOCIATE PROFESSOR	<i>[Signature]</i>
V. SATYANARAYANA	ASSOCIATE PROFESSOR	<i>[Signature]</i>
S.S. SARMA	ASSOCIATE PROFESSOR	<i>[Signature]</i>
B. ASHOK KUMAR	ASSOCIATE PROFESSOR	<i>[Signature]</i>
K.R.K. PANDU	ASSISTANT PROFESSOR	<i>[Signature]</i>
A. PAVAN KUMAR	ASSISTANT PROFESSOR	<i>[Signature]</i>

Name of the Faculty	Designation	Signature
P. DEVADAS	ASSISTANT PROFESSOR	<i>[Signature]</i>
S.M.V. DURGA RAO	ASSISTANT PROFESSOR	<i>[Signature]</i>
J. SURESH	ASSISTANT PROFESSOR	<i>[Signature]</i>
CH. PRADEEP KUMAR	ASSISTANT PROFESSOR	<i>[Signature]</i>
K. SATISH KUMAR	ASSISTANT PROFESSOR	<i>[Signature]</i>
M. JAYASREE	ASSISTANT PROFESSOR	<i>[Signature]</i>

*[Signature]*

*[Signature]*  
 HOD EEE





NAME OF THE LAB: POWER ELECTRONICS LAB  
NAME OF THE FACULTY: B. NARENDRA

A.Y.: 2018-19  
DEPARTMENT: EEE

CLASS/SEM: III-II  
DATE: 01/11/2018



Signature of the Faculty

B. Narendran

MOD, EEE



## DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Date: 16-11-2018  
Eluru.

To  
The Principal,  
Ramachandra College of Engineering,  
Eluru

Respected Sir,

Sub: Submitting report of Orientation Programme of Theory and Lab -CSE Department - Reg...

This is to inform you that the Department of CSE has conducted Orientation Programme for both Lab and Theory. Theory Orientation was conducted from 01-11-2018 to 06-11-2018 and Lab Orientation was conducted from 14-11-2018 to 16-11-2018. The details were attached for your reference.

### Orientation Program Schedule for Theory Subjects for the A.Y 2018-2019

Date/Time	9:30 to 10:30	10:30 to 11:30	11:30 to 12:30	1:30 to 2:30	2:30 to 3:30	3:30 to 4:30
01/11/2018	K.Gopi	M S Radha	G.Prasanth Kumar	P.Sudhakar	P.Naresh	Ch.Hemanand
02/11/2018	K.Venkatesh	B.Prasad Babu	Y.Nagendra Kumar	P.Chakradhar	Dr.A.Daveedu Raju	M.VijayaRaju
03/11/2018	G.Raghu Kumar	S.Nagavali	Dr.B R S Reddy	Dr.V.Suya Narayana	K.Kiran	P.VKishore kumar
05/11/2018	O.Shivabhagawan	A.Chiranjeevi	M.Raghu Chandra	K.Viswa Prasad	Dr.GSR	E.Mounica
06/11/2018	N.Ramakrishna	T.Rajya lakshmi	G.Hari hara kumar	S.Anusha	D.Malika	ASR

### Laboratory Orientation Programme

S No	Name Of The Lab	DATE	Faculty
1	Java Programming Lab	14-11-2018	Mr.K.Gopi
			Mr.K.Venkatesh
			Mr.G.Prasanth Kumar
			Mr.G.Hari Hara Kumar
2	Advance Data Structure Lab	14-11-2018	Ms.M S Radha M M
			Mr.Y.Nagendra Kumar
			Ms.S.Anusha
3	DWDM Lab	15-11-2018	Dr.A.Daveedu Raju
			Dr.B R S Reddy





# RAMACHANDRA COLLEGE OF ENGINEERING

NAAC  
NATIONAL ASSESSMENT AND  
ACCREDITATION COUNCIL



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NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

**B++ Grade**

## DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

			Mr.Ch.Raghu Kumar
			Mr.K.Viswa Prasad
4	NP Lab	15-11-2018	Mr.P.Sudhakar
			Mr.N.Rama Krishna
5	STM Lab	15-11-2018	Dr.G.Srinivasa Rao
			Mr.P.Naresh
			Mr.O shiv Bhagwan
6	Data Structures	16-11-2018	Ms.E.Mounica
7	Computer Programming	16-11-2018	Mr. M. Raghu Chandra
			Mr. P. V. Kishore Kumar

\* S. Nagavali - on-duty today

*S. Nagavali*  
18/11/18

Signature of HOD

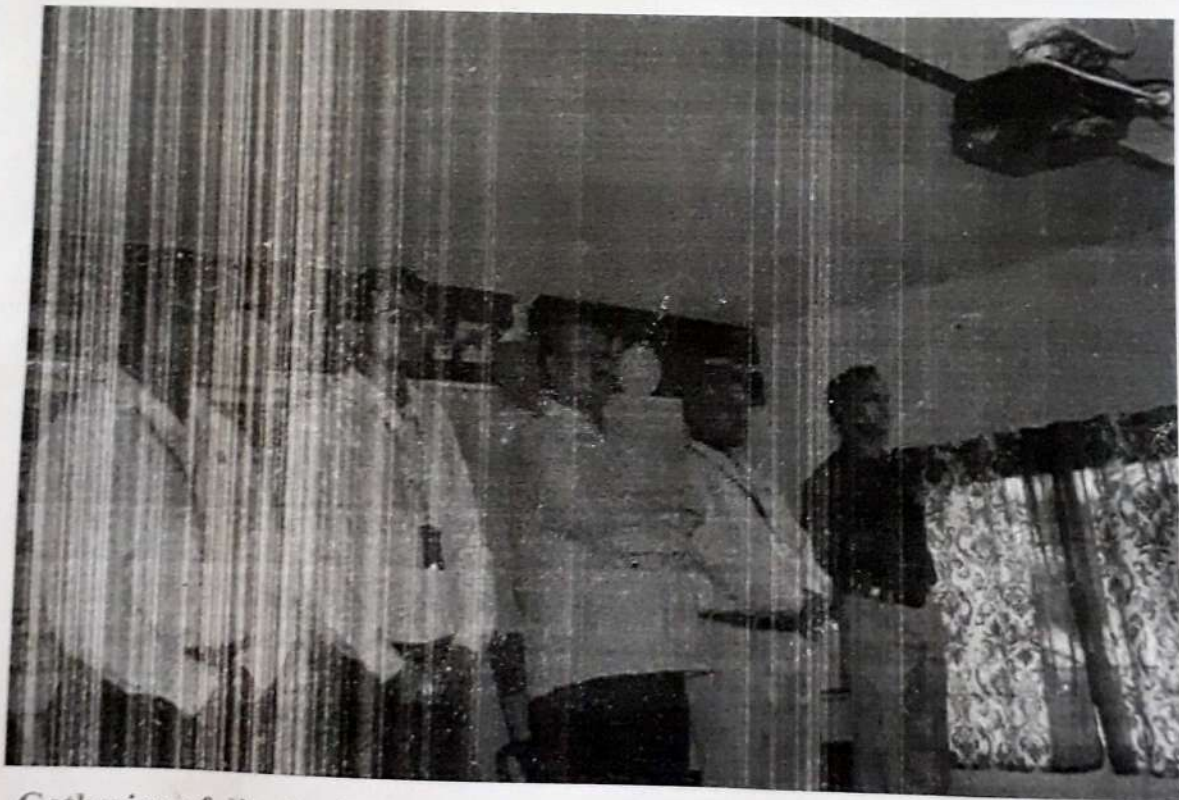
*S. Nagavali*  
18/11/18

*[Handwritten mark]*

## MECHANICAL ENGINEERING DEPARTMENT

A Report on Inauguration of Mechanical Engineering Department Association named "MECHANICAL ENGINEERING GLOBAL ASSOCIATION (MEGA) followed by the SEMINAR on ADVANCED FEA by YUCAN TECHNOLOGIES Bangalore organized by Students of Mechanical Engineering on 27-28 July 2018.

### INAUGURATION:



Gathering of dignitaries onto the dais and prayer by Ms.Akhila, Ms.Anusha.

Mechanical Engineering Association by named MEGA was inaugurated with prayer song by the dignitaries, firstly our beloved Principal Dr. Dola Sanjay S, chief guest Sri G.M. Rajamahindra, Simulation Engineer Yucan Technologies, Bangalore, followed by our beloved secretary Sri K.Venu gopal, Dr.Bh.V Prasad Head of the Mechanical Engineering Department, Convener of the event



Dr.Danaiah Puli (left to Right). The lightning of the lamp by the dignitaries followed by the prayer song.



All the dignitaries gave their speeches on "The importance of Mechanical Engineering Association". They congratulated the Students of Mechanical department for organizing this inauguration of the association. The opening remark was made by Dr.Danaiah Puli, Coordinator of the workshop, and Dr.Bh.V Prasad, HEAD Mechanical Engineering Department. The initial subjective speech was delivered by Sri **G.M. Rajamahindra, Simulation Engineer Yucan Technologies, Bangalore.**

The chief guest and secretary launch the mechanical engineering logo followed by the department logo.



Launching of Association Logo by the chief guest and secretary



Launching of Mechanical Engineering Department Logo





#### **Chief Guest giving the seminar on FEM analysis**

The chief guest giving the seminar on the basics of the FEM and its application with the advanced technologies to the students for enhancing their knowledge in the area for their future growth. He specifically gave the seminar on the mechanical behavior of materials and also analysis of the materials in the FEM analysis. Usage of different materials in different sectors by analyzing the material properties.



### Felicitations to the Chief Guest

Finally in the evening session our beloved principal and HOD felicitated the chief guest and vote of thanks was given by the students Association President.

*[Signature]*  
16.8.18  
Co-Ordinator

(Dr.DANAIAH PULI)

*[Signature]*  
16/8/18  
Convener

(Dr.Bh.VARAPRASAD)

Principal

*[Signature]*  
(Dr.DOLA SANJAY.S)





**RAMACHANDRA**  
**COLLEGE OF ENGINEERING**  
NH-5 Bypass Road, Vatluru (V), Eluru - 534007, W. G. Dist., A.P.  
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NAAC Accredited with B++, ISO 9001 : 2015 Certified

## **DEPARTMENT OF MECHANICAL ENGINEERING**

### **CERTIFICATE OF PARTICIPATION**

This is to certify that Mr/Ms Dr.Md Abid Ali participated in Two days Seminar on ADVANCED FEA by YUCAN TECHONOLIES organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

**Co-ordinator**

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This is to certify that Mr/Ms Mr.M.Pacha Khan participated in Two days Seminar on ADVANCED FEA by YUCAN TECHONOLIES organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms Mr.C.Siva Shankar participated in Two days Seminar on ADVANCED FEA by YUCAN TECHONOLIES organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms **Mr.B.Suresh Babu** participated in Two days Seminar on **ADVANCED FEA** by **YUCAN TECHONOLIES** organized by **RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH**, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms **Mr.B.Raja Kumar** participated in Two days Seminar on **ADVANCED FEA** by **YUCAN TECHONOLIES** organized by **RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH**, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms Mr.M.Ekambaram participated in Two days Seminar on ADVANCED FEA by YUCAN TECHONOLIES organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms Mr.Sk.Abdul Azeez participated in Two days Seminar on ADVANCED FEA by YUCAN TECHONOLIES organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms **Mr.G.V.Phani Babu** participated in Two days Seminar on **ADVANCED FEA** by **YUCAN TECHONOLIES** organized by **RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH**, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms Mrs.Ch.Lakshmi Mrudula participated in Two days Seminar on  
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This is to certify that Mr/Ms **Mr.K Suresh** participated in Two days Seminar on **ADVANCED FEA** by **YUCAN TECHONOLIES** organized by **RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH**, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms Mrs.K.Krishna Veni participated in Two days Seminar on ADVANCED FEA by YUCAN TECHONOLIES organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms Mr.Swarna Sri Krishna participated in Two days Seminar on ADVANCED FEA by YUCAN TECHONOLIES organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms Mr. B Narendra Kumar participated in Two days Seminar on ADVANCED FEA by YUCAN TECHONOLIES organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms **Dr.P.DANAIAH** participated in Two days Seminar on **ADVANCED FEA** by **YUCAN TECHONOLIES** organized by **RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH**, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms **Mr. P RAJESH** participated in Two days Seminar on ADVANCED FEA by **YUCAN TECHONOLIES** organized by **RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH**, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms Mrs.B.NALINI KUMARI participated in Two days Seminar on ADVANCED FEA by YUCAN TECHNOLOGIES organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

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This is to certify that Mr/Ms Mrs. O PAVITHRA participated in Two days Seminar on ADVANCED FEA by YUCAN TECHONOLIES organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 27<sup>th</sup>-28<sup>th</sup> July 2018.

**Co-ordinator**

**HOD**

**PINCIPAL**







## Department of Electronics and Communication Engineering

Format:9014/0

### Workshop Report

**Organized by** : Ramachandra College of Engineering  
**Name of the Speaker** : Mr. Dhanaraj. D  
**Designation** : Field Application Engineer  
**Topic** : Workshop On Mentor Graphics  
**Venue** : VLSI lab, B-Block  
**Date & Time** : 4<sup>th</sup> July to 7<sup>th</sup> July 2018

Branch	Year	Semester	No of Students Attended
ECE	IV	I	150
Total No of Students Attended			150

### Profile of the Speaker

The Name of the Speaker is **Dhanaraj.D**, He is working in Trident Techlabs Pvt Ltd company.  
 Qualification: MTECH -VLSI & Embedded Systems (Visveswaraya Technological University-Karnataka)  
 Experience: 2 years of experience in FPGA & ASIC Design Domain.  
 Have conducted training using Mentor Graphics Tools in various Defence agencies like DRDL and RCI.  
 Experienced in Layout and PCB design tools.

### Report

#### 1. Report in brief by Organizer / Coordinator / Convener:

The main objective of the workshop is to make the students have basic knowledge on Mentor Graphics. Five day work shop is organized for the Departments of ECE -06-2018 on Fundamental Concepts of VLSI circuits. A total of students have attended the workshop.

The workshop is divided into 5-sessions; the first session and second session is totally concentrated in making the students get strong at basics related to the implement the circuit design and how to execute.

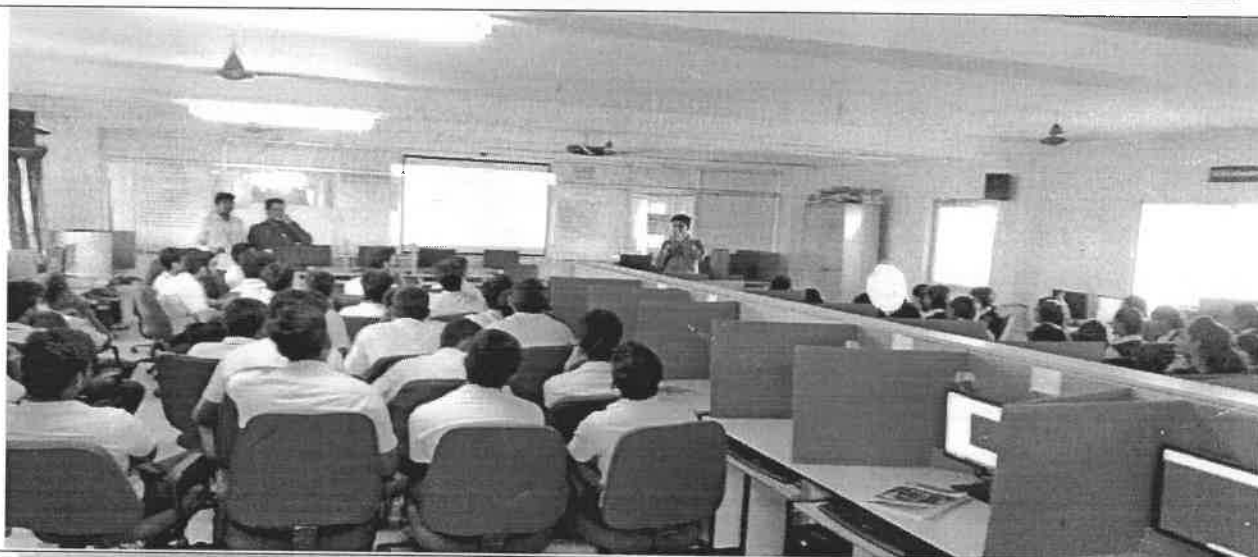
The third session is totally concentrated on layout design and checks the simulation results. The fourth session is discussed about updation of VLSI technologies and Career opportunities on this technology





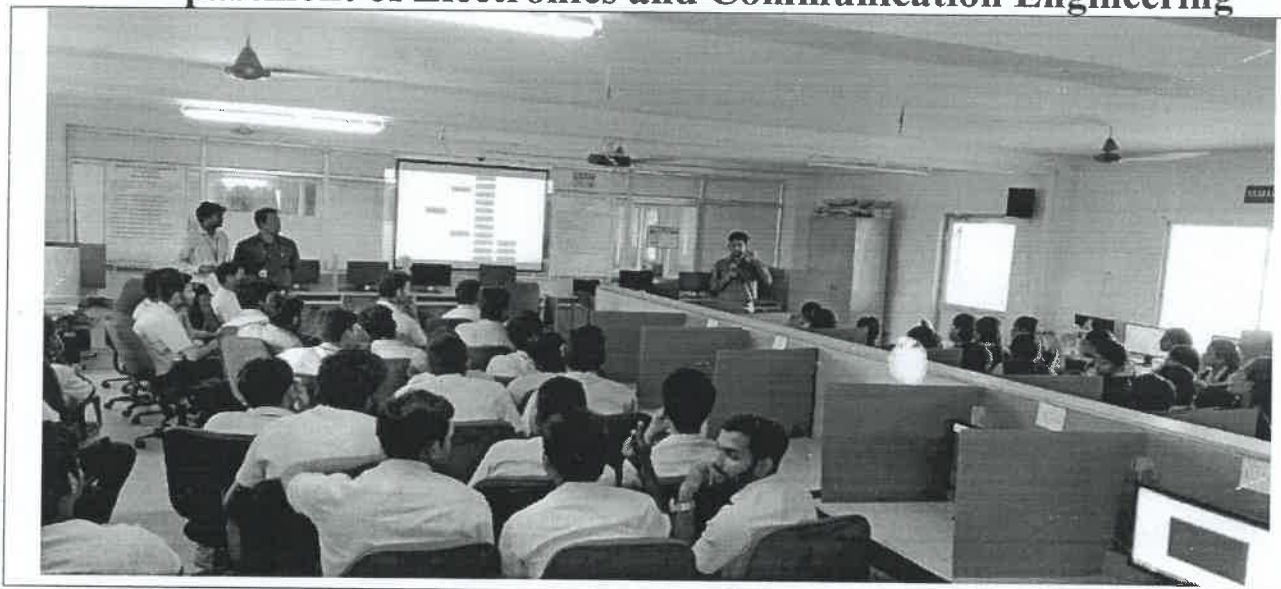
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### 2. Photos:





## Department of Electronics and Communication Engineering



**3. Feedback from students:** Students' have got good knowledge in VLSI backend tool of Mentor Graphics. Interaction of the mentor is quite good with the students.

**4. Remarks from Resource Person:** Very good opportunities are available in the field of VLSI. Students' are good and pay complete attention on my lecture.

*D. Sridhar*  
Co-Ordinator

*[Signature]*  
HoD  
10/7

*[Signature]*  
Principal







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**B++ Grade**

## Department of Electronics and Communication Engineering

The following is the List of Faculty attended for Five day national workshop on HEP-1

for A.Y 2018-19.

S.No	Name of the Faculty	Designation	Signature
1	Dr. S. Jagan Mohan Rao	Professor & HoD	
2	Dr. Dola Sanjay S.	Professor	
3	Dr. A. Ranganayakulu	Professor	
4	Dr. B.S.Satish	Professor	
5	Dr. P. Essakimuthu (P.G)	Professor	
6	Dr. L.Bharathi	Professor	
7	Dr. K.SangeethaPriya	Associate Professor.	
8	Mr. J. Prasanth Kumar	Associate Professor	
9	Mr. D. Sridhar	Associate Professor	
10	Mr. R.Radha Krishna	Associate Professor	
11	Mr. Are Rajesh Naidu	Associate Professor	
12	Mr. M. Ravi	Associate Professor	
13	Mr. D. VenkannaBabu	Associate Professor	
14	Mr. D. Rambabu	Associate Professor	
15	Mr. R. Durga Prasad	Associate Professor	
16	Mr. Manda Rama Krishna	Associate Professor	
17	Ms. Y. Lavanya	Associate Professor	
18	Mr. R. L.R LokeshBabu	Assistant Professor	
19	Mr. NVDP Murthy	Assistant Professor	
20	Mr. RelaRaghuram	Assistant Professor	

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Dept. of Electronics and Communication Engineering has undergone Five-Day  
National Workshop on HEP-1 conducted during \_\_\_\_\_ 04-7-18 to 08-07-18 at the  
Department of Electronics and Communication Engineering, RCE, Eluru.

  
**Mr. D. Dhanaraj**  
Application Engineer  
Trident Techlabs Pvt. Ltd

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**Dr. Dola Sanjay S.**  
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Dept. of Electronics and Communication Engineering has undergone Five-Day  
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This is to certify that Dr. A. Ranganayakulu working as an Professor in the  
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National Workshop on HEP-1 conducted during 04-7-18 to 08-07-18 at the  
Department of Electronics and Communication Engineering, RCE, Eluru.

  
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This is to certify that Dr. B.S.Satish working as an Professor in the  
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This is to certify that Dr. P. Essakimuthu (P.G) working as an \_\_\_\_\_ Professor, \_\_\_\_\_ in the  
Dept. of Electronics and Communication Engineering has undergone Five-Day  
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This is to certify that Dr. L. Bharathi working as an Professor in the  
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This is to certify that Dr. K.SangeethaPriya working as an Associate Professor. in the  
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This is to certify that Mr. D. Sridhar working as an Associate Professor in the  
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This is to certify that Mr. R.Radha Krishna working as an Associate Professor in the  
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## Department of Electronics and Communication Engineering

### Workshop Report

**Organized by** : Dr. Dola Sanjay S M.Tech , Ph.D, Principal  
Ramachandra College of Engineering

**Name of the Speaker** : Mr. Ch.Murali Krishna M.Tech,

**Designation** : Assistant Professor , Dept. of ECE,  
Ramachandra College of Engineering.

**Topic** : Microstrip antenna design using HFSS

**Venue** : VLSI LAB

**Date & Time** : 29-09-2018, 1:10PM TO 2:50PM

Branch	Year	Semester	No of Students Attended
ECE-B	III	I	31
Total No of Students Attended			31

#### Profile of the Speaker

Mr.Ch.Murali Krishna is an Assistant Professor in Ramachandra college of Engineering, department of ECE , has publications in five international conferences , five international journals in area of Microstrip patch antennas design using HFSS on several applications like ultra wide band, multi band using slot antennas , fractal antennas , PIFA's and FSS techniques. At present working on Reconfigurable antennas along with optimization techniques.

**Qualification:** M.Tech in Communication Systems and B.Tech in Electronics & Communication Engg.

#### Report

##### **1. Report in brief by Organizer / Coordinator / Convener:**

The main objective of the workshop is to make the students have basic knowledge on HFSS for designing antennas. Two hours Hands-on on HFSS is organized for the Departments of III-ECE-B on 29-09-2018 on Fundamental Concepts of antennas. A total no. of 31 students has attended the workshop.

The workshop is divided into 2-sessions; the first session is totally concentrated in making the students get strong at basics related to the Antenna Fundamentals

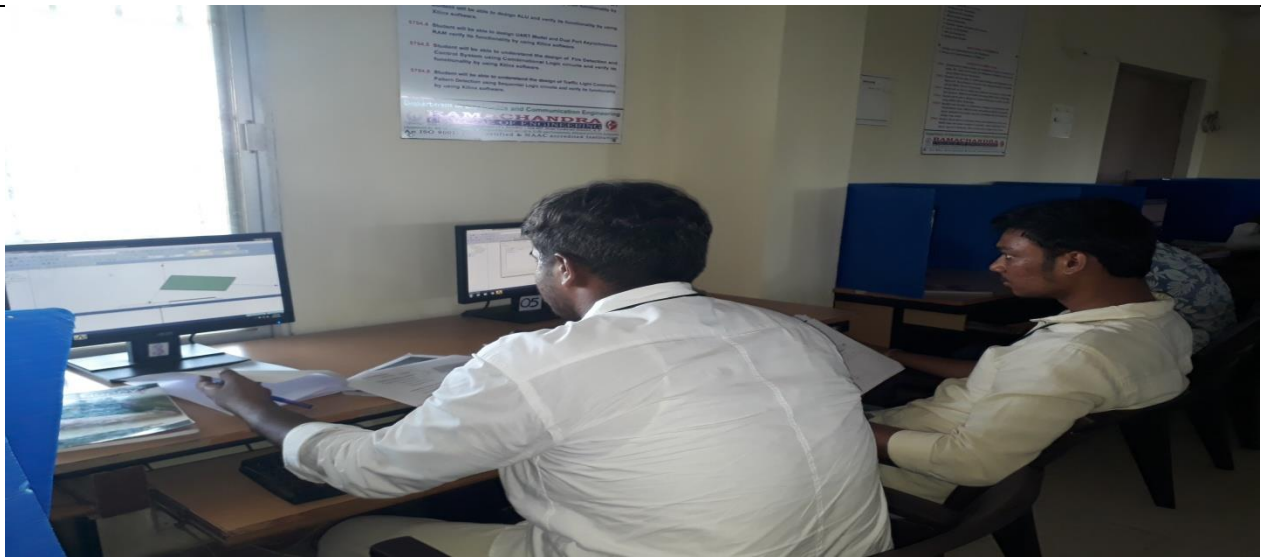
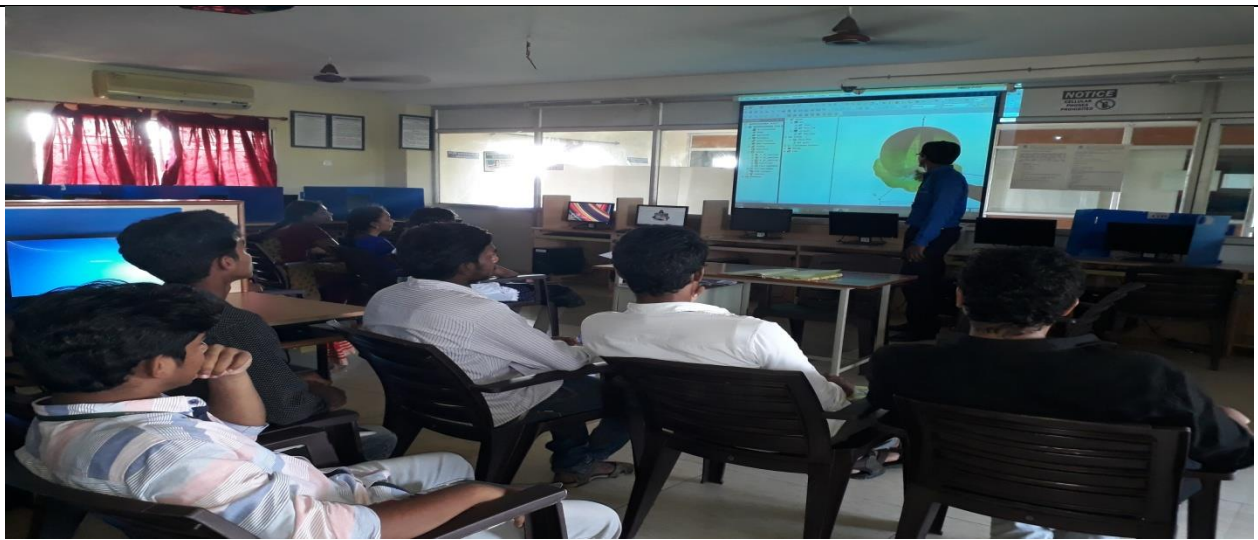
The second session is totally concentrated on, how to design a microstrip antenna.





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### 2. Photos:





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### 3. Feedback from students:

Students' have got good knowledge in introductory part of HFSS. Interaction of the HFSS is

### 4. Remarks from Resource Person:

Very good opportunities are available in the field of HFSS. Student's are good and pay

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**B++ Grade**

## Department of Electronics and Communication Engineering

The following is the List of Faculty attended for FDP ON HFSS for A.Y 2018-19.

S.No	Name of the Faculty	Designation	Signature
1	Mr. Nandam Venu	Assistant Professor	
2	Ms. Ch. Naga Lakshmi	Assistant Professor	
3	Ms. P. Bhagya Sri	Assistant Professor	
4	Mr. M. Ravi Kiran	Assistant Professor	
5	Mr. P. Satish Babu	Assistant Professor	
6	Mr. G. Rajesh Babu	Assistant Professor	
7	Mr. Ch. Jojibabu	Assistant Professor	
8	Mr. M. Venkata Amarnath	Assistant Professor	
9	Mr. Md. Abdul Aziz	Assistant Professor	
10	Ms. Ch. Siva Parvathi	Assistant Professor	
11	Ms. V. Sushma	Assistant Professor	
12	Ms. A. Pavani	Assistant Professor	
13	Ms. R. Naga Sarala	Assistant Professor	
14	Mr. Ch. Murali Krishna	Assistant Professor	
15	Mr. D. Ashok Kumar (P.G)	Assistant Professor	
16	Mr. K. Raveendra (P.G)	Assistant Professor	
17	Mr. G. Ramana Reddy	Assistant Professor	
18	Ms. B. Anitha Pravalii	Assistant Professor	
19	Mr. Ch. Praveen Kumar (A)	Assistant Professor	
20	Mr. D. Shavalaiah (A)	Assistant Professor	
21	Mr. T. Mallikarjuna (A)	Assistant Professor	

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Assistant Professor, Dept. of ECE. Organised by the Dept. of ECE, RCE, ELURU.*

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Co-ordinator

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Assistant Professor, Dept. of ECE. Organised by the Dept. of ECE, RCE, ELURU.*

Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

Dr. Jagan Mohan Rao S.  
HoD-ECE

Dr. Dola Sanjay S.  
Principal





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Format:9014/0

### Workshop Report

**Organized by** : Ramachandra College of Engineering  
**Name of the Speaker** : Mr. J.Prem Kumar  
**Designation** : Product Manager- Mathworks, Education Solutions,  
 Capricot Technologies Pvt. Ltd  
**Topic** : Programming on MATLAB  
**Venue** : DSP Lab  
**Date & Time** : 19<sup>th</sup> July 2018 to 23 July 2018

Branch	Year	Semester	No of Students Attended
ECE	IV	I	150
Total No of Students Attended			150

### Profile of the Speaker

J.Prem Kumar is a Product Manager for MathWorks Products in Capricot Technologies Private Limited and specializes in the field Dynamic System Modelling, Embedded Systems, Robotics, Image Processing and Speech Processing. He has around 6 years of experience in Capricot Technologies Pvt. Ltd. Prior to joining Capricot Technologies Private Limited, Prem Kumar has experience in Semiconductor Manufacturing Industry. Prem Kumar holds a Master Degree in VLSI System Design.

**Qualification** : M.Tech in VLSI System Design and B.E in Electronics & Communication Engg.

**Experience** : Around 6 years of experience in Capricot Technologies Pvt. Ltd.

### Report

#### 1. Report in brief by Organizer / Coordinator / Convener:

The main objective of the workshop is to make the students have basic knowledge on MATLAB Programming language. Five day work shop is organized for the Departments of ECE on 19<sup>th</sup> July 2018 to 23 July 2018 on Fundamental Concepts of Programming. A total no of 150 students have attended the workshop.

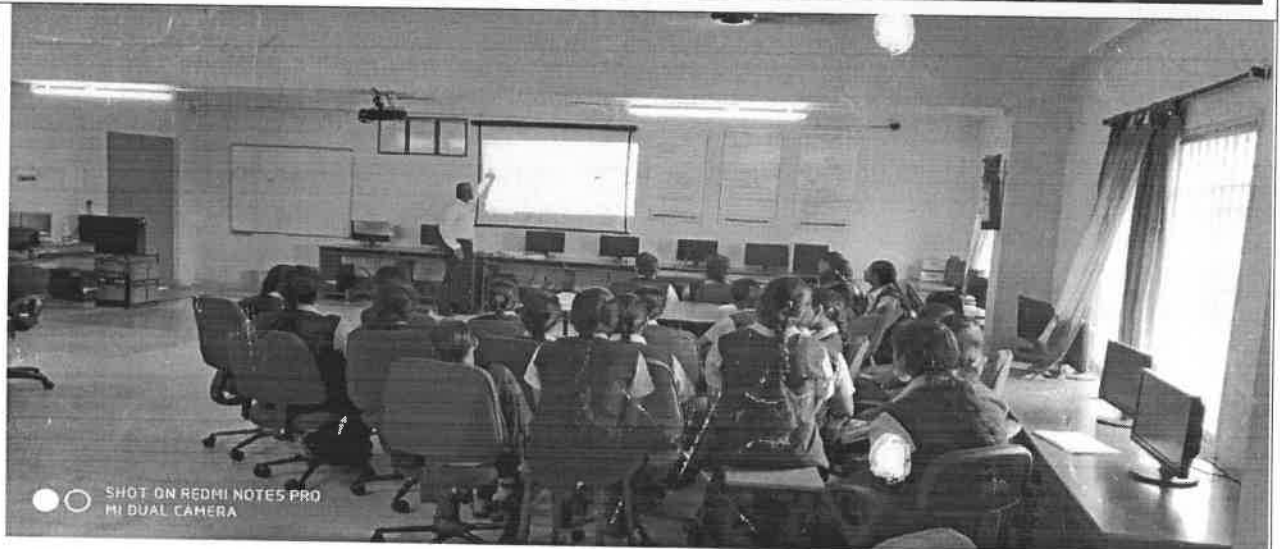
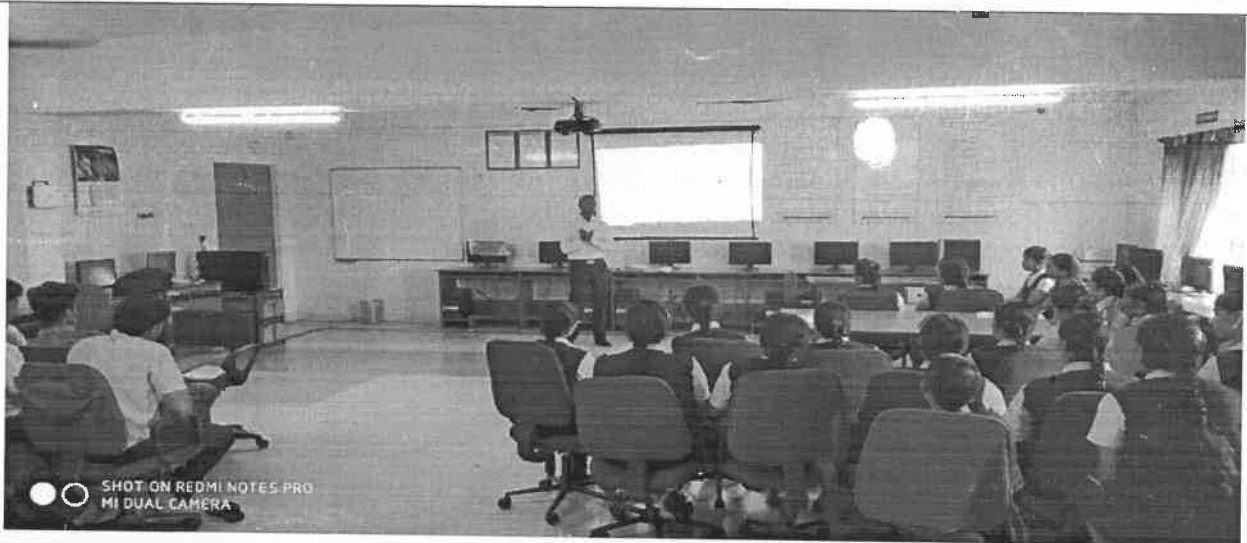
The workshop is divided into 2-sessions; the first session is totally concentrated in making the students get strong at basics related to the Arrays, Data types, Header files,& Fundamentals about Structures along with practical applications.

The second session is totally concentrated on Program implementation, writing a code & dumping it on Micro-Controllers using MATLAB.



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### 2. Photos:





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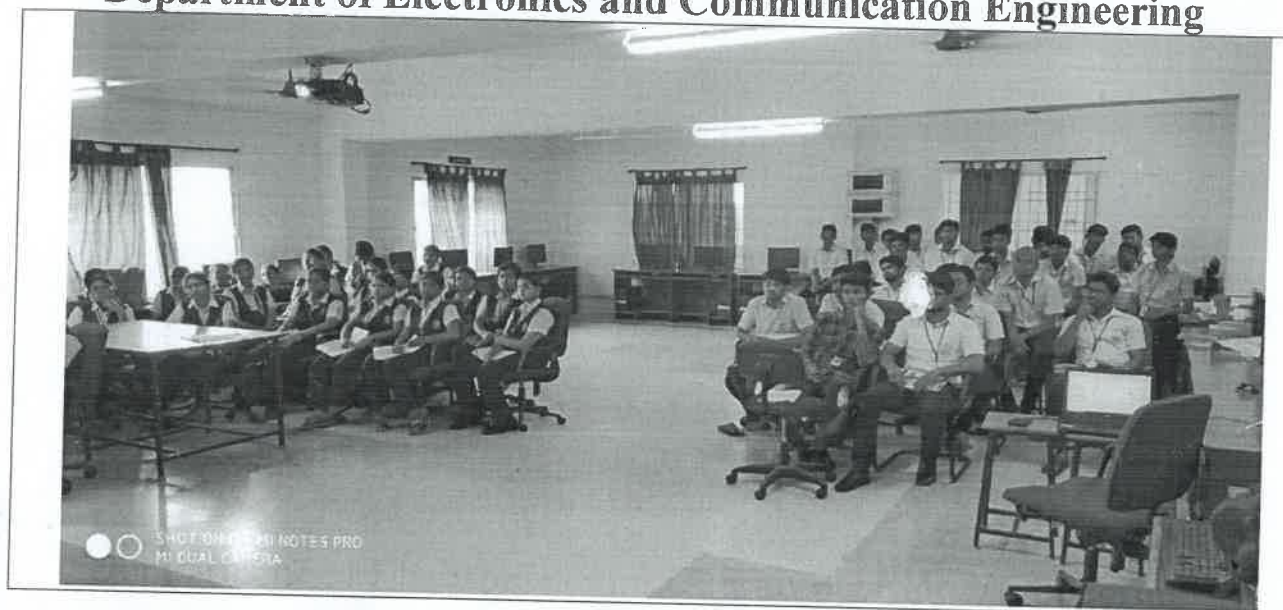
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**3. Feedback from students:** Students' have got good knowledge in introductory part of MATLAB. Interaction of the MATLAB is quite good with the students.

**4. Remarks from Resource Person:** Very good opportunities are available in the field of MATLAB. Students' are good and pay complete attention on my lecture.

  
Co-Ordinator

  
HoD

  
Principal







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The following is the List of Faculty attended for Workshop on MATLAB for A.Y 2018-19.

S.No	Name of the Faculty	Designation	Signature
1	Dr. S. Jagan Mohan Rao	Professor & HoD	
2	Dr. Dola Sanjay S.	Professor	
3	Mr. R. Radha Krishna	Associate Professor	
4	Mr. Are Rajesh Naidu	Associate Professor	
5	Mr. M. Ravi	Associate Professor	
6	Mr. D. Venkanna Babu	Associate Professor	
7	Mr. D. Rambabu	Associate Professor	
8	Mr. R. Durga Prasad	Associate Professor	
9	Mr. Manda Rama Krishna	Associate Professor	
10	Ms. Y. Lavanya	Associate Professor	
11	Mr. R. L.R Lokesh Babu	Assistant Professor	
12	Mr. NVDP Murthy	Assistant Professor	
13	Mr. Rela Raghuram	Assistant Professor	
14	Mr. Nandam Venu	Assistant Professor	
15	Ms. Ch. Naga Lakshmi	Assistant Professor	
16	Ms. P. Bhagya Sri	Assistant Professor	
17	Mr. M. Ravi Kiran	Assistant Professor	
18	Mr. P. Satish Babu	Assistant Professor	
19	Mr. G. Rajesh Babu	Assistant Professor	
20	Mr. Ch. Jojibabu	Assistant Professor	
21	Mr. M. Venkata Amarnath	Assistant Professor	
22	Mr. Md. Abdul Aziz	Assistant Professor	
23	Ms. Ch. Siva Parvathi	Assistant Professor	

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This is to certify that A. Ranganayakulu working as an Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that B.S.Satish working as an Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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**Dr. Dola Sanjay S.**  
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## *Certificate of Participation*

This is to certify that RelaRaghuram working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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## *Certificate of Participation*

This is to certify that Nandam Venu working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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## *Certificate of Participation*

This is to certify that Ch. Naga Lakshmi working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that P. Bhagya Sri working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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## *Certificate of Participation*

This is to certify that M. Ravi Kiran working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that P.SatishBabu working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that G. Rajesh Babu working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that M.VenkataAmarnath working as an Assistant Professor in the Dept.  
of Electronics and Communication Engineering has undergone Two-Day National  
Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department  
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This is to certify that Md.Abdul Aziz working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that Ch.SivaParvathi working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that V.Sushma working as Assistant Professor in the Dept.  
of Electronics and Communication Engineering has undergone Two-Day National  
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This is to certify that A.Pavani working as Assistant Professor in the Dept.  
of Electronics and Communication Engineering has undergone Two-Day National  
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This is to certify that R.NagaSarala working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that Ch.Murali Krishna working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that D.Ashok Kumar working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that K. Raveendra working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that G. Ramana Reddy working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that B.AnithaPravalli working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that Ch. Praveen Kumar working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that Ch.Praveen Kumar working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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This is to certify that A.VenkataSivaram working as an Assistant Professor in the Dept. of Electronics and Communication Engineering has undergone Two-Day National Workshop on MATLAB conducted during 20th and 21st July 2018 at the Department of Electronics and Communication Engineering, RCE, Eluru.

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ORIENTATION CLASSES (2018 - 19) Odd sem

Date: 07-05-2018

S.NO	DATE	NAME OF THE FACULTY	TOPICS	SIGNATURE
1.	7.05.2018	Dr.DOLA SANJAY S	Orientation on PCB	[Signature]
	8.05.2018		board presentation	
2.	7.05.2018	ANITHA PRAVALLI BUDIPUTI	Type of antennas	[Signature]
		Dr.SARIDE JAGAN MOHAN RAO	Introduction to Optical Communication	
		VENKANNA BABU DUKKIPATI	VLSI Design flow	
		MURALI KRISHNA CHINTAPANTI		
		DURGA PRASAD RAJULAPATI	Imaging Techniques.	
3.	8.05.2018	SUSHMA VANKAYALAPATI		[Signature]
		PAVANI APPALAREDDYGARI	Introduction to Linear ICs	
		SRIDHAR DONE		
		RAVIKIRAN MUSINADA	Digital Modulation Techniques	[Signature]
4.	9.05.2018	SRIDHAR DONE	System Design - through hole	
		RAVIKIRAN MUSINADA	A/D conversion	[Signature]
		CHINNALA JOJI BABU	Introduction to DLP	
		Dr.SANGEETHA PRIYA N		[Signature]
		VENKANNA BABU DUKKIPATI	Introduction to VHDL	
		AZIZ ABDUL MOHAMMED	Prob. distribution function	[Signature]
5.	10.05.2018	Dr.SARIDE JAGAN MOHAN RAO	Introduction to Electronics	
		Dr.SATHISH B S	Research Methodologies	[Signature]

		Dr. BHAGSHEEL AZIZ ALI MOHAMMED CHINNALA JOJI BABU ANITHA PRAVALLI BUDIPUTI RAVANI APPALARREDDYGARI	Prob. characteristics of the properties Microstrip patch antenna	1000
6.	11.05.2018	PAVULURI BHAGYA SRI Dr. SATHISH B S Dr. SANGEETHA PRIYA N PRASANTHA KUMAR JUJARAPU	Introduction to PLC Types of Antennas Combinational Circuits	1000
7.	12.05.2018	PAVULURI BHAGYA SRI Dr. BHARATHI L		1000
8.	13.05.2018	DURGA PRASAD RAJULAPATI MURALI KRISHNA CHINTAPANTI PRASANTHA KUMAR JUJARAPU LAVANYA	Introduction to Image processing Introduction to Antennas & Waves Introduction to UWB	1000

  
Academic Coordinator

HOD

	Dr. BHARATHI L		
	AZIZ ALI MOHAMMED	Prob. antenna direction & its properties	also
	CHINNALA JOJI BABU		
	ANITHA PRAVALU BUDIPUTI	microstrip patch antenna	see
6.	11.05.2018	Introduction to MS	see
	PAVULURI BHAGYA SRI		
	Dr. SATHISH B S		
	Dr. SANGEETHA PRIYA N		
	PRASANTHA KUMAR		
	JUJARAPU	Types of Antennas	see
7.	12.05.2018	Conventional Cells	see
	PAVULURI BHAGYA SRI		
	Dr. BHARATHI L		
8.	13.05.2018	Introduction to Image processing	see
	DURGA PRASAD		
	RAJULAPATI		
	MURATI KRISHNA		
	GHANTAPANI		
	PRASANTHA KUMAR	Introduction to Antennas & Waves	see
	JUJARAPU	properties	see
	LAVANYA	Introduction to LCA	see

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Date: 5-11-2018

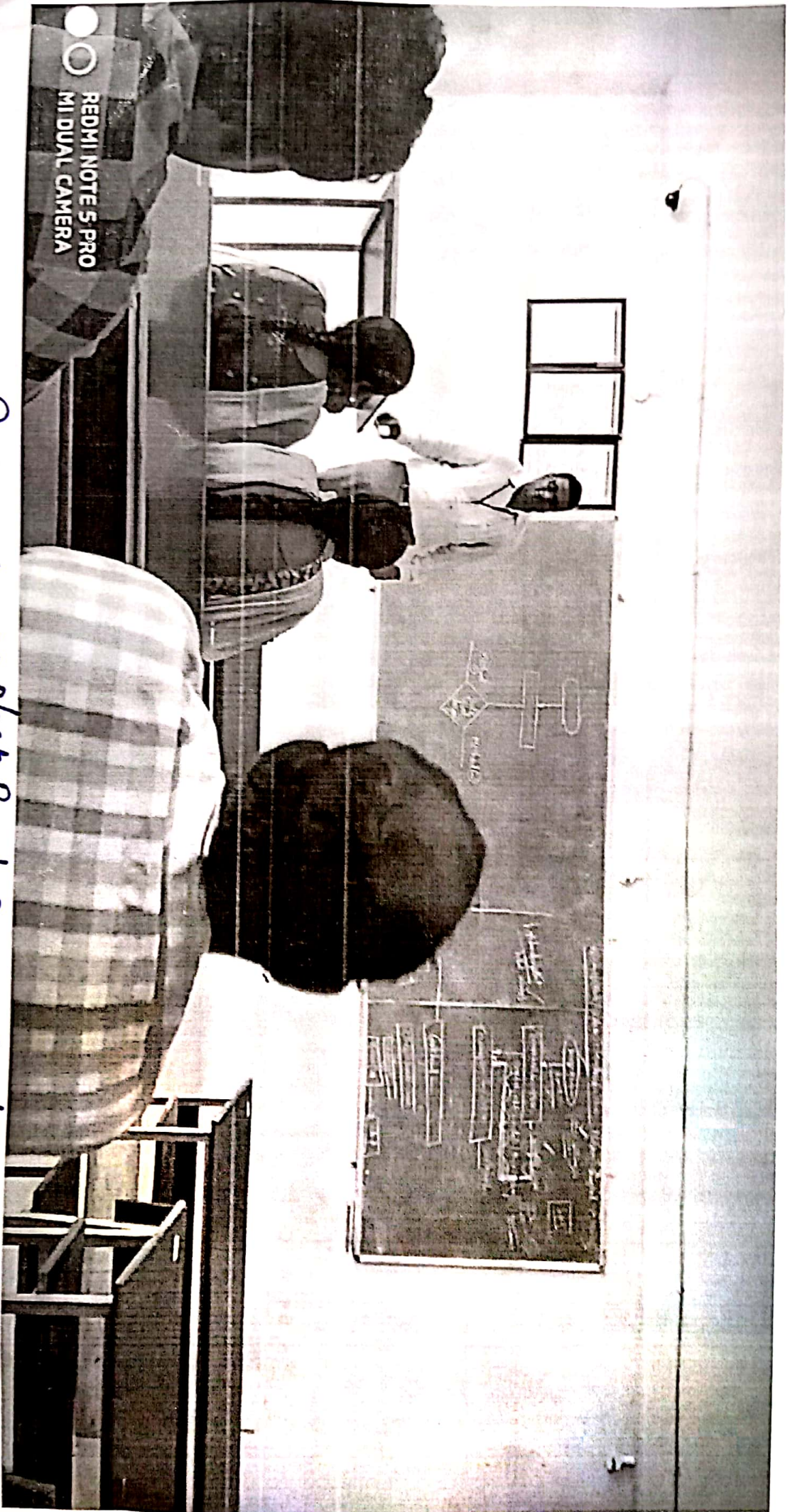
S.NO	DATE	NAME OF THE FACULTY	TOPICS	SIGNATURE
1.	5.11.2018	Dr.DOLA SANJAY S	Orientation on block board	[Signature]
	6.11.2018		Prescribed by [Signature]	
2.	5.11.2018	ANITHA PRAVALLI BUDIPUTRI	Introduction to Rucaves	[Signature]
		Dr.SARIDE JAGAN MOHAN RAO	Introduction to EMI	
		VENKANNA BABU DUKKIPATI	logic circuits	
		MURALI KRISHNA CHINTAPANTI	Linear constant Coefficient difference equation.	
		DURGA PRASAD RAULAPATI	Sampling Techniques	
		SUSHMA VANKAYALAPATI	AC bridges	
3.	6.11.2018	PAVANI APPALARREDDYGARI	Open and Closed loop systems	[Signature]
		SRIDHAR DONE		
		RAVIKIRAN MUSINADA	New trends in satellite comm.	
4.	7.11.2018	SRIDHAR DONE		[Signature]
		RAVIKIRAN MUSINADA	IDM,	
		CHINNALA JOJI BABU		
		Dr.SANGEETHA PRIYA N	Introduction to digital system	
		VENKANNA BABU DUKKIPATI	clippers & clamping	
5.	8.11.2018	AZIZ ABDUL MOHAMMED	Architecture of 8086 µP	[Signature]
		Dr.SARIDE JAGAN MOHAN RAO	Applications of 8086 µP	
		Dr.SATHISH B S		

		Dr. BHARATH L AZIZ AB. MOHAMMED CHINNALA JOJI BABU ANITHA PRAVALLI BUDIPUTI PAVANI APPALAREDDYGARI PAVULURI BHAGYA SRI	Small signal Amplifiers Re low pass & high pass filters Carm diode Mosfet gain formula - reduction technique	Presented 03/10
6.	9.11.2018	Dr. SATHISH B S Dr. SANGEETHA PRIYA N PRASANTHA KUMAR JUJARAPU	Dr. T. algorithma Loham's low Denek	Presented 03/10
7.	10.11.2018	PAVULURI BHAGYA SRI Dr. BHARATH L DURGA PRASAD RAULAPATI	Feedback Amplifier. Oscillator on 6M waves	Presented 03/10
8.	13.11.2018	MURALI KRISHNA CHINTAPANTI PRASANTHA KUMAR JUJARAPU LAVANYA	FM, TEM in LCD Roham's DSP	Presented 03/10

Academic Coordinator

A. R. Raghavi  
HOD



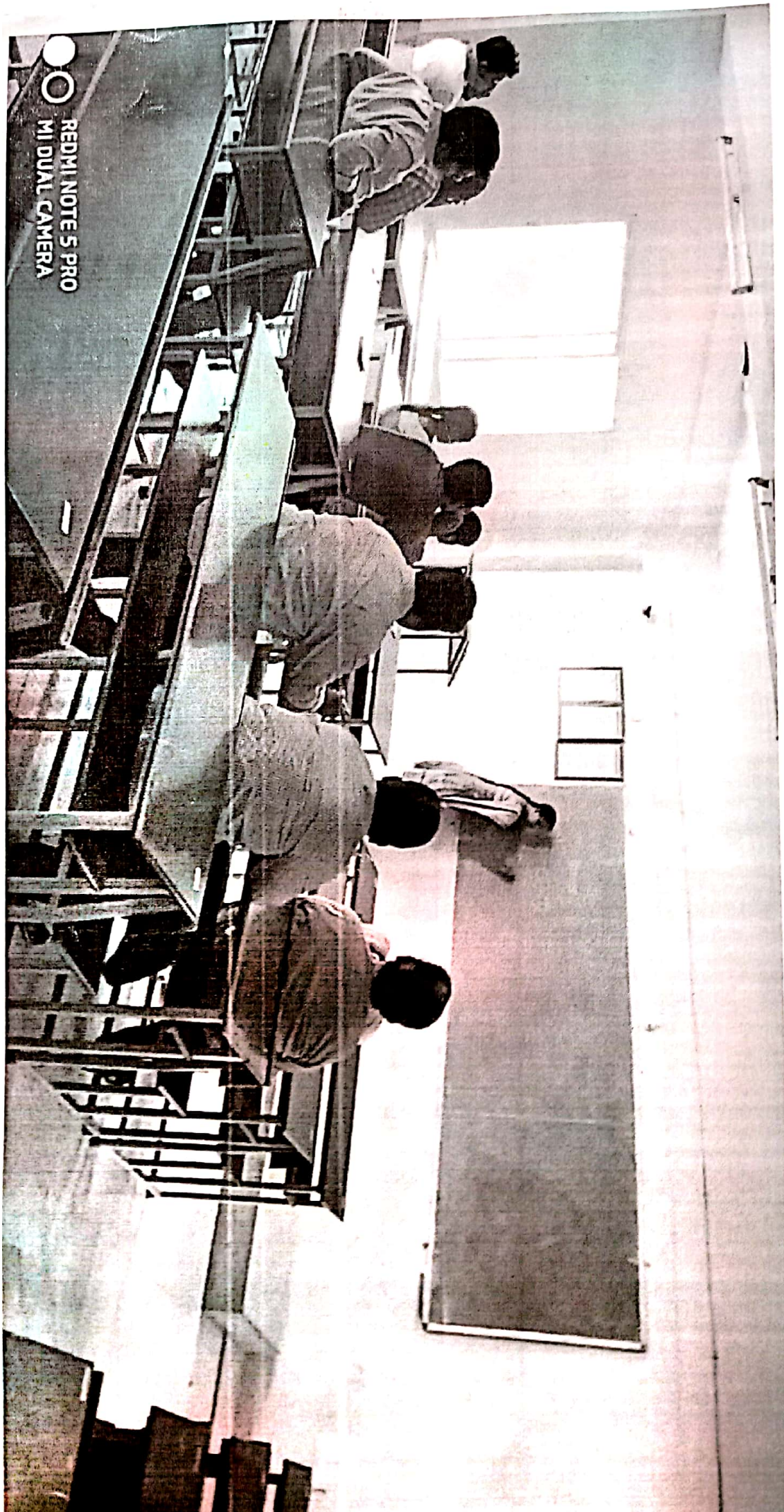


*Orientation on Block Board Presentation by Dr. Dola Sanjay S*

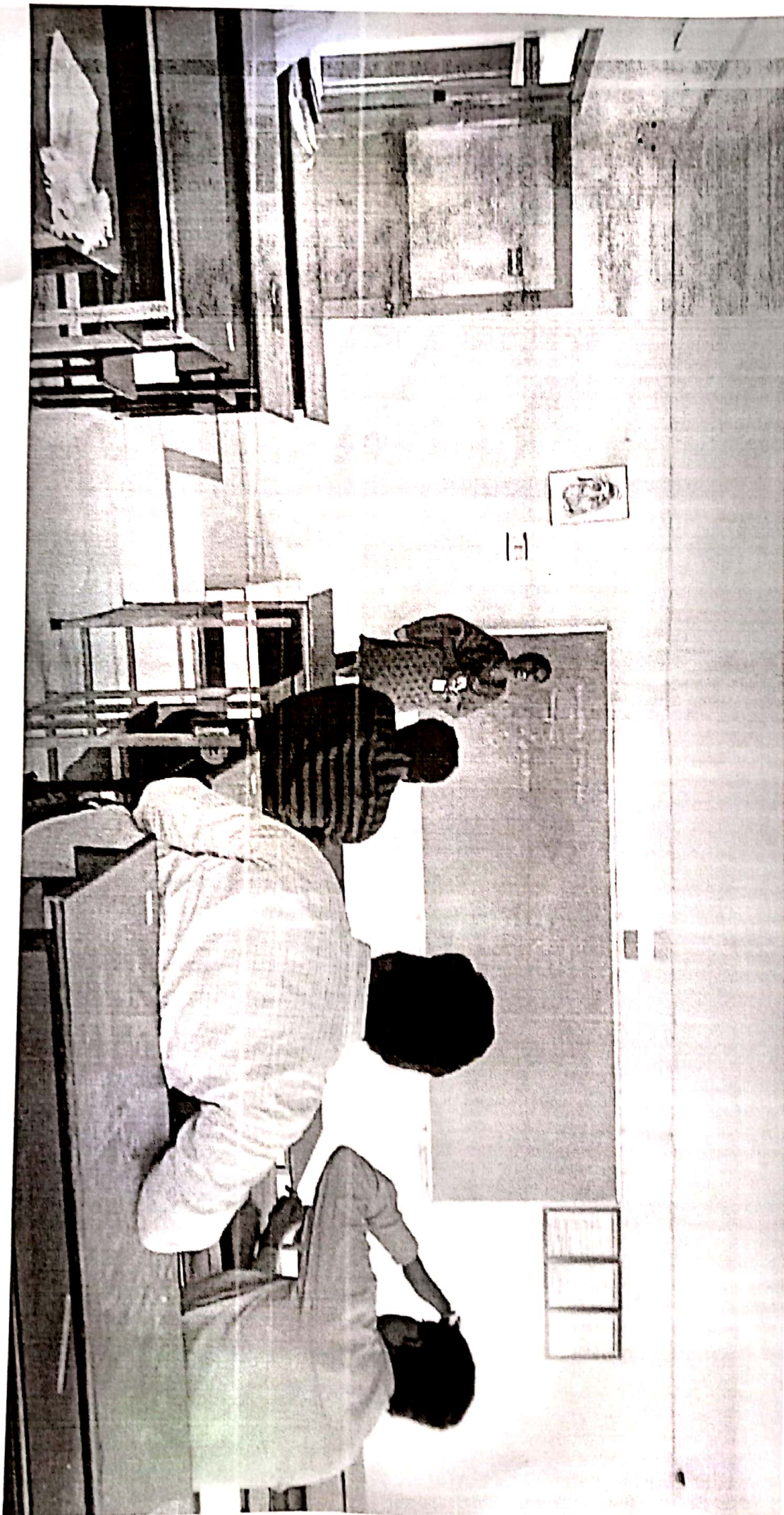












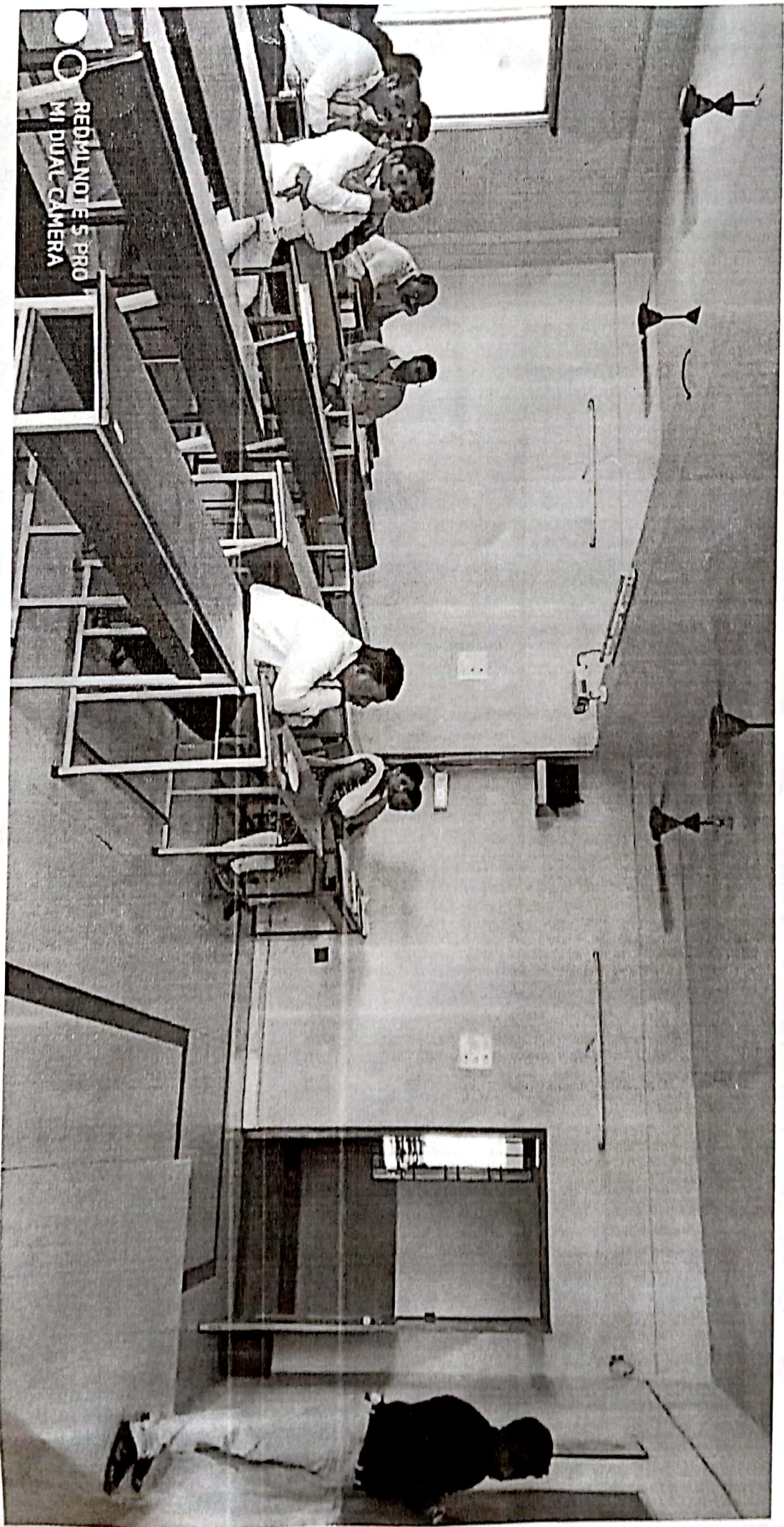












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# RAMACHANDRA COLLEGE OF ENGINEERING

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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### HANDS ON EXPERIENCE FOR FACULTY IN LABORATORIES

A. Y. 2018-19 (Even Semester)

Date: 14-11-18

Name of the Laboratory: pulse Amplitude modulation & Demodulation,  
Year II

Name of the Faculty: Ch. Swaparathi  
Designation Asst. prof

Name of Experiment: pulse amplitude modulation & Demodulation

Importance of Experiment:

Apparatus Required:

1. pulse amplitude modulation trainer kit
2. Dual trace oscilloscope.

Experimental Procedure/Tabular forms:  
(If required, Please attach separate Sheet)

1. As the Circuitry is already wired you. Just have to trace the circuit according to the ckt diagram given above.
2. connect trainer to mains & switch on the power
3. measure the output voltage of regulated & pulse generator power supply circuit i.e.  $\pm 12V$
4. observe the o/p of AF generator & pulse generator using C.R.O & note that Af signal is approximately 3Vp-p of 400 Hz frequency & pulse generator output is pulse train of 10Vp-p with frequency b/w 1 kHz & 6 kHz.

Inference /Outcome:

Correlation of experimental outcome with theoretical concept:

Practical Application:

Can you design new experiment with this setup?

Is the experimental set up in working condition:

Ch. Swapathi  
Signature of Faculty Member

2. R. Nagarajula
3. [Signature]
4. [Signature]





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## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

### HANDS ON EXPERIENCE FOR FACULTY IN LABORATORIES

A. Y. 2018-19 (Even Semester)

Date: 14-11-18.

Name of the Laboratory: Analog Communication

Year II

Name of the Faculty: ch. Swaparnathi

Designation Asst. prof

Name of Experiment: spectrum analysis using spectrum Analyzer.

Importance of Experiment:

Apparatus Required:

1. spectrum analyzer - 1
2. function generator - 2
3. BNC-BNC probes - 2

Experimental Procedure/Tabular forms:

(if required, Please attach separate Sheet)

1. connect the output of function generator - 1 to FM input of function generator - 2 through BNC-BNC cable which is acting as message input. keep its frequency FM to 200 kHz. sine wave and amplitude approximately 140 mV. (set the amplitude control in minimum position and press 20dB attenuator switch.
2. connect the output of the generator - 2 (i.e FM signal) to the i/p spectrum analyzer through BNC-BNC cable. keep its frequency as maximum as possible (1 MHz), which is acting as carrier signal f<sub>c</sub>. sine wave & set the amplitude approximately 1.4 V.

Inference /Outcome:

Correlation of experimental outcome with theoretical concept:

Practical Application:

Can you design new experiment with this setup?

Is the experimental set up in working condition:

1. ch. S. p. k.  
Signature of Faculty Member

2. R. Nagarajula,

3. [Signature]  
4. [Signature]



# KAMACHANDRA COLLEGE OF ENGINEERING

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Department of Electronics and Communication Engineering

## LAB ORIENTATION REPORT

Academic year: 2018-19

Sem : even

Dept: ECE

S.#	Name of the Lab	Year - sem	Lab I/C Signature
1	ANALOG COMMUNICATION LAB(R-16)	II-ii	
2	ELECTRONIC CIRCUIT ANALYSIS LAB(R-16)	II-ii	
3	ELECTRONIC DEVICES AND CIRCUITS LAB(R-16)	II-ii	
4	DIGITAL COMMUNICATION LAB (R-16)	III-ii	
5	MICRO PROCESSORS AND MICRO CONTROLLERS LAB(R-16)	III-ii	
6	VLSI LAB (R-16)	III-ii	

Academic Coordinator

HOD

Dean-Academics

Principal





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**B++ Grade**

## DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

DATE: 01-11-2017  
ELURU.

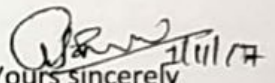
To,  
The Principal,  
Ramachandra College of Engineering,  
Vatluru, Eluru.

Dear Sir,

Sub: Request for allocation of funds for Faculty Development Program- reg.

I request you to kindly sanction us the amount of Rs.6, 655-/ for conducting the Faculty Development Program.  
During NOV, 7<sup>th</sup> to 11<sup>th</sup>, 2017.

Thanking you sir,

  
Yours sincerely  
HOD: CSE

### Details

1. Certificate's cost Rs.1,210-/
  2. Tea & snacks cost- Rs. 1,815\*3= Rs.5,445-/
- TOTAL = Rs.6, 655-/

P. Sathish  
co-ordinator

20 B/A/C/mpt  
21

#### About the college:

Ramachandra College of Engineering and Technology, ELURU (RCEE) established in the year 2008, In vatluru village, Padapadu mandal of West Godavari district. RCEE has self contained infrastructure located amidst the lush green paddy and maize fields of Vatluru village, located in NH5 Eluru. away from the hustle and bustle of the city life. RCEE is NAAC Accredited College with 'B++' grade and is permanently affiliated to JNTUK, Kakinada. RCEE, which offers 5 undergraduate programmes and 5 Post graduate programmes with a total intake of 850 students, ranks among the top three of the 200 Engineering Colleges affiliated to JNTUK, Kakinada.

The teaching staffs, about 250 at present, comprise well qualified professionals with varied experience and background. The faculty members take interest in continuing education and are actively engaged in research work and publish their findings regularly in standard national and international journals. Some of the faculty members even received awards for their publications. In-house placement training team, absolute discipline, air-conditioned classrooms, multimodal teaching methodology. Skill Development Corporation provides state of art training to the students for improving the practical Exposure to cater the needs of the present industry.

**Department Vision:** To produce creative, competent and critically & logically reflective graduates who possess deep knowledge in the field of Computer Science and Engineering by providing high quality technical education and build the intellectual capital of research in students' and faculty community, along with commitment, equity and social justice.

#### About CSE Department:

The Department of Computer Science and Engineering, established in the year 2008, offers a four year. Undergraduate programme with an intake of 120 students and one post graduate programme in Computer Science and Engineering with an intake of 18 students. Manned by a strong faculty of 33 members, the department possesses highly advanced hardware and software's. The department provides ample opportunities to students to work on mini projects, develop communication skills, explore internship opportunities in industry and take active participation in national and international contests. The laboratories are well equipped with modern training facilities that cater to the requirements of the university syllabus.

**Registration:** Interested participants have to send their registration form in the prescribed format duly attested by the head of the institution.

#### Entries must reach:

Dr.G.Srinivasa Rao Professor  
Department of Computer Science and Engineering  
Ramachandra College of Engineering,  
NH5,Vatluru (V), Pedapadu(M),  
West Godvari - 534007.

A Five day Faculty Development Program

7<sup>th</sup> to 11<sup>th</sup> November, 2017

Organized by



Association of Computer Science  
For Technical Improvement and Valuable  
Education

Department of Computer Science  
and Engineering



NAAC Accredited with 'B++' Grade  
& Permanently Affiliated to JNTU Kakinada  
NH5, Vatluru(V), Pedapadu(M), Eluru-534007  
Andhra Pradesh  
www.rcee.ac.in  
08812-215150, 215152

MS/MS/MS  
MS/MS/MS

## Registration Form

Name: \_\_\_\_\_

Designation: \_\_\_\_\_

Institution & Department: \_\_\_\_\_

Address: \_\_\_\_\_

Qualification: \_\_\_\_\_

Experience: Teaching/Research: \_\_\_\_\_ Years

Age: \_\_\_\_\_ Sex: Male/Female

Mobile: \_\_\_\_\_

Email: \_\_\_\_\_

Signature of the Candidate

Signature of the Head of the Institution

### FDP Venue:

Seminar Hall of EEE Department,  
C-Block,  
Ramachandra College of Engineering and  
Technology, Vatluu (V), Eluru

### Chief Patron

Sri G.S.Ramachandra Rao, Chairman

### Patrons

Sri K. VenuGopal, Secretary  
Dr. DolaSanjay.S, Principal

### Convener

Dr. G.Srinivasa Rao, Professor & HOD

### Co-Convener

Dr. A. Daveedu Raju, Professor  
Dr. B.R.S. Reddy, Professor

### Co-ordinator

Sri P.Sudhkar, Associate Professor

### Organizing Committee

Sri P.Chakardhar, Associate Professor  
Sri K.Kiran  
Smt S.Nagavali  
Sri O.ShivBhagawan  
Sri B.Yedukondalu

Sri CH.Raghukumar, Asst. Professor  
Sri Y.Nagendra Kumar  
Sri K.Gopi  
Sri K.Venkatesh  
Sri B. Prasad Babu  
Sri CH.Hemanand  
Sri M. Vijaya Raju  
Sri A.Srinivasa Rao  
Smt M.S.Radha M M  
Sri G.Harihara Kumar  
Sri P.Naresh kumar  
Sri G Prasanth Kumar

*Objective:* The aim is to improve the knowledge levels of Faculty by the experts from various technical institutions like IITs, NITs and Anna University Engg Colleges across the nation, involve in sharing technical and research experiences in their respective disciplines. This program ensures value addition to the participants and would definitely improve the quality of education of Faculty members.

*Theme:* The programme aims at providing an opportunity to exchange the knowledge among faculty, research scholars and industry. It provides a platform in expressing future trends in the area.

MS/MS/FH  
20





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## REPORT ON FACULTY DEVELOPMENT PROGRAM Format:9014/0

Organized by : EEE  
Name of the Speaker : Dr.Gurunath Gurralla  
Designation : Assistant Professor, IISC, Bangalore.  
Topic : How to do research and How to write a research paper  
Venue : A-block e-class room  
Date : 15.09.2017  
Conducted for :

Branch	No of faculty members Attended
All branch faculty members	55



### Profile of the Speaker

Gurunath Gurralla (M 09) received B.Tech from S.V.H. College of Engineering, Machilipatnam, in 2001, M.Tech from J.N.T.U. College of Engineering, Anantapur, in 2003 and Ph.D from Indian Institute of Science, Bangalore, India, in 2010. He was an assistant professor in SSN college of engineering, Ongole during 2001-2002 and in Anil Neerukonda Institute of Technology and Sciences (ANITS), Visakhapatnam, during 2003-2005. He was a research engineer in GE Global research, Bangalore during 2010-2012. He was a post doctoral fellow at Texas A&M university, USA during 2012-2013 and also at Oak ridge national lab, USA during 2014-2015. He is currently an assistant professor at department of electrical engineering, Indian Institute of Science. He received Prof DJ Badkas medal for best PhD thesis from EE, IISc, Bangalore. He received INAE Young Engineer Award 2015. His papers received best paper award in IEEE General meeting 2015, best poster awards in IEEE industrial society annual meeting 2015 and EEE PES T&D Conference and Exposition 2016. His research interests include power system stability, grid integration of renewables, Microgrid control, high performance computing applications to power systems, nonlinear and adaptive control of power systems.

### Report:

The faculty development program has been conducted for the faculty members on how to start the research and how to write a standard research paper. The expert starts his lecture from way to identify the problem and method to define a problem.





The main content of his lecture is as follows:

1. Preliminary

The first activity for publishing a technical paper is to figure out your technical area of interest. Make sure you had carried out enough studies on basics of that topic. Then you have to update yourself with the ongoing technical happenings in your chosen field. You can do this by

- 1) Reading and googling a lot of technical papers. There are a lot of journals and IEEE papers floating around in net.
- 2) Go to one or more conferences, listen carefully to the best talks, and find out what people are thinking about.

Once you are done with the above mentioned steps, then you are eligible for writing a paper

2. Read existing Papers

Read everything that might be relevant gives you different perspective of the focus topic. But be selective too, for not getting too much deviated from your topic of interest. Getting used to simulation software is much useful for simulating your work. You can find a lot of time during the days and utilize those holidays & free days.

3. A jump start

When you first start reading up on a new field, ask your fellow researcher what the most useful journals and conference proceedings are in your field, and ask for a list of important papers that you should read. This activity will give you a jump start.

4. Crack the jargons and terms

One of among the tough nuts to crack is to understand the paper published by others. The easiest way is to read it many times. The more times you read the more will be revealed to you. Keep the Internet handy so that you can crack the jargons and terms, which you may find strange.

5. Write down your studies

Write down speculations, interesting problems, possible solutions, random ideas, references to look up, notes on papers you've read, outlines of papers to write, and interesting quotes. Read back through it periodically. Keeping a journal of your research activities and ideas is very useful.

6. Bits and pieces together

Now you can identify important open problems in your research field and also you will be very much aware of what you are doing and what you have to do. The more you go, you'll notice that the bits of random thoughts start to come together and form a pattern, which may be a bright enough for a good paper.

#### 7. Simulation software easies

Please don't pick overly ambitious topics; instead identify a realistic size problem. Gather the Matlab files available in the Internet that is related to your topic and simulate it for the claimed results. Please don't expect the Mfiles readily available for a solution published in a paper. But you can make it of your own by modifying and adding. Believe me, Matlab is a very easy tool! Once you are able to get the simulated outputs of your solution, you can carry on for making a paper out of it.

#### 8. Essence of your work

The essence of your work can be diagnosed by analyzing below listed points. We can increase the maturity of the paper by improving these.

Significance: Why was this work done? Did you solve an important problem of current interest or is it an obscure or obsolete problem?

Originality/Novelty: Is your approach novel or is it tried-and-true? Did you need to develop new tools, either analytical or physical?

Completeness: Have you tested a wide range of scenarios, or is this just a simple proof-of-concept?

Correct: Is your solution technically sound or are there errors? [3]

Consider improving the same.

#### 9. Anatomy of Paper

Generally a paper has seven sections and a maximum of four pages. They are

1. Abstract,
2. Introduction,
3. Existing techniques,
4. Your contribution,
5. Results and
6. Conclusion.

#### 10. The procedure

As a part of your paper publication, you can start documenting the 'existing techniques' from the scrap journal you did during the studies. Here you have to extract what all are the techniques existing as a solution for the particular problem and the pros and cons of those.

Next, document the 'introduction' about what is the topic and what you are going to do. Better to keep it short. Follows your contribution and the simulated results.


1. Describe the problem
2. State your contributions

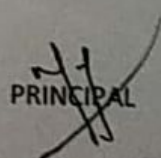
#### 1. Feedback from faculty members:

All the faculty members feel that the program is very much useful for proceeding their research and to publish a paper in the reputed journal.

#### 2. Remarks from Resource Person:

The resource person Dr.Gurunath Gurralla, from IISC, Bangalore mentioned that the session is interactive and nice.

  
HoD

  
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Format: 9014/0

Date: 17 Jul 2017

## Report on "Producing Quality Engineers" under FDP, by Mr. A K Pattabiraman on 14.07.2017

**Organized by:** Dept. of Training & Placements  
**Name of the Speaker:** Mr. A K Pattabiraman (AKP)  
**Designation:** Head-Accreditation Process for South India and Academic Interface Program (AIP) for Tamil Nadu  
**Topic:** Changing Technology; Building Competency...  
**Venue:** Aryabata Bhavan A110 (e-Classroom)  
**Date & Time:** 14 July 2017 12:45 am to 2:30 pm

Faculty	No of Faculty Attended
All Faculty from Engg and MBA	75

### Profile of the Speaker:

A K Pattabiraman (AKP) works for Tata Consultancy Services, Chennai. He heads the Accreditation Process for South India and Academic Interface Program (AIP) for Tamil Nadu; In the former role, he heads the process that evaluates colleges, their faculty and students, their teaching methodologies, commitment of the management to the cause of education and their combined efforts at competency buildings - leading to their accreditation to TCS. This in turn leads to 'Campus Recruitment'. In short he facilitates 'selection of campus' which leads to 'campus selection'. As the head of the AIP for Tamil Nadu, he is responsible for Knowledge Transfer exercises like workshops for students, Faculty Development Programs, Project Internships, Summer Training, financial and resource support for events etc. He is thus the bridge between academia and TCS. Prior to joining TCS in 2004, AKP was with Indian Institute of Technology Madras for about 23 years where he held a position in the Registry as also Head of Training and Placement. As the Head of Training and Placement and in charge of Alumni Affairs, he built up the training and placement activities right from the scratch. He was considered a trail blazer in as much as the best practices evolved in IIT M under his leadership came to be replicated in the rest of the IITs and other similarly placed institutions. He consolidated the Alumni Directory with considerable efforts, which subsequently paved the way for the Institute to open several chapters and intensify the involvement of powerful alumni across the world in the Institute's expansion - including a vast bonanza of financial aid. AKP who did his Masters from the University of Madras, started his career as Assistant Lecturer under the old Madras Educational Service. Later he joined the Central Information Service in 1964 through All India Competitive examinations, and saw posting in various Ministries and Departments of Central Government. He worked with the Cabinet Secretariat, Department of Cabinet Affairs, Research and Analysis Wing and undertook several assignments for the Prime Minister's office. Inducted into RAW in 1971, AKP served as a member of "Psywar" Committee, set up by the Government to mount psychological offensive to break the enemy morale. That it was crowned with greatest success was a recorded history at that time. AKP served on various Desks of South East Asia, West Asia and North Africa and Western Europe. AKP was associated





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with a number of sensitive and mission-critical track II political assignments during his tenure with RAW and was part of the group that worked on Strategic Analysis in respect of Security issues; AKP represented RAW on the JIC (Joint Intelligence Committee under the Union Cabinet) for a number of years. He was senior faculty in the RAW's Training Academy and built competency of fresh recruits for several years. AKP currently serves on Governing Councils, Academic Councils, Advisory Councils and Executive committee of many academic institutions. He is also the Advisor to the IIT -M Alumni Club.

## Report

### 1. Report in brief by Organizer / Coordinator / Convener:

Mr.A.K. Pattabiraman is the Accreditation head of Tata Consultancy Services (TCS). He is an eminent person, an academician and a corporate citizen. Prior to TCS he worked with IIT Madras handling multiple responsibilities including Training & Placement. His initiatives in training & placement were replicated by other IITs also. He also worked in the Research & Analysis Wing (RAW) under the Ministry of Defence handling many critical and sensitive responsibilities that are of National interest. He is visiting Ramachandra College of Engineering under the Industry-Institute Interface.

Welcoming the guest:

Mr.A.K. Pattabiraman was given a traditional "Purnakumbha swagatam"- (Ceremonial welcoming style of Andhra Pradesh with chanting of priests) into the campus by the students.

Escorted by the Hon. Secretary & Correspondent Mr.Venu Gopal & Principal Dr.Dola Sanjay the student volunteers invited Mr.Pattabiraman to Dr.A.P.J.Abdul Kalam Auditorium in the campus. After interacting with the students, Mr.Pattabiraman also interacted with the faculty members of the institution at the Seminar hall in the A block.

Interaction with the faculty:

After brief introduction formalities Mr.Pattabiraman started interacting with faculty from various departments. He spoke about the valuable work the teachers do in shaping up the future generation. He also complimented the good work done in RCE and expressed his content about the way students interacted with him during the interactive session.

He further mentioned that the way IT firms are recruiting has changed over a period of time from liberal to stringent. He checked with faculty as to how they check the quality of vegetables before buying them and extended the conversation that recruiters also check the prospective employees in all aspects.

He also mentioned that instead of teaching them the traditional way, it is always better to show them the practical way. Such realistic approach will help the students understand the concept and also will make him/ her get adapted to the industry quickly.

The interaction concluded with individual faculty members extending their regards to the guest for visiting the college and sparing time to meet the faculty.





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## Speech by the Guest:



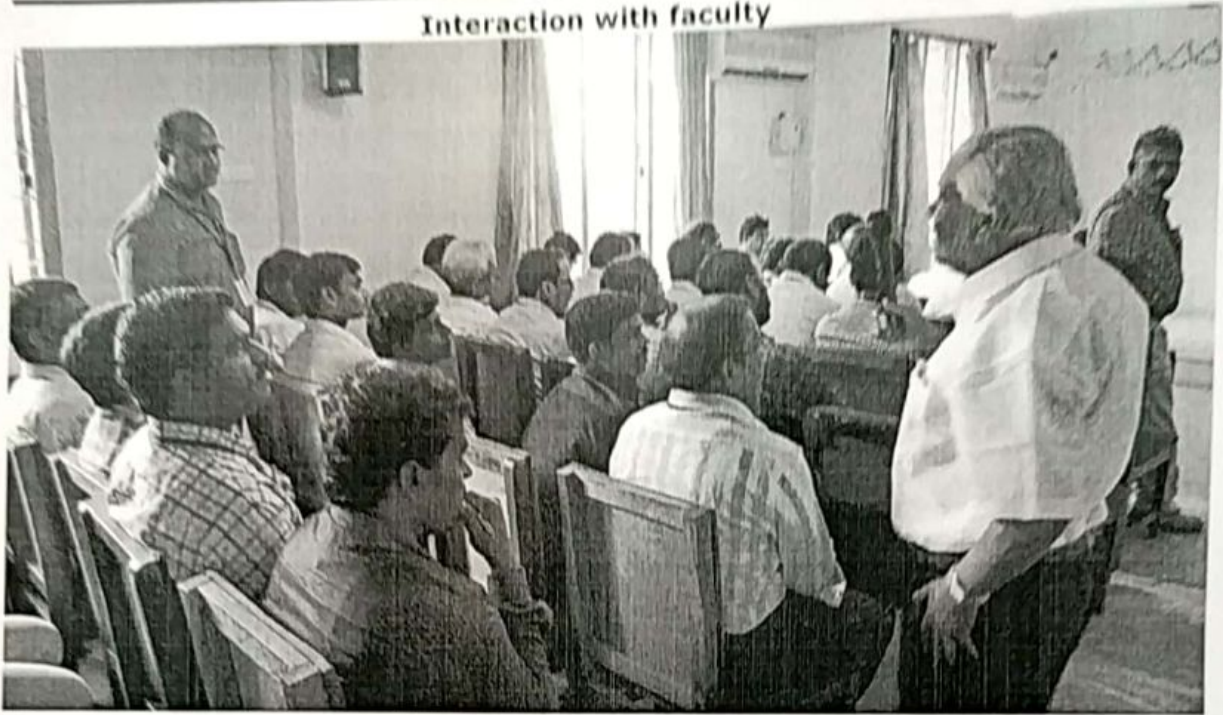




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Interaction with faculty





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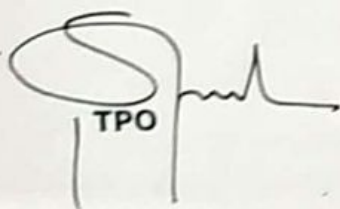
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## Feedback from Faculty:

All the felt happy about motivational and more informative speech by the guest, and, for giving tips for better production of quality Engineers.

## Feedback from the resource person:

The resource person felt happy on available doctorates in all disciplines and well experienced faculty.

  
TPO

  
PRINCIPAL



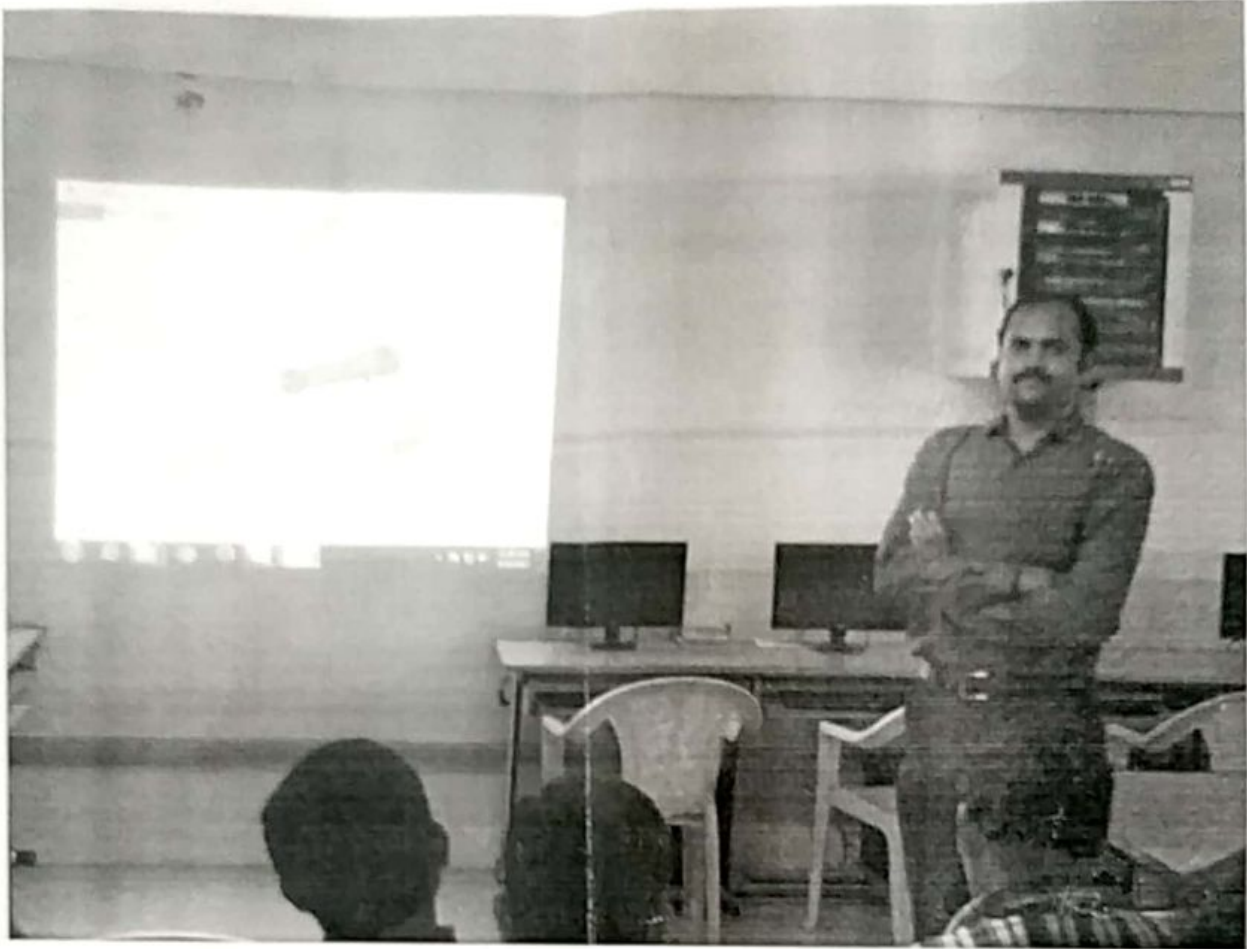
REPORT ON "A THREE DAY FACULTY DEVELOPMENT PROGRAM ON  
 CREO PARAMETRIC TRAINING."

(RESOURCE PERSON: Mr.K.KISHORE)



A three day Faculty development program (FDP-2017) was successfully conducted in the Ramachandra college of Engineering, on 17<sup>th</sup> August 2017 to 19<sup>th</sup> August 2017 by Mechanical Engineering Department in Simulation Lab of Mechanical Engineering Department, C-block 1<sup>st</sup> Floor. Only faculties were eligible for attending the FDP and 16 faculties from Mechanical Engineering Department participated in the FDP.





The session was taken by Mr.K.Kishore on Creo 4.0 parametric modeling. He discussed and suggested various tips and techniques for effective design using Creo 4.0 parametric. He practically demonstrated the designs using Creo 4.0 parametric. Every day of the training, the faculties are made to practice hands on exercises using Creo 4.0 parametric.

#### OUTCOME:

The sessions were very much informative. The discussed areas are of great benefit for the participants. Participants were enlightened with the most widely used advance technologies in today's Engineering CAD design. This in turn will help faculties to impart extra knowledge to their students.

N. E. H.  
Program  
Co-ordinator

*[Signature]*  
H.O.D

*[Signature]*  
PRINCIPAL

Eluru,

1-06-2018

To  
The Principal  
Ramachandra College of Engineering  
Eluru

Sir,

**Sub :** Permission to conduct Workshop for Technical Staff – Reg.

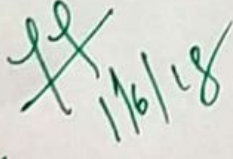
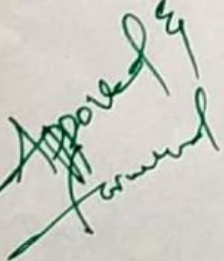
I, Dr G.Srinivasa rao working as Professor&HOD of CSE Department planning to conduct "**Python Programming**" training by K.GOPI for Technical Staff in our College on 7-06-2018. Hence, I request you to grant me permission to conduct the program.

Thanking you, Sir

Yours faithfully,

  
Dr G.Srinivasa rao

HOD-CSE

  
16/6/18  
  
Approved &  
Forwarded (K HOD)





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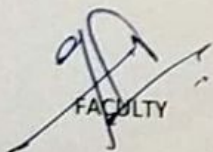
**NAAC**  
**B++ Grade**

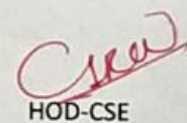
Date: 4/6/18

Department Of Computer Science and Engineering

Attendance Sheet For Add On Programs

S.NO	NAME OF TECHNICAL STAFF	SIGNATURE
1	MR. K. SRINIVAS	K. Srinivas
2	MR. R. SATEESH	R. Sateesh
3	Ms.T SONY KANAKA DEPTHI	T.S. Kanaka depthi
4	Ms.T.ASHA	T. Asha
5	Ms T DURGA BHAVANI	T. D. Bhavani
6	Ms D.NAGA SUJANA	D. Sujana

  
FACULTY

  
HOD-CSE



# Python Data Structures Tutorial

Get introduced to Python data structures; learn more about data types and primitive as well as non-primitive data structures, such as strings, lists, stacks, etc.

Data structures are a way of organizing and storing data so that they can be accessed and worked with efficiently. They define the relationship between the data, and the operations that can be performed on the data. There are many various kinds of data structures defined that make it easier for the data scientists and the computer engineers, alike to concentrate on the main picture of solving larger problems rather than getting lost in the details of data description and access.

In this tutorial, you'll learn about the various Python data structures and see how they are implemented:

- Abstract Data Type and Data Structures

- Primitive Data Structures

- Integers

- Float

- Strings

- Boolean

- Non-Primitive Data Structures

- Arrays

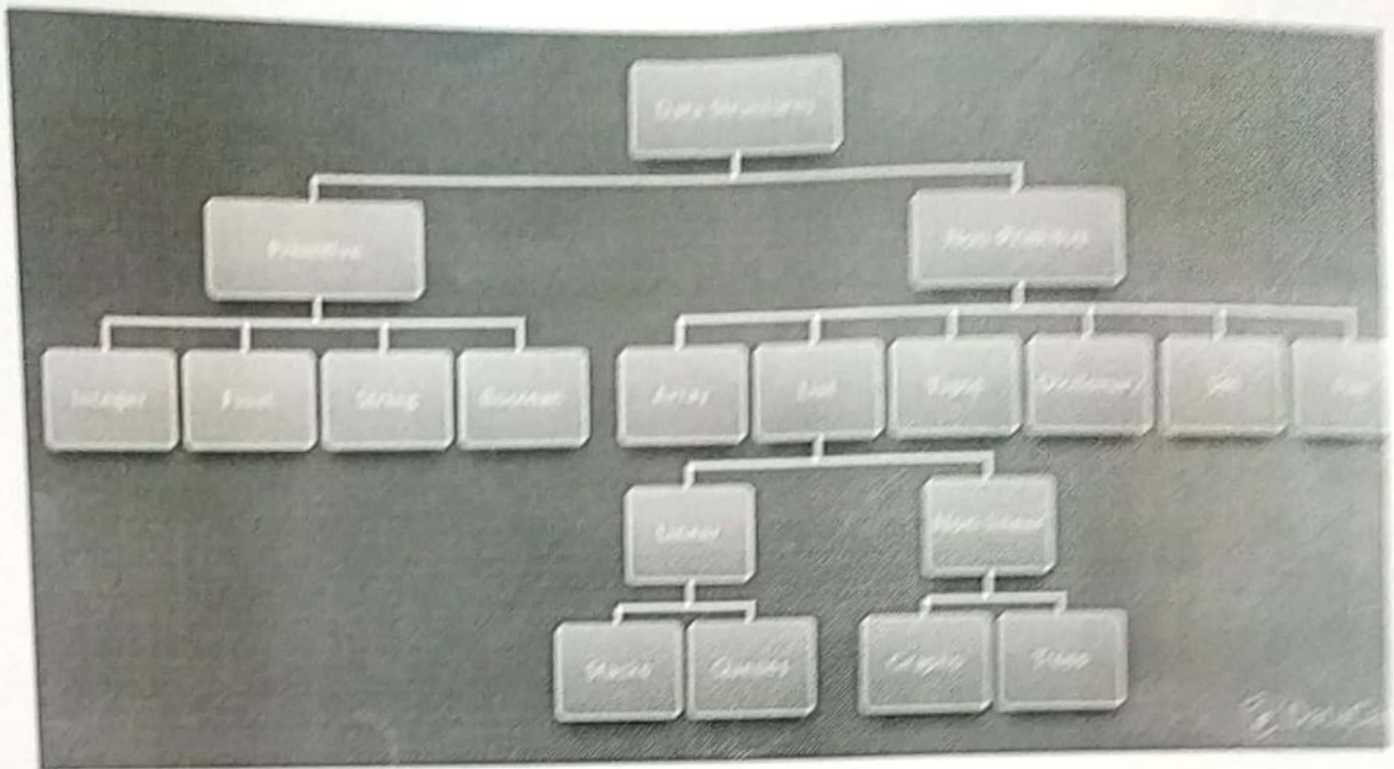
- Lists

- Tuples

- Dictionary

- Sets

- Files



In DataCamp's free *Intro to Python for Data Science* course, you can learn more about using Python specifically in the data science context. The course gives an introduction to the basic concepts of Python. With it, you'll discover methods, functions, and the NumPy package.

## Abstract Data Type and Data Structures

As you read in the introduction, data structures help you to focus on the bigger picture rather than getting lost in the details. This is known as data abstraction.

Now, data structures are actually an implementation of Abstract Data Types or ADT. This implementation requires a physical view of data using some collection of programming constructs and basic data types.

Generally, data structures can be divided into two categories in computer science: primitive and non-primitive data structures. The former are the simplest forms of representing data, whereas the latter are more advanced: they contain the primitive data structures within more complex data structures for special purposes.

## Primitive Data Structures



### Technical staff Skills Report

Organized by : Computer Science and Engineering  
Name of the Speaker : K .Gopi  
Designation : Assit.professor-RCE  
Course : Python  
Venue : Computer Lab1

S.NO	DATE	SESSION
1	07-06-2018	9.10 A.M -4.40 PM

#### **Report**

##### **1. Report in brief by Organizer / Coordinator / Convener:**

The Speaker Addressed the python importance & data structures in python..



*P. S. Dhar*  
Coordinator

*Gopi*  
HOD



Eluru,

1-06-2018

To  
The Principal  
Ramachandra College of Engineering  
Eluru

Sir,

**Sub :** Permission to conduct Workshop for Technical Staff – Reg.

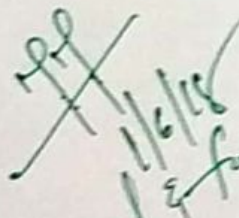
I, Dr G.Srinivasa rao working as Professor&HOD of CSE Department planning to conduct “**Unix Programming**” training by G.Prasanth Kumar for Technical Staff in our College on 04-06-2018. Hence, I request you to grant me permission to conduct the program.

Thanking you, Sir

Yours faithfully,

  
Dr G.Srinivasa rao

HOD-CSE

  
Approved & forward to HOD



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**B++ Grade**

03-06-2018

Circular

Rcee/Cse/technical/skill

This is to inform that CSE Department is going to organize skill upgradation in unix programming on 04-06-2018. In this regard, all the technical staff should attend without fail.

Venue:computer lab1

Time:9:10a.m

*Csrw*  
*3/6/2018*  
HOD-CSE



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**B++ Grade**

Date : 4/6/18

Department Of Computer Science and Engineering

Attendance Sheet For Add On Programs

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2	MR. R. SATEESH	R. Sateesh
3	Ms.T SONY KANAKA DEPTHI	T.s.k. Deepthi
4	Ms.T.ASHA	T. Asha
5	Ms T DURGA BHAVANI	T. D. Bhavani
6	Ms D.NAGA SUJANA	D. Sujana

FACULTY

HOD-CSE

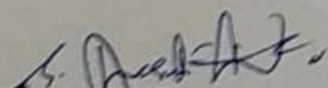



Date: 4/6/18

# UNIX PROGRAMMING

## TODAY DAIRY

S.NO	TOPIC COVERED
1	BASIC COMMANDS
2	CHANGE MODES
3	VI EDITOR

  
FACULTY

  
CSE-Module 1/4-2018

### Technical staff Skills Report

Organized by : Computer Science and Engineering  
Name of the Speaker : G. Prasanth Kumar  
Designation : Assit.professor-RCE  
Course : unix programming  
Venue : Computer Lab1

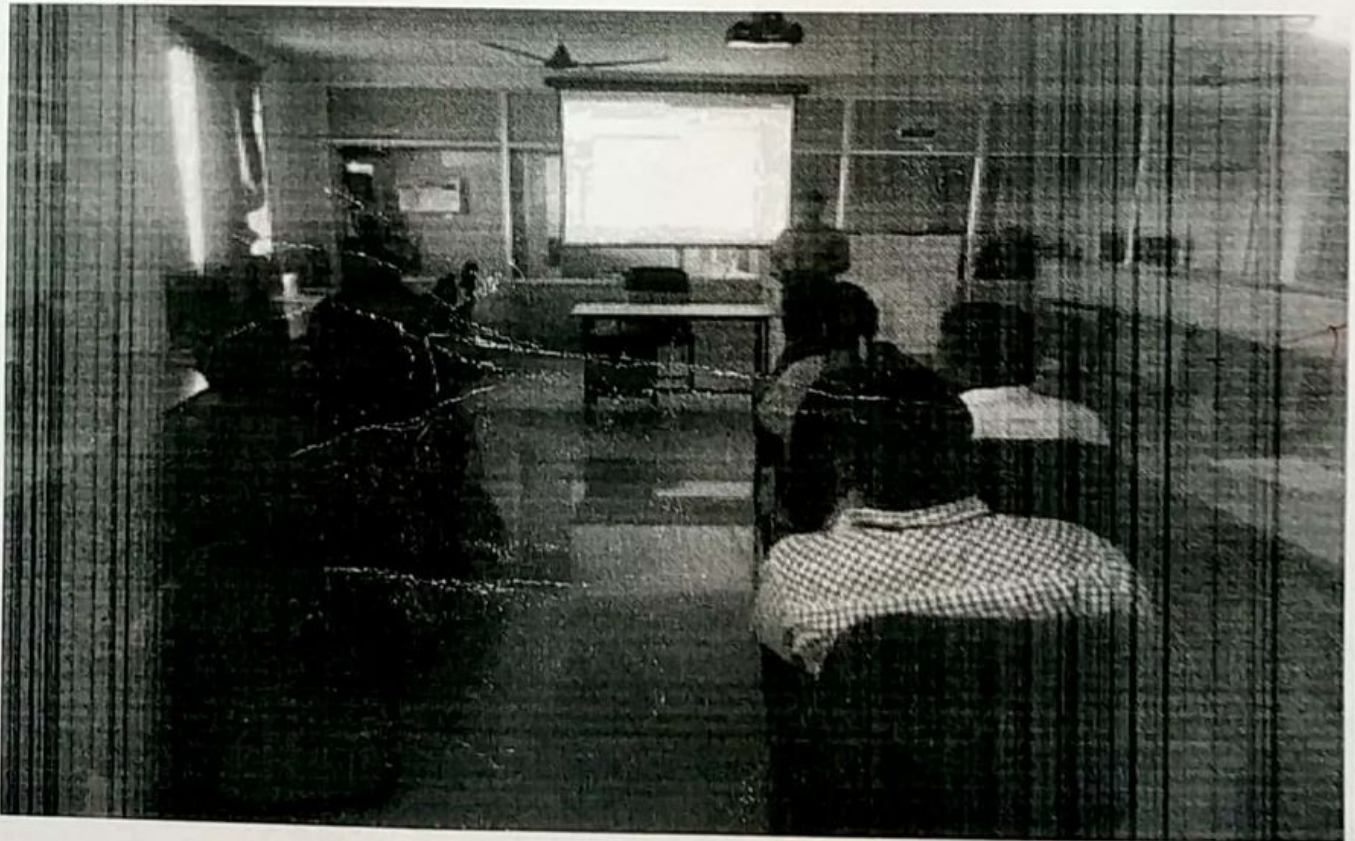
S.NO	DATE	SESSION
1	04-06-2018	9.10 A.M -4.40 PM

#### **Report**

##### **1. Report in brief by Organizer / Coordinator / Convener:**

The Speaker Addressed basic commands in unix, change modes,vi editors..

##### **2. Photos:**



*P. Redhaker*  
Coordinator

*Gany*  
HOD 4/6/18

## **PROGRAM SCHEDULE ICRDEST**

<b>Time</b>	<b>Event</b>
09:30 A.M	Welcome Note
09:35 A.M	Prayer Song
09:40 A.M	About College
09:45 A.M	Welome Semi-Traditional Dance by Mounica & Supriya
09:50 A.M	Inviting the Delgates on to the Dias by Anchors (Inaguration)
09:55 A.M	Lightening the Lamp
09:55 A.M	<p>Message by Delgates</p> <p style="padding-left: 40px;">Opening Remarksby Co-ordinator, Prof.M.Pacha Khan</p> <p style="padding-left: 40px;">Message by Dr.Bh.Vara Prasad, Dean</p> <p style="padding-left: 40px;">Message by Principal Dr.Dola Sanjay.S</p> <p style="padding-left: 40px;">Message by Vice-Principal Prof.S.Jagan Mohana Rao</p> <p style="padding-left: 40px;">Message by Dr.Siva Ganesh Malla, Chair Person-ICRDEST and Chairman CPGC Groups</p> <p style="padding-left: 40px;">Message by Dr.Suresh Pittala, Department of Physics-Indian Institute of Science, Banglore</p> <p style="padding-left: 40px;">Message by Dr.V.Ganesh, (DST-SERB) Young Scientist ,SRM University Chennai</p> <p style="padding-left: 40px;">Message by Dr.K.Sahayaraj, Director Crop Protection Research Centre, Associate Professor of Zoology, St.Xavier's College(Autonomous), Tamilnadu</p> <p style="padding-left: 40px;">Message by Prof.Konstantin S Kazakou, Department of Mechanis &amp; Mathematics, VSU University, Bulgaria</p> <p style="padding-left: 40px;">Message by Prof Jagan Mohana Rao, Director CPGC Groups</p>

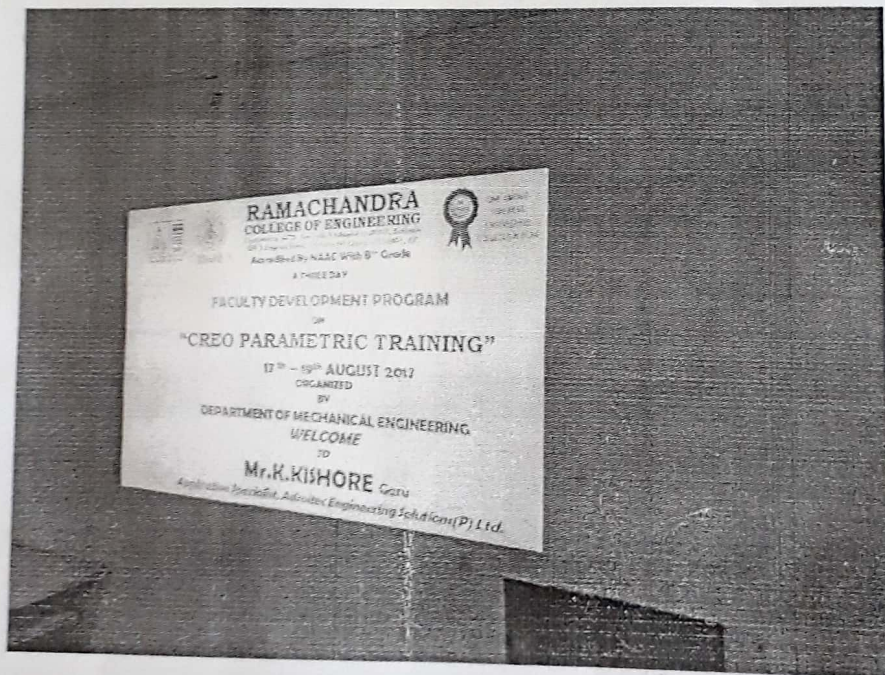


1. Dr.K.Sahayaraj Director Crop Protection Research Centre, Associate Professor of Zoology, St.Xavier's College(Autonomous), Tamilnadu
2. Prof.Konstantin S Kazakou Professor Department of Mechanis & Mathematics, VSU University, Bulgaria
3. Dr.V.Ganesh (DST-SERB) Young Scientist SRM University Chennai
4. Dr.Suresh Pittala Department of Physics-Indian Instituteof science, Bangalore
5. Vasanth Adari CEO CTGC Groups
6. M.G.Naidu, Founder CPGC Groups
7. Prof Jagan Mohana Rao Director CPGC Groups
8. Dr.Siva Ganesh Malla Chair Person, ICRDEST and Chairman CPGC Groups



REPORT ON "A THREE DAY FACULTY DEVELOPMENT PROGRAM ON  
CREO PARAMETRIC TRAINING."

(RESOURCE PERSON: Mr.K.KISHORE)



A three day Faculty development program (FDP-2017) was successfully conducted in the Ramachandra college of Engineering, on 17<sup>th</sup> August 2017 to 19<sup>th</sup> August 2017 by Mechanical Engineering Department in Simulation Lab of Mechanical Engineering Department, C-block 1<sup>st</sup> Floor. Only faculties were eligible for attending the FDP and 16 faculties from Mechanical Engineering Department participated in the FDP.

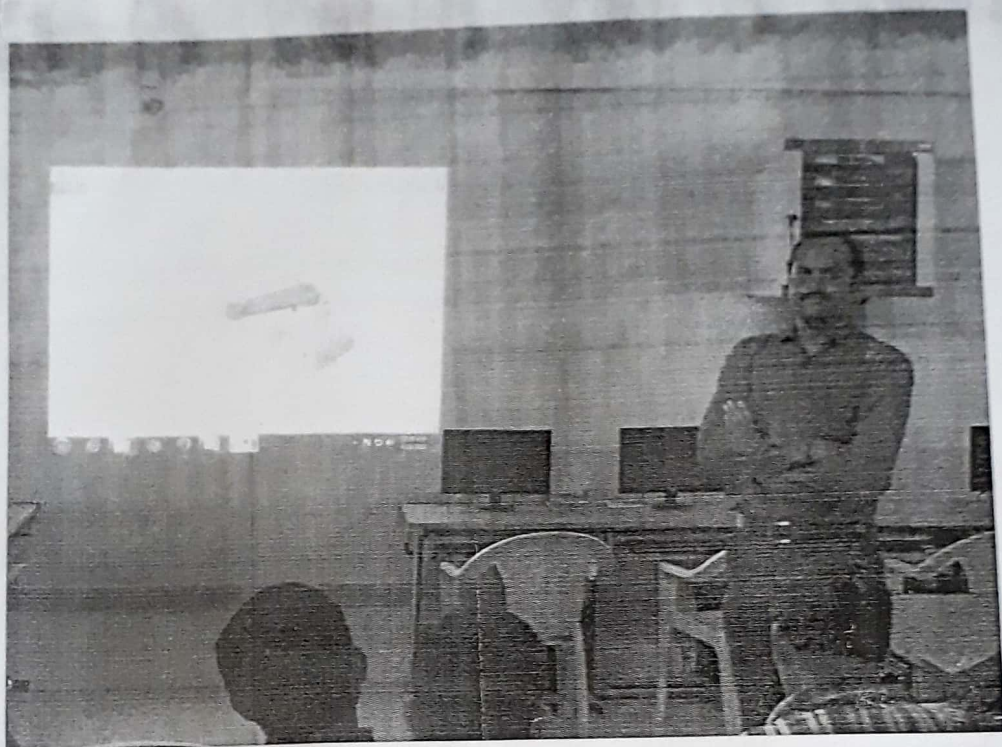


Topics Covered:

Introduction to modeling

- Introduction to Creo 4.0 parametric
- Sketching in Creo
- Part modeling in Creo
- Isometric models in Creo
- Piston design in Creo
- Modeling tools in Creo
- Extrude
- Revolve
- Hole
- Draft
- Rib
- Sweep
- Pattern
- Constraints
- Assembly drawings
- Drafting





The session was taken by Mr.K.Kishore on Creo 4.0 parametric modeling. He discussed and suggested various tips and techniques for effective design using Creo 4.0 parametric. He practically demonstrated the designs using Creo 4.0 parametric. Every day of the training, the faculties are made to practice hands on exercises using Creo 4.0 parametric.

#### OUTCOME:

The sessions were very much informative. The discussed areas are of great benefit for the participants. Participants were enlightened with the most widely used advance technologies in today's Engineering CAD design. This in turn will help faculties to impart extra knowledge to their students.

M. E. H.

Program  
Co-ordinator

H.O.D

PRINCIPAL



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This is to certify that Mr/Ms Mr.K.Giridhar participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup>-19<sup>th</sup> August,2017.

**Co-ordinator**

**HOD**

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This is to certify that Mr/Ms Mr.T.Mahaboob Basha participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup> -19<sup>th</sup> August,2017.

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This is to certify that Mr/Ms Mr.P.Sarafoji participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup>-19<sup>th</sup> August,2017.

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This is to certify that Mr/Ms Mr.M.Boopathi participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup>-19<sup>th</sup> August,2017.

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This is to certify that Mr/Ms Mr.K.Harinath Gowd participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup> -19<sup>th</sup> August,2017.

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This is the to certify that Mr/Ms Mr.H.Raghavendra participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup> -19<sup>th</sup> August,2017.

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This is to certify that Mr/Ms Mr.K.Radha Krishna participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup>-19<sup>th</sup> August,2017.

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This is to certify that Mr/Ms Mr.Y.Venkatesh participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup>-19<sup>th</sup> August,2017.

**Co-ordinator**

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This is to certify that Mr/Ms Mr. A Rahul Kumar participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup>-19<sup>th</sup> August,2017.

**Co-ordinator**

**HOD**

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### **CERTIFICATE OF PARTICIPATION**

This is to certify that Mr/Ms Mr. G V Subhash participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup>-19<sup>th</sup> August,2017.

**Co-ordinator**

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### **CERTIFICATE OF PARTICIPATION**

This is the to certify that Mr/Ms Dr.Md Abid Ali participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup> -19<sup>th</sup> August,2017.

**Co-ordinator**

**HOD**

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### **CERTIFICATE OF PARTICIPATION**

This is to certify that Mr/Ms Mr.B.Suresh Babu participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup>-19<sup>th</sup> August,2017.

**Co-ordinator**

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### **CERTIFICATE OF PARTICIPATION**

This is to certify that Mr/Ms Mr.B.Raja Kumar participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup> -19<sup>th</sup> August,2017.

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### **CERTIFICATE OF PARTICIPATION**

This is to certify that Mr/Ms Mr.Sk.Abdul Azeez participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup> -19<sup>th</sup> August,2017.

**Co-ordinator**

**HOD**

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### **CERTIFICATE OF PARTICIPATION**

This is to certify that Mr/Ms Mr.Swarna Sri Krishna participated in “A THREE DAYS FACULTY DEVELOPMENT PROGRAM ON CREO PARAMETRIC TRAINING ” organized by RAMACHANDRA COLLEGE OF ENGINEERING, ELURU, ANDHRA PRADESH, during 17<sup>th</sup> -19<sup>th</sup> August,2017.

**Co-ordinator**

**HOD**

**PINCIPAL**



### Workshop Report

**Organized by** : Dept. of ECE, Ramachandra College of Engineering  
**Name of the Speaker** : Mr. Khasim, Innovative Technologies, Vijayawada  
**Designation** : H/W Trainer  
**Topic** : Embedded systems  
**Venue** : Room.No.208, Block-B  
**Date & Time** : 07-08-2017, 9:00 AM

Branch	Year	Semester	No of Students Attended
ECE	IV,III,II	II	64
Total No of Students Attended			64

#### Profile of the Speaker

Mr. Khasim is a H/W trainer FROM Innovative technologies in Vijayawada. He had a great passion on designing and development of embedded systems fields. Give motivation to the students regarding ethics followed by the engineers to go for designing of any model and basics values.

#### Report

##### 1. Report in brief by Organizer / Coordinator / Convener:

The Guest lecture is planned on 7-08-2017 after receiving permission from Mr. Khasim Sir through phone. We had a very detailed explanation from the Sir about embedded systems. The Lecture is very useful for the ECE students.

After completion of this workshop students are enriched in the area of values and ethics to design a model in the field of electronics and communication engineering.

All the students are very happy after listening to the class.





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**B++ Grade**

## Department of Electronics & Communication Engineering

Date: 01/08/2017

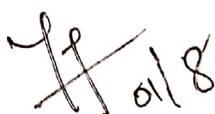
Eluru.

To:  
The Principal,  
RCEE, Eluru

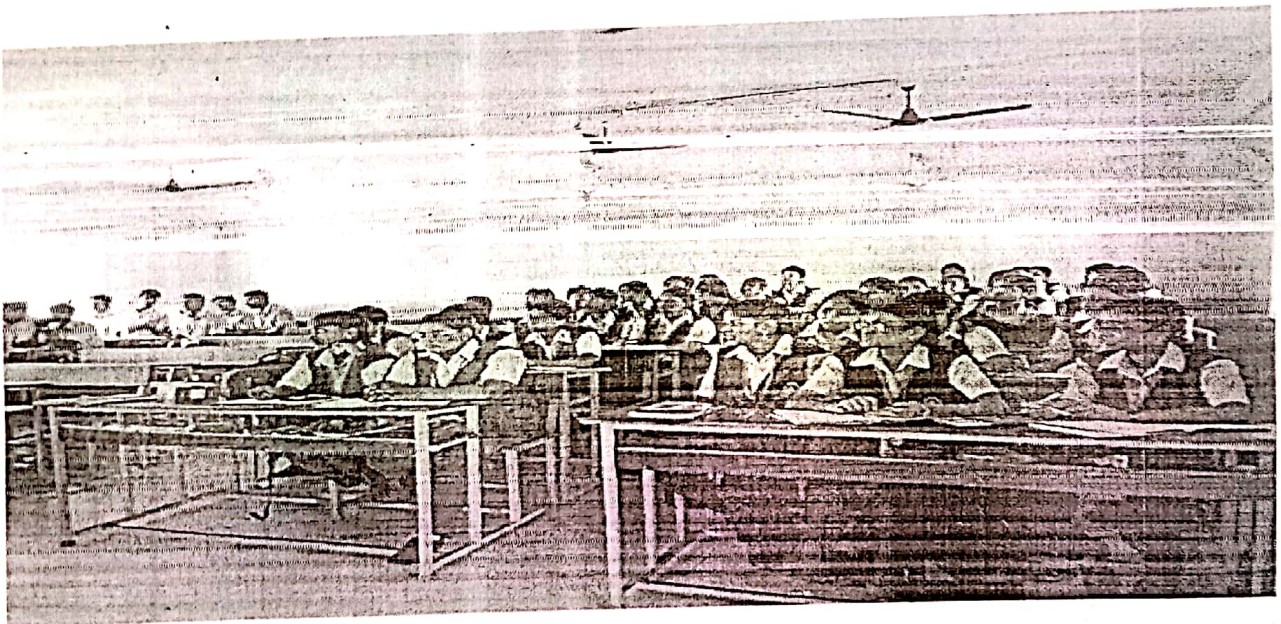
We the department of ECE is going to conduct a workshop on **Embedded Systems** with **Mr. Khasim, Innovative Technologies Vijayawada**, on 07-08-2017 in ECE seminar hall B-208. Give permission to conduct this as the purpose is serving and issue the amount of 10,000/- for the workshop regards.

Thanking you sir.

  
HOD

  
01/8

## 2. Photos



### 3. Feedback from students:

- Your overall experience: Excellent
- The comprehensiveness, clarity and accuracy of the information provided : Excellent
- Guest Lecturer knowledge, co-operation and level of explanation: Excellent
- Guide's enthusiasm: Excellent
- Guide's knowledge: Excellent

Guide's Communication: Excellent

### 4. Remarks from Resource Person:

- Students are well disciplined
- Students need to improve their subject knowledge by reading the standard text books.

Coordinator

HOD

PRINCIPAL





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**B++ Grade**

## Department of Electronics and Communication Engineering

The following is the List of Faculty attended for Workshop on Embedded Systems for A.Y 2017-18.

S.No	Name of the Faculty	Designation	Signature
1	Dr. U. Yedukondalu	Professor & HoD	
2	Dr. Jagan Mohan Rao S.	Professor	
3	Dr. Dola Sanjay S.	Professor	
4	Mr. Md. Baig Mohammad	Assoc. Prof.	
5	Mr. T. Rambabu	Assoc. Prof.	
6	Mr. J. Prasanth Kumar	Assoc. Prof.	
7	Mr. D. Sridhar	Assoc. Prof.	
8	Mr. R. Radha Krishna	Assoc. Prof.	
9	Mr. Md. Ali Baig	Asst. Prof.	
10	Mr. D. Venkanna Babu	Asst. Prof.	
11	Mr. D. Ram Babu	Asst. Prof.	
12	Mr. A. Rajesh Naidu	Asst. Prof.	
13	Mr. R. Durga Prasad	Asst. Prof.	
14	Mr. M. Rama Krishna	Asst. Prof.	
15	Mrs. Y. Lavanya	Asst. Prof.	
16	Mr. NVDP Murthy	Asst. Prof.	
17	Mr. N. Rama Krishna	Asst. Prof.	
18	Mr. R.L.R. Lokesh Babu	Asst. Prof.	
19	Mr. M. Ravi	Asst. Prof.	
20	Mr. G. Rajesh Babu	Asst. Prof.	
21	Mrs. Tenalirupa	Asst. Prof.	
22	Mr. B. China kotaiah	Asst. Prof.	
23	Mr. R. Raghu Ram	Asst. Prof.	
24	Mr. Venu Nandam	Asst. Prof.	
25	Ms. Ch. Naga Lakshmi	Asst. Prof.	
26	Mr. M. Ravi Kiran	Asst. Prof.	
27	Ms. P. Bhagya Sri	Asst. Prof.	
28	Mr. Ch. Joji Babu	Asst. Prof.	
29	Mr. P. Satish Babu	Asst. Prof.	
30	Mr. M. Venkataamarnath	Asst. Prof.	

Principal





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(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH 5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

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## Workshop Report

Organized by : Ramachandra College of Engineering  
Name of the Speaker : Dr.B.Madhavan  
Designation : Professor from the KLU university  
Topic : Workshop on Antenna design using HFSS  
Venue : EEE e-Class room, C-block  
Date & Time : 04.09.2017 & 11.00AM

Branch	Year	Semester	No of Students Attended
ECE	IV,III	I	90
Total No of Students Attended			90

### Profile of the Speaker

Mr. is known consultant for Antennas. He has over 12 years of experience in this industry.

### Report

#### 1. Report in brief by Organizer / Coordinator / Convener:

The main objective of the workshop is to make the students have basic knowledge on Antenna design using HFSS . One day work shop is organized for the Departments of ECE on 04-09-2017 on Fundamental Concepts of HFSS . A total of 180 students have attended the workshop.

The workshop is divided into 2-sessions; the first session is totally concentrated in making the students get strong at basics related to the Voltage, Electromagnetic waves, VSWR and Laws & Fundamentals about Refraction, Reflection angle along with practical applications with help Parabolic antenna.

#### 2. Photos:

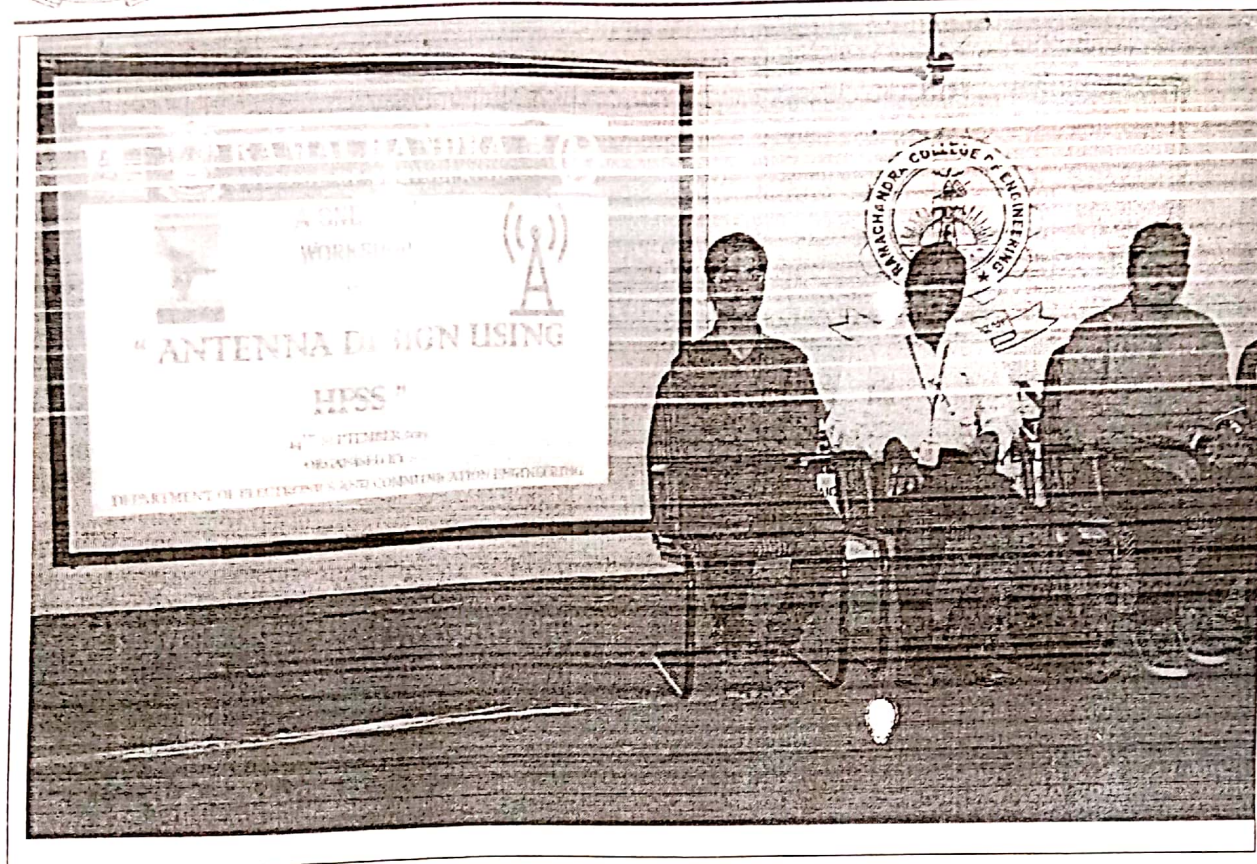






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**3. Feedback from students:** Students' have got good knowledge in introductory part of Embedded Systems. Interaction of the mentor is quite good with the students.

**4. Remarks from Resource Person:** Very good opportunities are available in the field of Embedded systems. Students' are good and pay complete attention on my lecture.

  
Co-ordinator

  
HoD

  
Principal





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**B++ Grade**

## Letter from HOD to the principal

Place: Eluru,  
Date: 29-08-2017

From:  
Prof.U.YEDUKONDALU  
HoD, ECE Department  
RCE, Eluru.

To,  
The Principal,  
RCE,  
Eluru.

Sub: Requisition for permission to conduct workshop-Reg

Respected sir,

We the Department of ECE going to conduct Workshop on " Antenna design using HFSS " from 4<sup>th</sup> sep of 2017. In this context, I request you to grant me permission for the same.

Thanking you sir

Yours sincerely,

*Putthi  
y  
All the best*



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**B++ Grade**

## Circular

Date: 29-08-2017

All the students III & IV year are hereby informed that Department of ECE is planning to organize a Workshop on "Antenna design using HFSS " on 4<sup>th</sup> Sep of 2017. In this context all are requested to attend the program without fail.

Venue: EEE e-Class room, C-block

  
HoD



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**B++ Grade**

To,  
The Principal,  
RCEE, Eluru.

Date:

Sub: Request for Budget to conduct a workshop -Reg

Sir,

We the Department of ECE are going to conduct a "Workshop on Antenna design using HFSS " from 4<sup>th</sup> sep of 2017. In this regard, I request you to grant the following budget.

## Proposed Budget for Workshop

Expert Remuneration	:	10000
TA	:	
Lunch + Tea Snacks	:	
Banner	:	200
Certificates (198 x 8)	:	1584

Total :

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*[Handwritten signature]*  
HoD





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**B++ Grade**

## Department of Electronics and Communication Engineering

The following is the List of Faculty attended for Workshop on Antenna design using HFSS for A.Y 2017-18.

S.No	Name of the Faculty	Designation	Signature
1	Dr. U. Yedukondalu	Professor & HoD	
2	Dr. Jagan Mohan Rao S.	Professor	
3	Dr. Dola Sanjay S.	Professor	
4	Mr. Md. Baig Mohammad	Assoc. Prof.	
5	Mr. T. Rambabu	Assoc. Prof.	
6	Mr. J. Prasanth Kumar	Assoc. Prof.	
7	Mr. D. Sridhar	Assoc. Prof.	
8	Mr. R. Radha Krishna	Assoc. Prof.	
9	Mr. Md. Ali Baig	Asst. Prof.	
10	Mr. D. Venkanna Babu	Asst. Prof.	
11	Mr. D. Ram Babu	Asst. Prof.	
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15	Mrs. Y. Lavanya	Asst. Prof.	
16	Mr. NVDP Murthy	Asst. Prof.	
17	Mr. N. Rama Krishna	Asst. Prof.	
18	Mr. R.L.R. Lokesh Babu	Asst. Prof.	
19	Mr. M. Ravi	Asst. Prof.	
20	Mr. G. Rajesh Babu	Asst. Prof.	
21	Mr. Andra Babul Reddy	Asst. Prof.	
22	Mr. D. Ashokkumar	Asst. Prof.	
23	Mr. D. Lakshmi murthy	Asst. Prof.	
24	Mr. G. Ramana Reddy	Asst. Prof.	
25	Mr. K. Raveendra	Asst. Prof.	
26	Mr. B. Venkatesh	Asst. Prof.	
27	Mr. Y. Chiranjeevi	Asst. Prof.	
28	Mr. S.C. Penchalaiah	Asst. Prof.	

Principal



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**Department of Electronics and Communication Engineering**

## CERTIFICATE OF PARTICIPATION

This is to certify that Mr./ Ms. Dr. U. Yedukondalu,  
\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
using HFSS" conducted on 4<sup>th</sup> Sept. 2017 in Association with Dr. B. T. P. Madhav,  
Professor, ECE, KL University, Organised by the Dept. of ECE, RCE, ELURU.

*W. Venkanna Babu*

Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

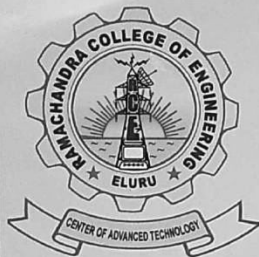
*Dr. U. Yedukondalu*

Dr. Udara Yedukondalu  
HoD-ECE

*Dr. Dola Sanjay S.*  
Principal







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\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
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Professor, ECE, KL University, Organised by the Dept. of ECE, RCE, ELURU.

*W. Venkanna Babu*

Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

*Dr. Udara Yedukondalu*

Dr. Udara Yedukondalu  
HoD-ECE

*Dr. Dola Sanjay S.*  
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This is to certify that Mr./ Ms. Mr. T. Rambabu,  
\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
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*W. Venkanna Babu*

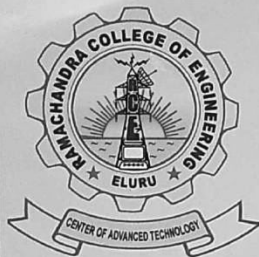
Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

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Dr. Udara Yedukondalu  
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Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
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\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
using HFSS" conducted on 4<sup>th</sup> Sept. 2017 in Association with Dr. B. T. P. Madhav,  
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This is to certify that Mr./ Ms. Mr. R. Radha Krishna,  
\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
using HFSS" conducted on 4<sup>th</sup> Sept. 2017 in Association with Dr. B. T. P. Madhav,  
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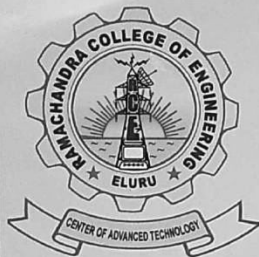
Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
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This is to certify that Mr./Ms. Mr. Md. Ali Baig,  
\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
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*Mr. J. Prasanth Kumar*

Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

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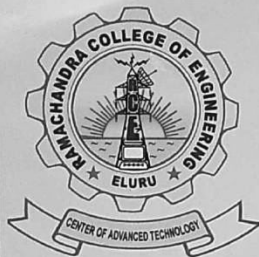
Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

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This is to certify that Mr./ Ms. Mr. A. Rajesh Naidu,  
\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
using HFSS" conducted on 4<sup>th</sup> Sept. 2017 in Association with Dr. B. T. P. Madhav,  
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Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

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*This is to certify that Mr./ Ms. \_\_\_\_\_ Mr. R. Durga Prasad \_\_\_\_\_,  
\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
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Professor, ECE, KL University, Organised by the Dept. of ECE, RCE, ELURU.*

W. Henkenel Bebe

**Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu**  
**Co-ordinator**

10/5/12

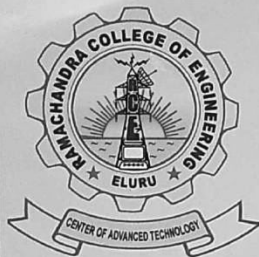
**Dr. Udara Yedukondalu**  
**HoD-ECE**

ola Sania

**Dr. Dola Sanjay S.**  
**Principal**







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This is to certify that Mr./ Ms. Mrs. Y. Lavanya,  
\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
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Professor, ECE, KL University, Organised by the Dept. of ECE, RCE, ELURU.

*W. Venkanna Babu*

Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

*Dr. Udara Yedukondalu*

Dr. Udara Yedukondalu  
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## CERTIFICATE OF PARTICIPATION

This is to certify that Mr./Ms. Mr. NVDP Murthy,  
\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
using HFSS" conducted on 4<sup>th</sup> Sept. 2017 in Association with Dr. B. T. P. Madhav,  
Professor, ECE, KL University, Organised by the Dept. of ECE, RCE, ELURU.

*W. Venkanna Babu*

Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

*Dr. Udara Yedukondalu*

Dr. Udara Yedukondalu  
HoD-ECE

*Dr. Dola Sanjay S.*  
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Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
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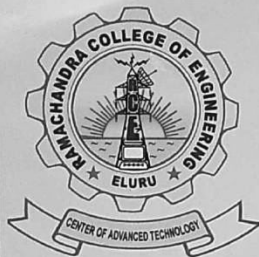
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Dr. Udara Yedukondalu  
HoD-ECE

*Dr. Dola Sanjay S.*

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\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
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*W. Venkanna Babu*

Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

*Dr. Udara Yedukondalu*

Dr. Udara Yedukondalu  
HoD-ECE

*Dr. Dola Sanjay S.*  
Principal



# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved By AICTE, New Delhi and Affiliated to JNTUK, Kakinada)

NH-5, Bypass Road, Vatluru(V), Eluru-534007

08812 - 20150, 215152, e mail: ecehod@rcee.ac.in, www.rcee.ac.in

An ISO 9001-2008 Certified, Accredited by NAAC with B++

ANSYS

HFSS

**Department of Electronics and Communication Engineering**

## CERTIFICATE OF PARTICIPATION

This is to certify that Mr./ Ms. Mr. M. Ravi,  
\_\_\_\_\_ has participated in "A one day workshop on Antenna Design  
using HFSS" conducted on 4<sup>th</sup> Sept. 2017 in Association with Dr. B. T. P. Madhav,  
Professor, ECE, KL University, Organised by the Dept. of ECE, RCE, ELURU.

*W. Venkanna Babu*

Mr. J. Prasanth Kumar / Mr. D. Venkanna Babu  
Co-ordinator

*Dr. Udara Yedukondalu*

Dr. Udara Yedukondalu  
HoD-ECE

*Dr. Dola Sanjay S.*

Dr. Dola Sanjay S.  
Principal

Approved by AICTE - New Delhi, Recognized by UGC w/s 2 (f) & 12 (B) and Permanently affiliated to JNTUK Kakatiya University, Warangal (N), Telangana - 506 007, West Godavari District, A. P. 506 007 : 2015 certified & NVAAC Accredited Institute

Accredited

Department of Electronics and Communication Engineering

Lab Orientation Program for the A. Y. 2017-18 - Even Semester

#	Name of the Faculty Member	Dates	Time	Name of the lab	Remarks	Signature
1	Mr. M. Ravi Kiran	01-11-2017 2-11-2017	9am-12pm	ECA LAB	12 Experiments are conducted and the results are verified	Mr. M. Ravi Kiran
2	Ms. P. Bhagya Sri					Ms. P. Bhagya Sri
3	Mr. Ch. Jothi Babu	01-11-2017 2-11-2017	1pm-4pm	MPMC LAB	12 Experiments are conducted and the results are verified	Mr. Ch. Jothi Babu
4	Mr. P. Satish Babu					Mr. P. Satish Babu
5	Mr. M. Venkataamarath					Mr. M. Venkataamarath
6	Mr. Andra Babul Reddy	03-11-2017 04-11-2017	9am-12pm	VLSI LAB	10 Experiments are conducted and the results are verified	Mr. Andra Babul Reddy
7	Mr. D. Ashokkumar					Mr. D. Ashokkumar
8	Mr. D. Lakshmi murthy					Mr. D. Lakshmi murthy
9	Mr. G. Ramana Reddy	03-11-2017 04-11-2017	1pm-4pm	DC LAB	12 Experiments are conducted and the results are verified	Mr. G. Ramana Reddy
10	Mr. K. Ravendra					Mr. K. Ravendra
11	Mr. NVDP Murthy					Mr. NVDP Murthy
12	Mr. N. Rama Krishna	03-11-2017 04-11-2017	1pm-4pm	DC LAB	12 Experiments are conducted and the results are verified	Mr. N. Rama Krishna



13	Mr. R.L.R. Lokesha Babu					Chokkiah
14	Mr. M. Ravi	06-11-2017	8am-12pm	ECR LAB	12 Experiments are conducted and the results are verified	Mr. Ravi
15	Mr. A. Rajesh Babu	07-11-2017				Chokkiah

  
HOD

Department of Electronics and Communication Engineering

**Table Orientation Program for the A.Y. 2016-17 - Even Semester**

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1st Name	
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3rd Name	

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8th Name	
9th Name	
10th Name	

Practical  
Doubt  
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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 Faculty Orientation Program A.Y 2017-18 Odd Sem

S.No	Date	Time	Lecture By	Lecture Topic	Signature
1	23-4-2017	10:00 AM	Mr. G. Rajesh Babu	Introduction to chip fabrication	G.Rajesh
		10:30 AM	Mrs. Tenalirupa	Standards of DSP processors	Tenali
		11:00 AM	Mr. B. China kotaiah	Introduction to PDC	B.China
2	24-4-2017	10:00 AM	Mr. R. Raghu Ram	DSP layers & TCP/IP	R.Raghu
		10:30 AM	Mr. VenuNandam	Basics of signals & systems	Venu
		11:00 AM	Ms. Ch. Naga Lakshmi	Introduction to optical communications	Ch.Naga
3	25-4-2017	10:00 AM	Mr. M. Ravi Kiran	Transponder.	M.Ravi
		10:30 AM	Ms. P. Bhagya Sri	SSBSC and PSBSC Techniques (Modulation & Demodulation)	P.Bhagya
		11:00 AM	Mr.Ch JojiBabu	Introduction Digital Design	Ch.Joji
4	26-4-2017	10:00 AM	Mr. P. Satisl.Babu	History of Embedded systems	Satisl
		10:30 AM	Mr. M. verkataaniarnath	Basis of Integrated circuits	M.V.

		11:00 AM	Mr. Andra Babul Reddy	Turnover to Digital Scaphic	B. Reddy
		10:00 AM	Mr. D. Ashokkumar	Turnover to Micro processors & control system	Atch
5	27-4-2017	10:30 AM	Mr. D. Lakshmi murthy	Digital image processing	Muthu
		11:00 AM	Mr. G. Ramana Reddy	Introduction to Computer Maths	Ramana Reddy
		10:00 AM	Mr. K. Raveendra	Basics of Electronic circuitry system	B. Raveendra
6	28-4-2017	10:30 AM	Mr. B. Venkatesh	Introduction to Microsaves	Venkatesh
		11:00 AM	Mr. Y. Chiranjeevi	Tests & Cots Processors.	Y. Chiranjeevi
		10:00 AM	Mr. S. C. Penchalaiah	Analog IC Design	Penchalaiah
7	05-01-2017	10:30 AM	Mr. K. Ravikiran	Introduction to Transponders	K. Ravikiran
		11:00 AM	Mr. S. K. Rasool	Network security & cryptography	Rasool
		10:00 AM	Mr. M. Nagendraprasad	Performance Estimation & Performance	Nagendraprasad
		10:30 AM	Mr. M. D. Abdul Aziz	Probability theory & stochastic process	A. A.
8	05-02-2017	11:00 AM	Mr. Ch. Praveer Kumar	Basics of Antennas	Ch. Praveer

PRINCIPAL



## Department of Electronics and Communication Engineering

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Faculty Orientation Program A.Y 2017-18 Even Sem

S.No	Date	Time	Lecture By	Lecture Topic	Signature
1	16.10.2017	10:00 AM	Mr. T. Rambabu	Introduction to P.D.C	T. Rambabu
		10:30 AM	Mr. J. Prasanth Kumar	Introduction to Antennas & Wave Propagation	J. Prasanth Kumar
		11:00 AM	Mr. D. Sridhar	Chip Designing.	D. Sridhar
		10:00 AM	Mr. R. Radha Krishna	Communication Systems	R. Radha Krishna
2	17.10.2017	10:30 AM	Mr. Md. Ali Baig	Electro statics & Magnetostatics	Md. Ali Baig
		11:00 AM	Mr. D. Venkanna Babu	Introduction to Verilog	D. Venkanna Babu
		10:00 AM	Mr. D. Ram Babu	Types of Control Systems	D. Ram Babu
3	18.10.2017	10:30 AM	Mr. A. Rajesh Naidu	Microprocessors & Interfacing	A. Rajesh Naidu
		11:00 AM	Mr. R. Durga Prasad	Digital signal Processing	R. Durga Prasad
		10:00 AM	Mr. M. Rama Krishna	Turn to G.S & types	M. Rama Krishna
4	18.10.2017	10:30 AM	Mrs. Y. Lavanya	Feed back amplifiers	Y. Lavanya
		11:00 AM	Mr. NVD P. Murthy	Integrated Circuits Design	NVD P. Murthy



5	19.10.2017	10:00 AM	Mr. N. Rama Krishna	Intro to Satellite Communication - Mr. R. B. Lobster	R. B. Lobster
		10:30 AM	Mr. R. L. R. Lokes Babu		
		11:00 AM	Mr. M. Ravi		
6	20.10.2017	10:00 AM	Mr. G. Rajesh Babu	Direct to famous Transponders Basis of WSN transducers & types.	G. Rajesh Babu
		10:30 AM	Mrs. Tenalirupa		
		11:00 AM	Mr. B. China kotaiah		
7	23.10.2017	10:00 AM	Mr. R. Raghu Ram	Design of Digital filters Routing protocols At bridge measurement of inductance	R. Raghu Ram
		10:30 AM	Mr. Venu Nandam		
		11:00 AM	Ms. Ch. Naga Lakshmi		
8	24.10.2017	02:00:PM	Mr. M. Ravi Kiran	Introduction to DSP processors Introduction to E.M.I Parts of AF oscillators MOS Design	M. Ravi Kiran
		2:30 PM	Ms. P. Bhagya Sr.		
		3:00 PM	Mr. Ch. Joji Babu		



**SEMINAR REPORT**

**Organized by** : Computer Science and Engineering  
**Name of the Speaker** : K Sailaja  
**Designation** : Senior Civil Judge  
**Topic** : Social And Legal Issues  
**Venue** : A-302

S.NO	DATE	SESSION	Faculty Attended (%)
1	10/8/2017 to 11/8/2017	2-00 PM -4.00PM	10

**Report**

**1. Report in brief by Organizer / Coordinator / Convener:**

The Speaker Addressed the gathering as National integration is impossible without social justice. Social justice means that in the society the citizens must interact and treat one another on the basis of equality. No one should consider others as inferior to themselves on the grounds of religion, race, color, caste or sex.

The framers of the Constitution of India realized that inequality is a hindrance to realize democracy. Thus securing social justice was identified as the first objective before the nation and was included in the Preamble of the Constitution of India. It stands for elimination of social discrimination on the grounds of caste, creed, color, religion, sex or place of birth.

The Constitution provides a comprehensive list of rights (Part III of the Constitution) of which the right to equality is the most significant for the achievement of social justice. In part IV of the Directive Principles of State Policy also reflects the constitutional strategy for securing social justice.

The right to equality includes:

- equality before law,
- prohibition of discrimination on the grounds of religion, race, sex, place of birth or anyone of them,
- equality of opportunity in matters of public employment and abolition of untouchability.

Further the constitution empowers the state to make special provision for the advancement of any socially and educationally backward classes of citizens or for the scheduled castes and scheduled tribes. Accordingly there is reservation in government services for these classes. Seats are also reserved for scheduled castes and scheduled tribes and Anglo-Indian community in the Central and State Legislatures.

Under the right to freedom, the citizens can pursue any business, trade or profession or career.

Speaker also stated as Engineering is not only applying scientific laws and principles to technical problems. It is focused on improving the lot of society, and as such, it brings engineers into the mainstream of business and industry. Almost all entry-level engineers become involved, at least tangentially, with situations that call for some understanding of the law and situations that call for ethical judgments. Therefore, this chapter presents a brief overview of some legal and ethical issues in engineering. With topics as broad as law and ethics we can only scratch the surface, so we have chosen to focus on those issues that are most pertinent to engineering design. The following are examples of where a design engineer might be concerned with legal and ethical issues:

Preparing a contract to secure the services of a product data management firm.  
Reviewing a contract to determine whether a contractor who built an automated production facility has satisfactorily fulfilled the terms of a contract.  
Deciding whether it is legal and ethical to reverse engineer a product.  
Managing a design project to avoid the possibility of a product liability suit.  
Protecting the intellectual property created as part of a new product development activity.  
Deciding whether to take a job with a direct competitor that is bidding on a contract in the area where you are now working.

The law is a formalized code of conduct describing what society feels is the proper way to behave. In other words, laws reflect what society values. As society evolves, its attitude toward behavior changes, and the laws change as well. Also, the advances in technology create new ethical issues. Ethics is the study of human conduct that lays out the moral ground rules. It is intimately related to the values of society. Thus, 826 chapter 18: Legal And Ethical Issues I n Engineering Design laws and ethics, while distinct, are not independent. Ethical conduct is the behavior desired by society that is separate from the minimum standards of the law. For example , making a defective product despite taking all due care may subject you to product liability law, but it is not generally considered unethical

## **2. Photos:**

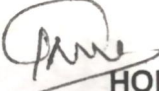


## **3. Feedback from Faculty:**

Faculty are undergone through the interactive session and inspired by the speaker to self develop the Various android apps.

## **4. Remarks from Resource Person:**

He asked to practice regularly by faculty for the sake of Organization as well as for the benefit of faculty.

  
HOD  
Dr.G. Srinivasa Rao

  
PRINCIPAL





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COLLEGE OF ENGINEERING**  
(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : K Sailaja

**Designation** : Senior Civil Judge

**Topic** : Social And Legal Issues

**Venue** : A-302

S.NO	NAME OF THE FACULTY	SIGNATURE
1	Dr. G Srinivasa Rao	G. Srin Rao
2	Mr. K Kiran	K Kiran
3	Mr. P Chakradhar	P. Chakradhar
4	Ms. S Nagavali	S. Nagavali
5	Mr. P Sudhakar	P. Sudhakar
6	Mr. O Shiv Bhagawan	O. Shiv Bhagawan
7	Mr. B Yedukondalu	B. Yedukondalu
8	Mr. M Vijaya Raju	M. Vijaya Raju
9	Mr. S Pavan Sai Kumar	S. Pavan Sai Kumar
10	Mr. P Hari Nrayana	P. Hari Nrayana



**WORKSHOP REPORT**

Organized by : Computer Science and Engineering  
Name of the Speaker : Dr P V R D Prasad Rao  
Designation : Professor, Associate Dean ( Alumni, Placements and Progression) at  
KL University  
Topic : Awareness on Innovative project management  
Venue : A-302

S.NO	DATE	SESSION	Faculty Attended (%)
1	20/12/2017 to 21/12/2017	2-00 PM -4.00PM	11

**Report**

**1. Report in brief by Organizer / Coordinator / Convener:**

The Speaker Addressed the faculty & said as His goal is to explain the key ways that creativity and innovation can be used to differentiate you as a project manager, advance your career, increase your effectiveness, and transform you into a project hero instead of merely a project manager!

First, a WARNING: This is one of those 'soft' project management topics. It does not deal with 'hard' content. I will not be mentioning equations, methodologies, formulas, or process diagrams. This subject involves the softer concepts, such as attitude, behavior, enthusiasm. Hopefully I can still provide some concrete examples and suggestions to help plant the seed so that you can begin to incorporate these strategies into your day to day project management role. I will include examples and techniques within key project management process areas, where creativity can be extremely valuable.

Creativity and innovation are rare in the field of project management. For example, if you look at an organization as a whole, it is within IT that you are likely to find the most structured, logical, and 'left brained' people. Then, if you consider the entire IT department, my observation is that project managers are frequently the most organized, disciplined, and structured resources within IT. I like to call it the left of the left brained population

## 2. Photos:



## 3. Feedback from students:

Faculty are undergone through the interactive session and got solutions for the plenty of their doubts and satisfied & showed interest towards the creative project management.

## 4. Remarks from Resource Person:

He asked to practice these regularly by faculty for the sake of Organization as well as for the benefit faculty and students. Resource person has proclaimed the importance of self updating and holding of our beautiful ideas for the development.

HOD

**PRINCIPAL**





# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Dr P V R D Prasad Rao

**Designation** : Professor, Associate Dean ( Alumni, Placements and Progression) at KL University

**Topic** : Awareness on Innovative project management

**Venue** : A-302

S.NO	NAME OF THE FACULTY	SIGNATURE
1	Dr. B Raja Srinivasa Reddy	
2	Dr. A Daveedu Raju	
3	Mr. K Kiran	
4	Mr. P Chakradhar	
5	Ms. S Nagavali	
6	Mr. P Sudhakar	
7	Mr. O Shiv Bhagawan	
8	Mr. B Yedukondalu	
9	Mr. M Vijaya Raju	
10	Mr. A Srinivasa Rao	
11	Mr. G Prasanth Kumar	



**WORKSHOP REPORT**

Organized by : Computer Science and Engineering  
Name of the Speaker : Dr Ashok Vardhan  
Designation : Professor  
Topic : Seminar on professional and social relations in society  
Venue : A-302

S.NO	DATE	SESSION	Faculty Attended
1	15/02/2018 to 16/02/2018	2-00 PM -4.00PM	14

**Report**

**1. Report in brief by Organizer / Coordinator / Convener:**

The Speaker Addressed the gathering A society is mainly defined as a collection of individuals. It consists of individuals belonging to male and female and different age groups. The sex ratio in the society strike balance. The vital activities such as the birth, growth and death and going on in the society as long as the society is there. The immigration and emigration and the birth and death of the population also strike balance of the population in the society. But the improvement in the medical facilities increased the average life span of the individual there by lead to the population explosion.

A common geographical area: A particular society has been demarcated by the other with natural or artificial boundaries. The natural boundaries such as the rivers, mountain ranges or forests, canals etc. The artificial boundaries are there demarcated by political settlements. The people of the area share the resources in common and participate to reach the common goals of population. The people develop unity, we feeling, integrity oneness and collection consciousness.

Variety of interactions: The society is full of interactions and the different social processes and going on in the society. The people come face to face and interact among themselves. People share certain interests, attitudes, aptitudes, traditions, customs, values, objectives and mores. The people of the society depend upon each other for their survival. The division of labour among the individuals exists and the functions assigned to them are performed. This develops functional inter relationship among the members of the society.

Feeling of Solidarity: Since individuals of the society occupy a common territory, common customs and traditions common values, common history common cultures, self contained interdependence on each other obviously causes oneness and we feeling and develops feeling of solidarity among themselves. Though occasionally interact with other societies, they never lore their identity and remains united as long as their society survives.

Total culture: Each society has its own culture and the individual relationships are organized and structured by the culture. Because of commonness in the culture content, traditional of the society unite together. The society will be differentiated by the other society because of its unique culture. Culture is present in human society and the same is absent in animal society.

Social Organization: Members of a society are socially organized. Society itself has a structure and the important components and elements of social structure are norms, rules, statuses, power, authority, groups, associations and institutions. The norms are the important which give it stability, order and structure to human society that without them social interaction would be

difficult and chaotic. Organization of human society is maintained with the help of norms and institutions that pervade the society. Social organization helps maintain society in social equilibrium.

Functional differentiation: All the individuals in human society never perform similar activities and functions. They perform different functions depending upon their sex, age, interest, abilities, skills and other qualifications. There is more and more specialization in each work and are expected to do their work allotted to them. Thus several persons work on a single activity. There is division of labour depending upon sex and age.

He also exposes the professional mannerism and its impact on the public and society.

## **2. Photos:**

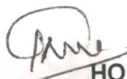


## **3. Feedback from Faculty:**

Faculty told that as they are very happy because of justified & proven moral values and relations importance and they also realized, convinced.

## **4. Remarks from Resource Person:**

He asked teach the faculty to regularly show the professional mannerism and teach the moral values, public relations and family importance. He also stress the leadership quality and commitment to wards the responsibility of education and job

  
HOD  
Dr.G. Srinivasa Rao

  
PRINCIPAL





# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK, Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

Organized by : Computer Science and Engineering  
Name of the Speaker : Dr Ashok Vardhan  
Designation : Professor  
Topic : Seminar on professional and social relations in society  
Venue : A-302

S.NO	NAME OF THE FACULTY	SIGNATURE
1	Dr. G Srinivasa Rao	
2	Dr. B Raja Srinivasa Reddy	
3	Dr. A Daveedu Raju	
4	Mr. K Kiran	
5	Mr. P Chakradhar	
6	Ms. S Nagavali	
7	Mr. P Sudhakar	
8	Mr. O Shiv Bhagawan	
9	Mr. B Yedukondalu	
10	Mr. M Vijaya Raju	
11	Mr. K N V Devendra Kumar	
12	Ms. V Sai Bhanu	
13	Mr. K.V Varaprasad	
14	Ms. G Poornima	



# RAMACHANDRA COLLEGE OF ENGINEERING

NH-5, Bypass Road, Vatluru (V), Eluru - 534 007, (A. P.)  
(Approved by AICTE, New Delhi, Affiliated to JNTUK, Kakinada)

## DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

### Department Circular

Ref: RCEE/CSEO/CIR/2016-'17/003

Date : 14-05-2016

### FDP Programme

The entire teaching faculties who have been dealing subjects to the II/III/IV year first semester of CSE are hereby informed that, There will be a Faculty Development Programme (FDP) scheduled from **18-05-2016**. The duration of the FDP will be 1½ hour and we got our slot between **10:30 AM and 12:00 Noon**. During 1½ hour, either of three faculty members are expected to deliver a lecture. Each faculty must deliver a lecture for 30 min in their allocated subject. During the lecture hour remaining faculty members must be the audience and they could assess the performance. All the faculty members are requested to observe the given schedule for FDP.

Information noted  
4/5/16  
11/4/16

(K GOPALA REDDY)  
HOD : CSE

S No	Name of the Faculty	Subject	Date	Time	Signature
1	Mr. B Yedukondalu	CP	18-05-2016	10:30 AM - 11:00 AM	
2	Ms. S Nagavali	DS	18-05-2016	11:00 AM - 11:30 AM	
3	Mr. B Prasad Babu	OS	18-05-2016	11:30 AM - 12:00 Noon	
4	Mr. P Chakradhar	OOP	19-05-2016	10:30 AM - 11:00 AM	
5	Mr. A Chiranjeevi	STM	19-05-2016	11:00 AM - 11:30 AM	
6	Mr. Y P Narasimha Rao	DS	19-05-2016	11:30 AM - 12:00 Noon	
7	Mr. K Kiran	CNS	20-05-2016	10:30 AM - 11:00 AM	
8	Mr. P Sudhakar	UML&DP	20-05-2016	11:00 AM - 11:30 AM	
9	Ms. M S Radha	DC	20-05-2016	11:30 AM - 12:00 Noon	
10	Mr. O Shiv Bhagawan	MC	21-05-2016	10:30 AM - 11:00 AM	
11	Mr. Y Nagendra Kumar	CD	21-05-2016	11:00 AM - 11:30 AM	
12	Mr. Ch Raghu Kumar	CAO	21-05-2016	11:30 AM - 12:00 Noon	
13	Mr. K Venkatesh	DS	23-05-2016	10:30 AM - 11:00 AM	
14	Mr. K Gopi	CP	23-05-2016	11:00 AM - 11:30 AM	
15	Mr. G Vijaya Raju	DLD	23-05-2016	11:30 AM - 12:00 Noon	
16	Mr. N Venu	DLD	24-05-2016	10:30 AM - 11:00 AM	
17	Mr. U Gangadhar	MEFA	24-05-2016	11:00 AM - 11:30 AM	
18	Mr. T Srinivasa Rao	MFCS	24-05-2016	11:30 AM - 12:00 Noon	
19	Dr. A Daveedu Raju	H&Bd	25-05-2016	10:30 AM - 11:00 AM	
20	Dr. B R Srinivasa Reddy	PPL	25-05-2016	11:00 AM - 11:30 AM	
21	Mr. K Gopala Reddy	DBMS	25-05-2016	11:30 AM - 12:00 Noon	



Eluru,

02-06-2016

To  
The Principal  
Ramachandra College of Engineering  
Eluru

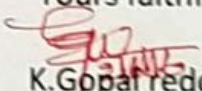
Sir,

**Sub :** Permission to conduct Workshop for Technical Staff – Reg.

I, K.Gopal Reddy working as Professor&HOD of CSE Department planning to conduct "**C Programming**" training by K.Kiran for Technical Staff in our College on 09-05-2016. Hence, I request you to grant me permission to conduct the program.

Thanking you, Sir

Yours faithfully,

  
K.Gopal reddy

HOD-CSE

21/06/16  
Approved &  
forwarded to  
HOD-CSE





7-05-2016

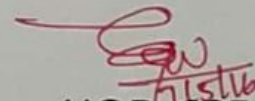
Circular

Rcee/Cse/technical/skill

This is to inform that CSE Department is going to organize skill upgradation in C programming on 09-05-2016. In this regard, all the technical staff should attend without fail.

Venue:DS lab

Time:9:30a.m

  
HOD-CSE



# RAMACHANDRA COLLEGE OF ENGINEERING

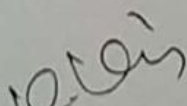
Approved By AICTE, NEW DELHI, Affiliated to JNTUK-Kakinada. NH-5 By-Pass Road, Vatluru(V), Eluru, 534007 A.P

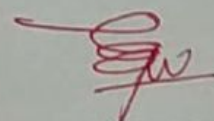
Date: 9-05-2016

Department Of Computer Science and Engineering

Attendance Sheet For Add On Programs

S.NO	NAME OF TECHNICAL STAFF	SIGNATURE
1	Mr. CH VENKATESH	CH Venkatesh
2	Mr. Y AMARNATH	Y. Amarnath
3	Ms. M NAGA SARITHA	M. Naga Saritha
4	Ms. J MANJULA	Manjula
5	Mr. GVENKATA SAI KRISHNA	G. Venkata Sai Krishna
6	Ms. T V PRASANNA	T.v.prasanna
7	Mr. P KISHORE	P. Kishore
8	Mr. R. SATEESH	R. Sateesh

  
FACULTY

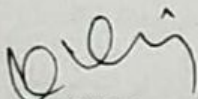
  
HOD-CSE

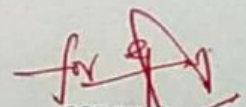
Date: 9-05-2016

COMPUTER PROGRAMMING

TODAY DAIRY

S.NO	TOPIC COVERED
1	Files
2	Enum explains

  
FACULTY

  
CSE-HOD



# Why files are needed?

- When a program is terminated, the entire data is lost. Storing in a file will preserve your data even if the program terminates.
- If you have to enter a large number of data, it will take a lot of time to enter them all. However, if you have a file containing all the data, you can easily access the contents of the file using few commands in C.
- You can easily move your data from one computer to another without any changes.

## Types of Files

When dealing with files, there are two types of files you should know about:

1. Text files
2. Binary files

### 1. Text files

Text files are the normal .txt files that you can easily create using Notepad or any simple text editors.

When you open those files, you'll see all the contents within the file as plain text. You can easily edit or delete the contents.

They take minimum effort to maintain, are easily readable, and provide least security and takes bigger storage space.

### 2. Binary files

Binary files are mostly the .bin files in your computer.

Instead of storing data in plain text, they store it in the binary form (0's and 1's).

They can hold higher amount of data, are not readable easily and provides a better security than text files.

## File Operations

In C, you can perform four major operations on the file, either text or binary:

1. Creating a new file
2. Opening an existing file
3. Closing a file
4. Reading from and writing information to a file

## Working with files

```
enum boolean check;
```

Here, a variable `check` of type `enum boolean` is created.

Here is another way to declare same `check` variable using different syntax.

```
enum boolean  
  
{  
  
    false, true  
  
} check;
```

## **Example: Enumeration Type**

```
#include <stdio.h>  
  
enum week { sunday, monday, tuesday, wednesday, thursday, friday, saturday };  
  
int main()  
{  
    enum week today;  
    today = wednesday;  
    printf("Day %d",today+1);  
    return 0;  
}
```

### **Output**

Day 4

## **Why enums are used in C programming?**



Eluru,

08-11-2016

To  
The Principal  
Ramachandra College of Engineering  
Eluru

Sir,

**Sub :** Permission to conduct Workshop for Technical Staff – Reg.

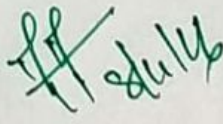
I, K.Gopal Reddy working as Professor&HOD of CSE Department planning to conduct “**Java Programming**” training by M S Radha M M for Technical Staff in our College on 11-11-2016. Hence, I request you to grant me permission to conduct the program.

Thanking you, Sir

Yours faithfully,

  
K.Gopal Reddy

HOD-CSE

  
Approved & forwrd  
to HOD





8-11-2016

Circular

Rcee/Cse/technical/skill

This is to inform that CSE Department is going to organize skill upgradation in Java programming on 11-11-2016. In this regard, all the technical staff should attend without fail.

Venue:OOPS lab

Time:9:30a.m

  
HOD-CSE



# RAMACHANDRA COLLEGE OF ENGINEERING

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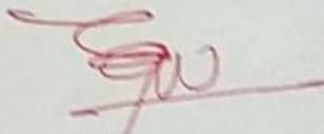
Date: 11/11/2016

Department Of Computer Science and Engineering

Attendance Sheet For Add On Programs

S.NO	NAME OF TECHNICAL STAFF	SIGNATURE
1	Mr. CH VENKATESH	CH Venkatesh
2	Mr. Y AMARNATH	Y. Amarnath
3	Ms. M NAGA SARITHA	M. Naga Saritha
4	Ms. J MANJULA	Manjula
5	Mr. GVENKATA SAI KRISHNA	G. Venkata Sai Krishna
6	Ms. T V PRASANNA	T.V. prasanna
7	Mr. P KISHORE	P. Kishore
8	Mr. R. SATEESH	R. Sateesh

H. S. Reddy  
FACULTY

  
HOD-CSE

Date: 11-11-2016

## JAVA PROGRAMMING

### TODAY DAIRY

S.NO	TOPIC COVERED
1	AWT
2	events
3	Event handling
4	Awt Examples

H.S. Reddy  
FACULTY 11/11/16

SPW  
CSE-HOD



## Technical staff Skills Report

Organized by : Computer Science and Engineering  
Name of the Speaker : M S Radha M M  
Designation : Assit.professor-RCE  
Course : Java Programming  
Venue : Computer Lab6/OOPS Lab

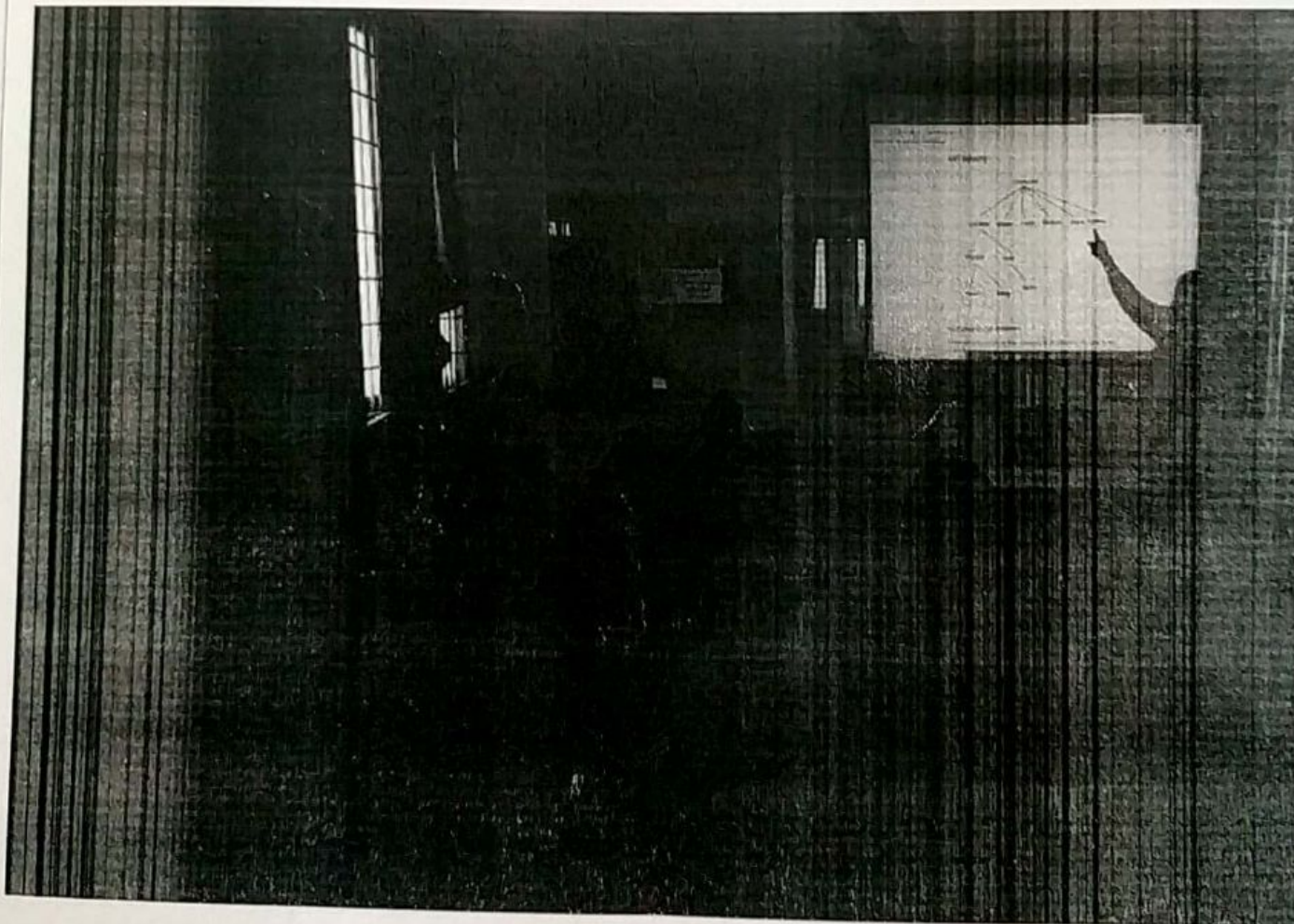
S.NO	DATE	SESSION
1	11-11-2016	9.30 A.M -4.40 PM

### **Report**

#### **1. Report in brief by Organizer / Coordinator / Convener:**

The Speaker Addressed about AWT, Events, Event Handling & AWT Examples

#### **2. Photos:**



*P. Radha*  
Coordinator

*[Signature]*  
HOD





# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

## DEPARTMENT OF MECHANICAL ENGINEERING

A Report On

ICRDEST-2017

INTERNATIONAL CONFERENCE

Functional Materials, Characterization,  
Solid State Physics, Power, Thermal &  
Combustion Energy. (FCSPTC)

On 7<sup>th</sup> & 8<sup>th</sup> April - 2017.



Coordinated by:

- 1) Mr. Patcha Khan,
- 2) Mr. V. Nagarjuna

Under the guidelines of:

Dr. Bh. Varaprasad





# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

Format:9014/2

## International Conference Report

**Organized by** : Department of Mechanical Engineering,  
Ramachandra College of Engineering-Eluru

**Names of the Speakers** : 1. Prof. Konstantin Kazakov  
2. Dr. K. Sahayaraj  
3. Dr.V.Ganesh  
4. Dr.P.Suresh

**Venue** : Dr.APJ Abdul Kalam Auditorium, Ramachandra College of Engineering, Eluru

**Date & Time** : 7<sup>th</sup> & 8<sup>th</sup> April, 2017 (10:00 A.M to 05:00 P.M)

	No of People Attended
Scholars from other countries	04
Scholars from other states	50
Scholars within the state	66
<b>Total Attendance</b>	<b>120</b>

### Report

#### 1. Report in brief by Organizer / Coordinator / Convener:

The prestigious international conference organized by the Ramachandra College of Engineering, Eluru successfully by attracting international scholars and professors. The aim of the conference was to provide a platform for academicians, research scholars, practicing managers and students to develop, contribute and present technical papers in Engineering field. The event helped in bringing together experts from industry and academia to meet, discuss, and share views and experiences on various recent technical topics and challenges of today's world.

**Prof.Konstantin S. Kazakov** delivered key note on "*Dynamic Thermal Wave Response And Propagation Through Building Structures Using Infinite Elements In Time And Frequency Domain*". Later **Dr.V.Ganesh, DST,SERB young scientist award recipient** has delivered key note address on "*Nitride Semi Conductors For Hydrogen Evolution Application Through Photo Electrochemical Water Splitting*" on the inaugural day.

In the second day **Dr.Suresh Pittala, Post Doc at IISC, Bangalore**, Delivered key note address on "*Room Temperature Multiferroic Materials.*" **Dr.K.Sahaya Raj, Director Crop Protection Research Centre**, Delivered key note address on "*Seaweed Usages And Advantages*".

About 120 participants including faculties of Ramachandra college of engineering benefited from the above key note addresses. All these technical sessions were chaired by senior faculty members of the institute.



## 2. Photos:







### 3. Feedback from students/Stakeholders:

- Dignitaries present during 2-Day International Conference highly appreciated the efforts of organizing committee.
- Participants were much impressed by the organizing the conference even this is the 1st International Conference for the institute.
- Participants appreciated the ambiance of the campus, institute building, Conference kit, quality of presentation, schedule and arrangement as well as seminar hall and facilities in the hall.
- Dignitaries and Participants appreciated by hospitality during both days of the conference.

### 4. Remarks from Resource Person:

- **Resource Persons** were also much impressed by the organizing the conference by the organizing committee even this is the 1st International Conference for the institute.
- **Resource Persons** appreciated efforts of the participants in presenting research papers.
- Dignitaries highly appreciated the hospitality and arrangements by the management during both days of the conference.



# Press Note

## పరిశోధనాసంఘంపై ఆసక్తి పెంచుకోవాలి

దేశంలోని వివిధ విశ్వవిద్యాలయాలలో ఉన్న పరిశోధనాసంఘాలను ప్రోత్సహించి, వాటిని మరింత బలోపేతం చేయాలి అని ప్రభుత్వం కోరుకుంటుంది. పరిశోధనాసంఘాలు విద్యార్థులకు అవసరమైన అభివృద్ధిని తెలియజేయడంలో సహాయపడతాయి. వీరిని ప్రోత్సహించి, వాటిని మరింత బలోపేతం చేయాలి అని ప్రభుత్వం కోరుకుంటుంది.



పరిశోధనాసంఘాలను ప్రోత్సహించి, వాటిని మరింత బలోపేతం చేయాలి అని ప్రభుత్వం కోరుకుంటుంది. పరిశోధనాసంఘాలు విద్యార్థులకు అవసరమైన అభివృద్ధిని తెలియజేయడంలో సహాయపడతాయి. వీరిని ప్రోత్సహించి, వాటిని మరింత బలోపేతం చేయాలి అని ప్రభుత్వం కోరుకుంటుంది.

## స్వచ్ఛ నాణ్యత తీర్మానం ఆలోచించాలి


స్వచ్ఛ నాణ్యత తీర్మానం ఆలోచించాలి. స్వచ్ఛ నాణ్యత తీర్మానం ఆలోచించాలి. స్వచ్ఛ నాణ్యత తీర్మానం ఆలోచించాలి. స్వచ్ఛ నాణ్యత తీర్మానం ఆలోచించాలి.



స్వచ్ఛ నాణ్యత తీర్మానం ఆలోచించాలి. స్వచ్ఛ నాణ్యత తీర్మానం ఆలోచించాలి. స్వచ్ఛ నాణ్యత తీర్మానం ఆలోచించాలి. స్వచ్ఛ నాణ్యత తీర్మానం ఆలోచించాలి.

## కామచంద్ర ఇంజనీరింగ్ కళాశాలలో అంతర్జాతీయ సదస్సు

కామచంద్ర ఇంజనీరింగ్ కళాశాలలో అంతర్జాతీయ సదస్సు. కామచంద్ర ఇంజనీరింగ్ కళాశాలలో అంతర్జాతీయ సదస్సు. కామచంద్ర ఇంజనీరింగ్ కళాశాలలో అంతర్జాతీయ సదస్సు.



కామచంద్ర ఇంజనీరింగ్ కళాశాలలో అంతర్జాతీయ సదస్సు. కామచంద్ర ఇంజనీరింగ్ కళాశాలలో అంతర్జాతీయ సదస్సు. కామచంద్ర ఇంజనీరింగ్ కళాశాలలో అంతర్జాతీయ సదస్సు.

Signature of Organizer

Signature of HEAD & DEAN

Signature of Principal



5

23/1/17,  
EWSU.

To  
The principal  
Rama chandra college of Engineering  
EWSU.

Sub: Regarding "Request for permission of conducting a workshop  
related to "Non-destructive testing" on February 8<sup>th</sup> & 9<sup>th</sup> by  
"FUTURE NOT, Vijayawada" request.

Respected Sir,

I K.P.V.S.R Vinay kumar co-ordinator of "A Two Days Work  
shop on application of NDT For Industries" and Asst.  
professor in Dept of Mechanical Engineering want to inform  
to your kind notice about the interaction of the above issue  
with the future NDT, Vijayawada ANAGERNG PARTNER. G. VIJAY  
kumar on December 16<sup>th</sup> 2016 and they have confirmed the  
above dates for conducting workshop to the final year Mechanical  
engineering students. In addition to the workshop, the future  
NDT trainers will also give tips regarding NDT technical &  
employability skills which are helpful to the students for their  
career prospects in their future endeavours.

Workshop. for  
two days.

∴ 300/- per  
student.

Refreshments: 50/- enclosed.

∴ 350/- per student.

Thanking you Sir  
This workshop for  
IV Year students.  
Kindly do needful  
forward to Principal Sir  
(M.E. HOD).  
23/1/2017

put this

23/1/17

Yours Humbly,  
K.P.V.S.R Vinay Kumar  
(K.P.V.S.R Vinay Kumar)





# **FUTURE NDT**

(An ISO 9001:2015 Certified Organization)

Ph: +91 7382755986. Email: vijay@futurendt.com; Website: www.futurendt.com;

TO

**Mr. KPVSR VINAY KUMAR**

**Assistant Professor- Mech Dept)**

Ramachandra College of Engineering,  
Eluru, West Godavari District.

## **QUOTATION for WORKSHOP on NDT**

**BY FUTURE NDT,**

(An ISO 9001:2015 Certified Organization)

#11-293,

2<sup>ND</sup> FLOOR

KRISHNA NAGAR,

NEAR AUTONAGAR CHECK POST,

BESIDE ANDHRA BANK,

VIJAYAWADA-7 AP.

Head office: #11-293, 2nd Floor, Krishna Nagar, Beside Andhra Bank, Vijayawada-07, A.P.

Branch Office: Plot No: 69, Vennela Residency, Navodaya Colony, Near Kunta Park, Kukatpally-72





# FUTURE NDT

(An ISO 9001:2015 Certified Organization)

Ph: +91 7382755986. Email: vijay@futurendt.com; Website: www.futurendt.com;

## WORKSHOP SCHEDULE

### INTRODUCTION:

- ❖ About Our Organization.
- ❖ Brief Description about Types of Welding's & Different Defects In Welding.
- ❖ Importance of Quality Testing's on Materials.
- ❖ Types of Quality Testing's.
  - Destructive Testing.
  - Non- Destructive Testing.
  - Quality Assurance/ Quality Control.
- ❖ Differences b/t Destructive Testing & NDT.
- ❖ Brief Description about Different Standards.
  - ISNT (Indian Society of NDT)
  - ASNT (American Society of NDT)
  - BINT or PCN (British Standard)
- ❖ Overview of different NDT Methods.
  - Liquid Penetrate Testing.
  - Magnetic Particle Testing.
  - Radiography Testing.
  - Ultrasonic Testing.

### Liquid Penetrate Testing.

- ❖ Introduction of LPT.
- ❖ Advantages & Disadvantages of LPT.
- ❖ Applications of LPT.
- ❖ Chemicals used in performing LPT.
- ❖ Steps involved in performing LPT.
- ❖ Types of LPT Testing's.
- ❖ Codes & Standards for conducting LPT Test & Report writing.
- ❖ Practical Lab on LPT.

### Magnetic Particle Testing.

- ❖ Introduction of MPT.
- ❖ Advantages & Disadvantages of MPT.
- ❖ Applications of MPT.
- ❖ Chemicals used in performing MPT.
- ❖ Steps involved in performing MPT.
- ❖ Types of MPT Testing's.
- ❖ Codes & Standards for conducting MPT Test & Report writing.
- ❖ Practical Lab on MPT.

### Radiography Testing.

- ❖ Introduction of RT.
- ❖ Advantages & Disadvantages of RT.
- ❖ Applications of RT.
- ❖ Steps involved in performing RT.
- ❖ Codes & Standards for conducting RT Test & Report writing.
- ❖ Practical Lab on RT.

### Ultrasonic Testing.

- ❖ Fundamentals of Ultrasonic Testing.
- ❖ Advantages & Disadvantages of UT.
- ❖ Applications of UT.
- ❖ Modes of Sound Energies.
- ❖ Sound wave velocities in Different Materials.
- ❖ Types of Probes and its functions & Applications. UT machine parts specifications.
- ❖ Calibration of standard blocks (V1, V2).
- ❖ Methods for drawing DAC using Reference blocks. Conducting UT tests on different specimens as per BINT standards
- ❖ Acceptance & Rejection criteria using DAC curve.
- ❖ Analysis of different defects founded in different specimens.

### CONCLUSION

- ❖ Future scope on advanced certifications over Quality Department.
  - ASNT Level III.
  - PCN Level II & III.
  - CSWIP.
  - API.
  - NACE.
  - T, K, Y- Joints.
  - BARC Level I & II.
  - RSO.
  - IRATA.
- ❖ Brief description about applications of NDT in Mechanical Industries.
- ❖ Scope of doing projects on NDT.
- ❖ Career opportunities in NDT.
- ❖ Abroad opportunities with High Salaries.

Head office: #11-293, 2nd Floor, Krishna Nagar, Beside Andhra Bank, Vijayawada-07, A.P.  
Branch Office: Plot No: 69, Vennela Residency, Navodaya Colony, Near Kunta Park, Kukatpally-72



## DEPARTMENT OF MECHANICAL ENGINEERING

A Report on Interdepartmental workshop on "A Two Day Hands-On workshop of NDT & its Applications in Mechanical Industries" in association with FUTURE NDT, VIJAYAWADA and organized by Students of Mechanical Engineering

8th & 9th February 2017

### INAUGURATION:



Gathering of dignitaries onto the dais and prayer by Ms.Bhavani, Ms.Parvin and Ms.Mounika

The interdepartmental workshop inaugurated with prayer song by the dignitaries, firstly the chief guest, Sri G. Vijay Kumar, Managing Partner to Future NDT, Vijayawada, followed by Sri Revanth garu and Sri Mohammad garu, Future NDT Technical Team Members, Vijayawada, followed by Dr. Dola Sanjay S, Principal, Ramachandra college of Engineering, followed by K P V S R Vinay Kumar, Assistant Professor in Department of Mechanical Engineering & Coordinator of the workshop, followed by Dr.Bh.V Prasad, Dean of MBA and Sri M.Patcha Khan, Head of the Mechanical Engineering Department & Convener of the workshop.



5



Co-Ordinator

*K. R. Vinay Kumar*  
(KPVSR VINAY KUMAR)

Convener

*M. K.*  
(M. PACHA KHAN)

Principal

*Dr. Dola Sanjay S.*  
(Dr. DOLA SANJAY.S)



# RAMACHANDRA COLLEGE OF ENGINEERING

NH 5 Bypass Road, VATHURU, DIST. TIRURU - 534007 (A.P.)  
(Approved by AICTE, New Delhi and Affiliated to JNT University, Kakinada)

## DEPARTMENT OF MECHANICAL ENGINEERING

7-9-2017.

It is hereby informed to all the staff members of mechanical engineering and all the students of IV B.Tech that there is a workshop on entrepreneurship jointly organised by RCE and NSIC on 9-9-2017 at 10:00 AM, Venue: mechanical engineering e-class room.

HOD/ME

S.NO	YEAR/ SECTION	Signature of The Teacher
1	IV A	
2	IV B	

S.No	Name of the faculty	Signature	S.No	Name of the faculty	Signature
1	Dr. Md.Abid Ali		15	Shri.A.Rahul Kumar	
2	Shri.B.Suresh Babu		16	Shri.Bhavanarayan .K	
3	Shri.C.Siva sankar		17	Shri.K.sudhakar babu	
4	Shri.M.Pacha khan		18	Shri Y.Venkatesh	
5	Shri.Chitti babu . G		19	Shri.M.Ekambaram	
6	Shri.K.P.V.S.R Vinay kumar		20	Shri.Sk.Abdul Azeez	
7	Shri.B.Raja Kumar				
8	Shri.Raffi Mohammed				
9	Shri.V Nagarjuna				
10	Shri.M Vimal Teja				
11	Shri.B.Sudhakara Rao				
12	Shri.K.Radha Krishna				
13	Shri.B.Narendra Kumar				
14	Shri.V.Sai Surendra				





# RAMACHANDRA COLLEGE OF ENGINEERING

NH-5 Bypass Road, Vallur (V), SRIPUR - 535502 (A.P.)  
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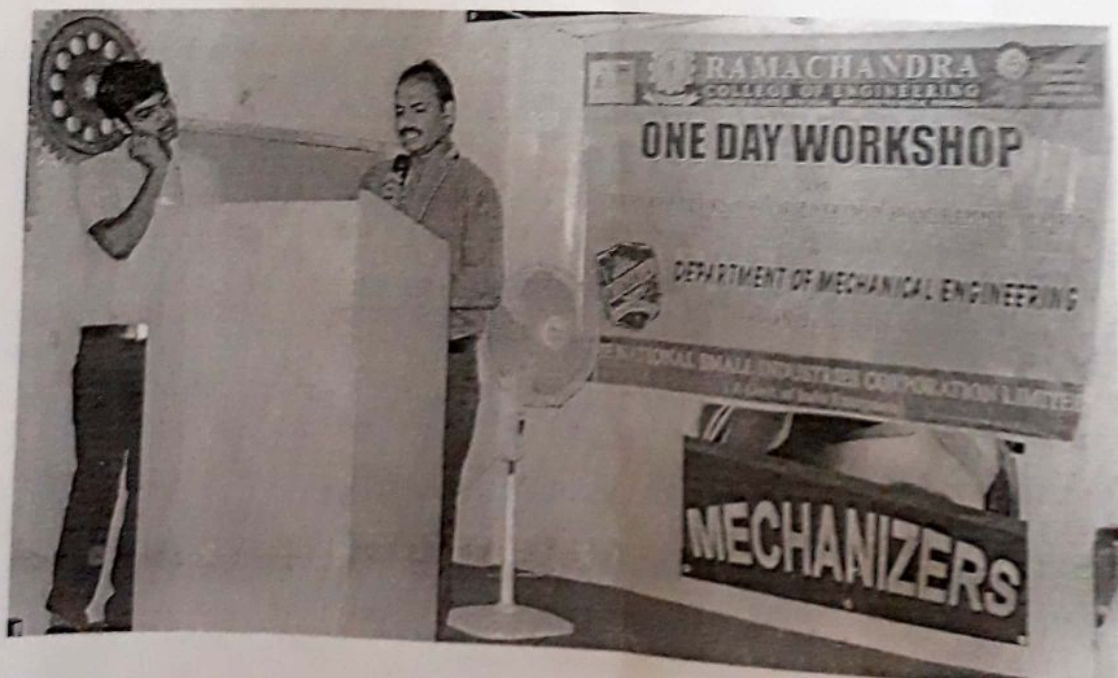
## DEPARTMENT OF MECHANICAL ENGINEERING

11-9

Dr. Bh.V. Prasad said in his speech that there are lots of opportunities in SMEs in the field of tourism, software, food, textiles and horticulture industries. Utilize the opportunity given by the NSIC.



Program coordinator has given the importance of the program.







# RAMACHANDRA COLLEGE OF ENGINEERING

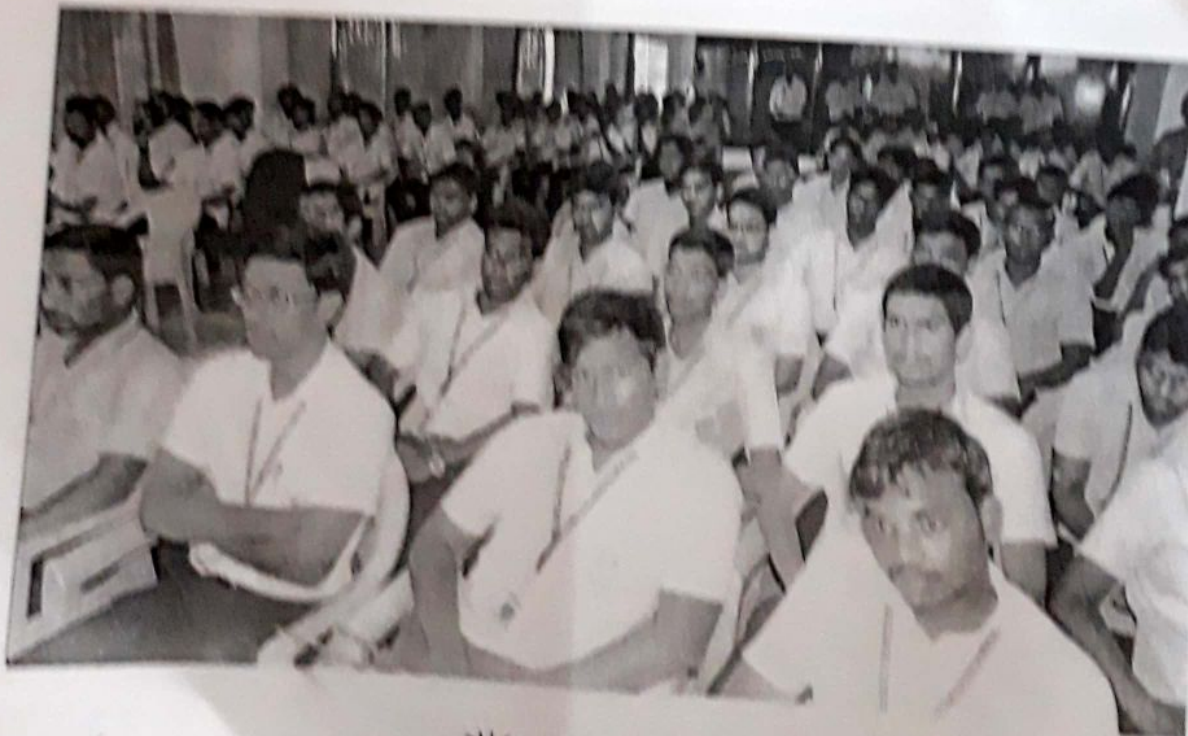
Old S. Byrappa Road, Vallur - 630007 (A.P.)  
(Approved by AICTE, New Delhi and Affiliated to JNTU University, Kakinada)

## DEPARTMENT OF MECHANICAL ENGINEERING

Today Mr. G. Sudarshan, takeover the session and gave the importance and created awareness on Entrepreneurship.



All department HoDs, Staff and 100 students of final year ME were participated in this program.



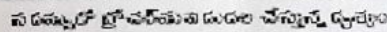
CP  
COORDINATOR

MS  
HOD

PRINCIPAL



## 5

[illegible]

Signature of Principal  
page 5 of 5



B-1

## Gmail

COMPOSE

ONE DAY WORKSHOP- REG

Inbox x

Inbox (46)

Starred

Important

Sent Mail

Drafts (72)

All Mail

Spam (116)

Trash

Circles

ADHAAR

CBU

faculty +

HEAT TRANSFER

IM

INDIAN

new login

new res

NIEL

Personal

Gmail

Chitti Babu <cb.gaddem@gmail.com>  
to nitschy

Dear sir /madam,

Ref: Advertisement in the Hindu news paper and Discussions over tele  
DT: 23/05/2016.

Sub: - One day workshop at our College-reg

It is our privilege to invite you to conduct a **ONE DAY WORKSHOP** at our college campus. We request you to go through details given below. The proposed dates to conduct workshop at our college is **July 11<sup>th</sup> 2016**. We are ready to do arrangements on any day of above said dates. Please let us know the date as per your convenience.

CO-ORDINATOR FOR THIS WORKSHOP  
G.CHITTI BABU,

Assistant professor,

Department Of Mechanical Engineering,  
RCEE, VATLURU,  
ELURU.

Contact Number: 9030068379

Email: cb.gaddem@gmail.com

5-13

## NSIC-Technical Services Centre, Hyderabad

### EOP programme schedule

S.No	Time	Programme Details
1	10.00 AM to 10.30 AM	Registration
2	10.30 AM to 12.00 Noon	Introduction to NSIC activities and Concept of Entrepreneurship.
3	12.00 Noon to 12.15 PM	Tea Break
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#### Please contact:

U Venkatachalapathi  
General Manager  
NSIC-Technical Services Centre,  
Hyderabad

Ph No: 040-27124597, 9959726692





# RAMACHANDRA COLLEGE OF ENGINEERING

NH 5 Bypass Road, Yattur, T. LURU - 534007 (A.P.)  
(Approved by AICTE, New Delhi and Affiliated to JNT University, Kakinada)

## DEPARTMENT OF MECHANICAL ENGINEERING

7-9-2017.

It is hereby informed to all the staff members of mechanical engineering and all the students of IV B.Tech that there is a workshop on entrepreneurship jointly organised by RCE and NSIC on 9-9-2017 at 10:00 AM, Venue: mechanical engineering e-class room.

HOD/ME

S.NO	YEAR/SECTION	Signature of The Teacher
1	IV A	
2	IV B	

S.No	Name of the faculty	Signature	S.No	Name of the faculty	Signature
1	Dr. Md.Abid Ali		15	Shri.A.Rahul Kumar	
2	Shri.B.Suresh Babu		16	Shri.Bhavanarayan .K	
3	Shri.C.Siva sankar		17	Shri.K.sudhakar babu	
4	Shri.M.Pacha khan		18	Shri Y.Venkatesh	
5	Shri.Chitti babu . G		19	Shri.M.Ekambaram	
6	Shri.K.P.V.S.R Vinay kumar		20	Shri.Sk.Abdul Azeez	
7	Shri.B.Raja Kumar				
8	Shri.Raffi Mohammed				
9	Shri.V Nagarjuna				
10	Shri.M Vimal Teja				
11	Shri.B.Sudhakara Rao				
12	Shri.K.Radha Krishna				
13	Shri.B.Narendra Kumar				
14	Shri.V.Sai Surendra				





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7	Shri.B.Raja Kumar				
8	Shri.Raffi Mohammed				
9	Shri.V Nagarjuna				
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11	Shri.B.Sudhakara Rao				
12	Shri.K.Radha Krishna				
13	Shri.B.Narendra Kumar				
14	Shri.V.Sai Surendra				





# RAMACHANDRA COLLEGE OF ENGINEERING

NH-5 Bypass Road, Vallur (V), SRIPUR - 535502 (A.P.)  
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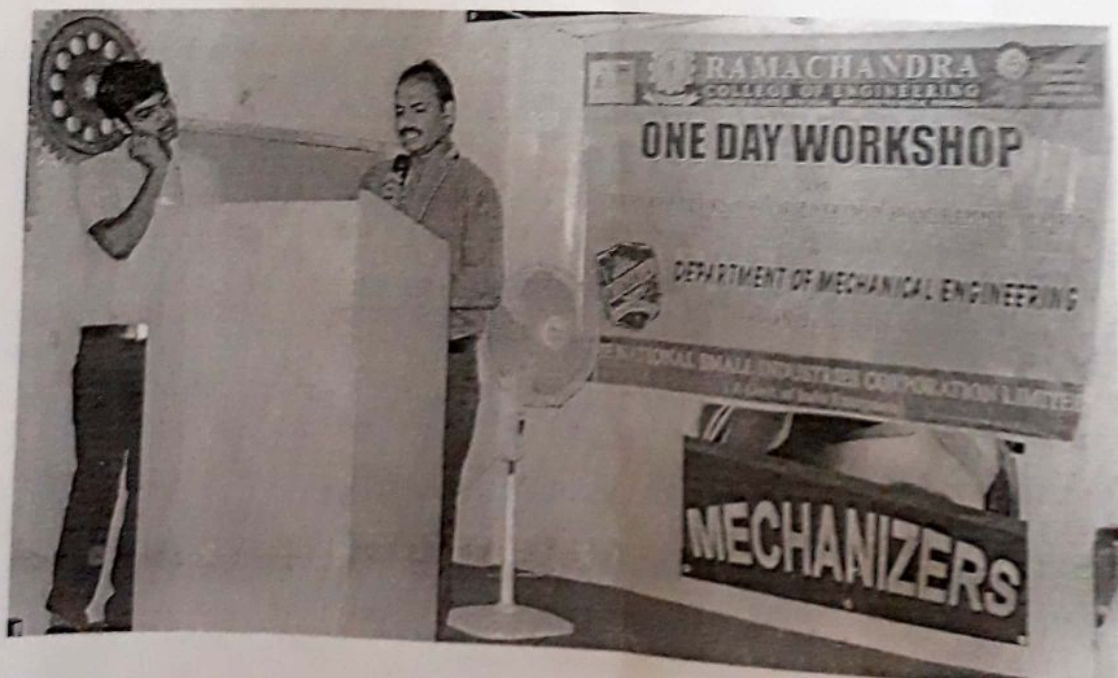
## DEPARTMENT OF MECHANICAL ENGINEERING

11-9

Dr. Bh.V. Prasad said in his speech that there are lots of opportunities in SMEs in the field of tourism, software, food, textiles and horticulture industries. Utilize the opportunity given by the NSIC.



Program coordinator has given the importance of the program.







# RAMACHANDRA COLLEGE OF ENGINEERING

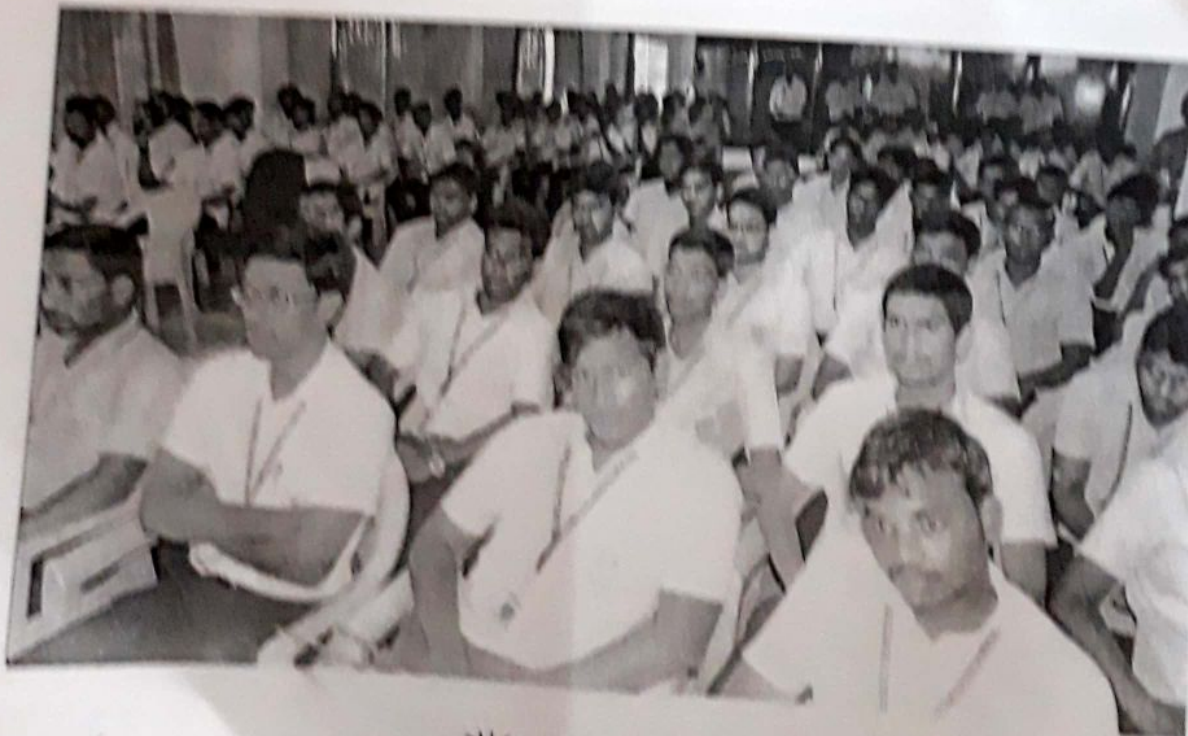
Old S. Byrappa Road, Valluvar 601 110, Kumbakonam - 608002 (T.N.)  
(Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai)

## DEPARTMENT OF MECHANICAL ENGINEERING

Today Mr. G. Sudarshan, takeover the session and gave the importance and created awareness on Entrepreneurship.



All department HoDs, Staff and 100 students of final year ME were participated in this program.



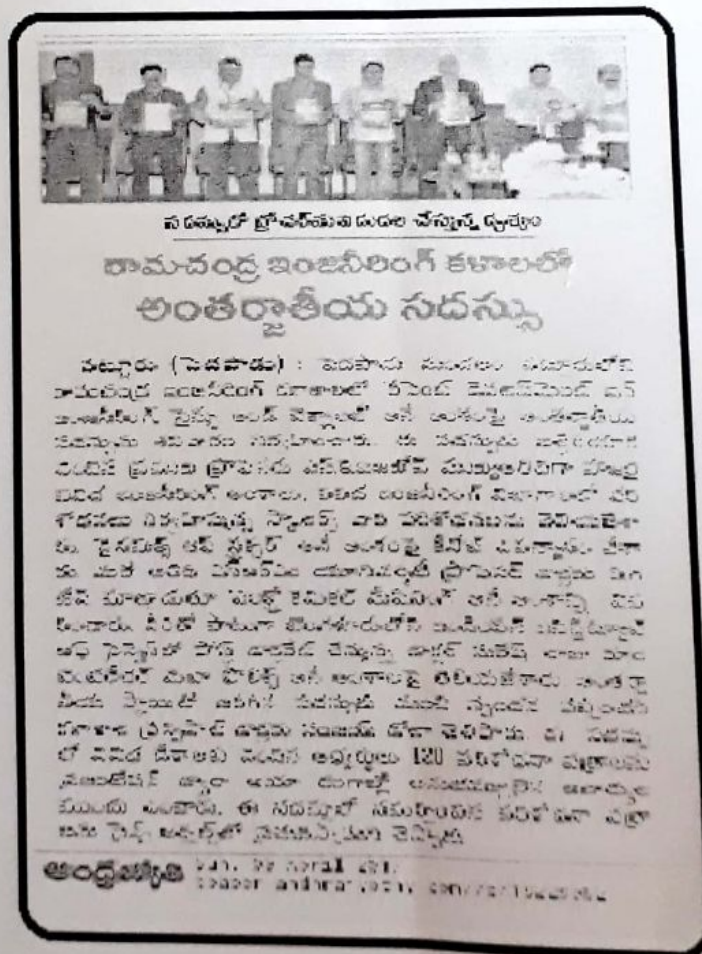
CP  
COORDINATOR

MS  
HOD

PRINCIPAL



## 5



**Signature of Organizer**

Signature of HEAD & DEAN

Signature of Principal  
page 5 of 5

B-1

## Gmail

COMPOSE

ONE DAY WORKSHOP- REG

Inbox x

Inbox (46)

Starred

Important

Sent Mail

Drafts (72)

All Mail

Spam (116)

Trash

Circles

ADHAAR

CBU

faculty +

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11	Shri.B.Sudhakara Rao				
12	Shri.K.Radha Krishna				
13	Shri.B.Narendra Kumar				
14	Shri.V.Sai Surendra				

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On  
**COMPUTATIONAL RESEARCH USING MATLAB**  
(Fill in Block Letters)

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Class & Dept. : \_\_\_\_\_

College : \_\_\_\_\_

Address : \_\_\_\_\_

Mobile : \_\_\_\_\_

E-mail : \_\_\_\_\_

Accommodation required: Yes / No

Fee Payment details:

Draft No. \_\_\_\_\_ Dated \_\_\_\_\_

Amount: \_\_\_\_\_ Bank: \_\_\_\_\_

Date: \_\_\_\_\_ Signature

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Sri. K. Venugopal, Secretary and Correspondent

**Patron**

Dr. Dola Sanjay S, Principal

**Convener**

Mr. M. Pacha Khan, HoD/ME

**Coordinator**

Prof. Bh. V. Prasad,

**Organizing Committee:**

Mr. S. Siva Sankar, Associate Professor

Mr. P. Gangadhar Rao, Associate Professor

Mr. J. Rajasekhar, Assistant Professor

Mr. M. Vimal Teja, Assistant Professor

Mr. V. Nagarjuna, Assistant Professor

Mr. B Sudhakara Rao, Assistant Professor

Mr. Veerla Sai Surendra, Assistant Professor

Mr. M. Ekambaram, Assistant Professor

Mr. G. Chitri Babu, Assistant Professor

Mr. Kotte Bhavanarayana, Assistant Professor

Mr. K P V S R Vinay Kumar, Assistant Professor

Mr. Ketha V Gangadhar praveen, Assistant Professor

Mr. Nudurupati S V N Hanuman, Assistant Professor

Mr. K. Shakti Kiran, Assistant Professor

**DD Drawn in favor of the Principal, RCEE.**

Payable at Eluru

Department of Mechanical Engineering

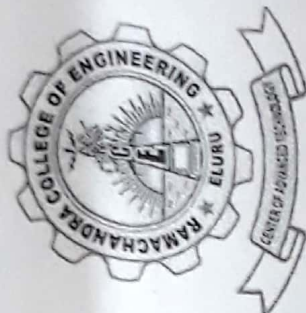
**DST Sponsored**

A Three Day National Level Workshop

On  
**COMPUTATIONAL RESEARCH USING**

**MATLAB**

**10<sup>th</sup> - 12<sup>th</sup> November 2016**



**RAMACHANDRA COLLEGE OF ENGINEERING**

[Approved by AICTE, New Delhi and Affiliated to JNTUK, Kakimaddi]  
NH-5, Bypass Road, Vatturu (V), Eluru - 534007, W. G. District.

AP, 08812 - 215150 (O), 08812 - 215150/52 (R)



**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**Faculty Orientation Program A.Y 2016-17 Odd Sem**

S.No	Date	Time	Lecture By	Lecture Topic	Signature
1	<del>05-02-2016</del> 2-5-2016	10:00 AM	Mr. M. Rama Krishna	Signals & Systems	M.R.K
		10:30 AM	Mr. R.L.R. Lokesh Babu	Micro Processors Controllers	M.R.K
		11:00 AM	Mr. NVDP Murthy	Introduction to LCA	NVDP Murthy
2	<del>05-03-2016</del> 3-5-2016	10:00 AM	Mr. D. Shavalaiah	Embedded systems Appl	D. Shavalaiah
		10:30 AM	Mr. K. Ravendra	ECBC, DSBs Modulation & Demod	D. Ravendra
		11:00 AM	Mr. S.C. Penchalaiah	Basics of Digital Design	S. Penchalaiah
3	<del>05-04-2016</del> 4-5-16	10:00 AM	Mr. T. Mallikarjuna	Intro to Optical Communication	T. Mallikarjuna
		10:30 AM	Mr. Y. Chiranjeevi	Introduction to Computer Networks	Y. Chiranjeevi
		11:00 AM	Mr. R. Raghu Ram	IP, TCP IP, OSI models	R. Raghu Ram



4	05-05-2016	10:00 AM	Mr. B. China Setaiah	Advances buses	Ch. Borah
		10:30 AM	Mr. Geethala Ramana Reddy	Random Variables & Process	G. R. R.
		11:00 AM	Mr. G. Surendra Reddy	Prepersonal Study of Human Behav	G. S. Reddy
		10:00 AM	Mr. P. Satish Babu	Analoge IC Design	P. Setaiah
5	05-08-2016 6-5-16	10:30 AM	Mr. M. Venkata Amarnath	Introduce to DIF	M. S. Amarnath
		11:00 AM	Mr. A. Babul Reddy	Digital IC Applications	A. Babul Reddy
		10:00 AM	Mr. N. Venu	Architecture of EOS1	N. Venu
		10:30 AM	Mr. D. Ashokkumar	Electronic Switching Systems	D. Ashokkumar
6	05-09-2016 9-5-16	11:00 AM	Mrs. Tenalirupa	DSP processors & applications	Tenali Rupa
		10:00 AM	Mr. B. Venkatesh	Networks Security and Cryptography	B. Venkatesh
		10:30 AM	Mr. K. Ravikiran	Transponders & Characteristics	K. Ravikiran
		11:00 AM	Mrs. J. Sridevi	Antennas & waves propagation	J. Sridevi
7	05-10-2016 10-5-16	10:00 AM	Mr. M. Nagendra Prasad	ASIC Processors	M. Nagendra

8	05-11-2016 U-5-16	10:30 AM	Mr. S.K. Rasool	Digital Design Systems	D. L. Murthy
		11:00 AM	Mr. D. Lakshmi murthy		

PRINCIPAL

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
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1	17-10-2016	10:00 AM	Mr. Md. Ali Baig	IC's Designing	Mr. Baig
		10:30 AM	Mr. A. Rajesh Naidu	Computer Architecture	Mr. Naidu
		11:00 AM	Mr. R. Durga Prasad	Introduction to Digital signal Proc	R.D.P
2	18-10-2016	10:00 AM	Mrs. P. Neelima	Control systems & types	P. Neelima
		10:30 AM	Ms. K. Swathi	feedback Amplifiers	K. Swathi
		11:00 AM	Mr. Aziz Md. Abdul	Electronics & Micro systems	Mr. Abdul
3	19-10-2016	10:00 AM	Mr. M. Rama Krishna	Introduction to CS	Mr. Krishna
		10:30 AM	Mr. R.L.R. Lokesh Babu	Architecture of DSP process	R.L.R. Babu
		11:00 AM	Mr. NVDP Murthy	Turned to Linear Te Appce - Microprocessor	Mr. Murthy



4	20-10-2016	10:00 AM	Mr. D. Sravalaiah	Introduction to Venkates	Dishu
		10:30 AM	Mr. K. Raveendra	Filter & Digital Ckts	Chakravarthi
		11:00 AM	Mr. S.C. Penchalaiah	Mos Design	Penela
5	21-10-2016	10:00 AM	Mr. T. Mellikarjuna	Satellite Communication.	T. Mallesh
		10:30 AM	Mr. Y. Chiranjeevi	Design of Digital filters	Chakravarthi
		11:00 AM	Mr. R. Raghu Ram	Routing protocols	P. R. R.
6	24-10-2016	10:00 AM	Mr. B. Chitra kotaliah	Digital filter applications	P. Chakravarthi
		10:30 AM	Mr. Geetha Lal Ramana Reddy	Ac bridge measurement	S. R. Reddy
		11:00 AM	Mr. G. Surendra Reddy	Electron measurement	Chakravarthi
7	25-10-2016	10:00 AM	Mr. P. Satish Babu	Modulators and demodulators	P. Chakravarthi
		10:30 AM	Mr. M. Venkata Amarnath	Discrete Fourier Transform	M. V. R.
		11:00 AM	Mr. A. Babu Reddy	Time to Satellites	Abdul
8	26-10-2016	10:00 AM	Mr. N. Venu	Mosfets	Abdul
		10:30 AM	Mr. D. Ashokkumar	Time to Digital filters	Dishu

~~28-10-16~~

11:00 AM

Mrs. Tennirupa

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### CHIEF PATRON

**Sri. Ghanta Sri Ramachandra Rao** *Chairman*

**Sri. K. Venu Gopal** *Secretary*

**Smt. K. Rama Venu Gopal** *Correspondent*

### PATRON

**Dr. Dola Sanjay S.** *Principal*  
M. Tech., Ph. D.

### CONVENER

**Prof. Udara Yedukondalu** *Prof. & HOD- ECE*

### CO-ORDINATORS

**Mr. Md. Baig Mohammad** *Professor*

**Mr. J. Prasanth Kumar** *Assoc. Professor*

**Mr. M. Rama Krishna** *Assoc. Professor*

### ADVISORY COMMITTEE

**Prof. S. Varadarajan** *Secretary, APSCHE*

**Dr. Ch. Srinivasa Rao** *Prof. JNTUCEV*

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**Dr. G. Raghu Ram** *Former Professor, JNTUK*

**Dr. N. Srinivas Rao** *Former Professor,  
IIT Kharagpur*

**Dr. Dola Sanjay S.** *Principal, RCE*

**Prof. Jagan Mohan Rao S.** *Vice-Principal, RCE*

**Dr. B R Srinivasa Reddy** *Professor-CSE*

**Dr. A. Daveedu Raju** *Professor-CSE*

**Dr. Bh. Varaprasad** *HOD-MBA*

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**Mr. K. Gopala Reddy** *HOD-CSE*

**Mr. B. Satyanarayana** *HOD-CIVIL*

**Mr. K V Subrahmanian** *HOD-H&S*

### ORGANIZING COMMITTEE

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*Assoc. Professor*

**Mr. Devathoti Rambabu**

*Assoc. Professor*

**Mr. Dukkipati Venkanna Babu**

*Assoc. Professor*

**Mr. Ali Baig Mohammad**

*Assoc. Professor*

**Mr. Rajulapati Durga Prasad**

*Assoc. Professor*

**Mrs. Tenali Rupa**

*Asst. Professor*

**Ms. Jalakam Sridevi**

*Asst. Professor*

**Mr. Mekala Venkata Amarnath**

*Asst. Professor*

**Mr. Andra Babul Reddy**

*Asst. Professor*

**Mr. Ramakuri Lal Raja Lokesh Babu**

*Asst. Professor*

**Mr. Shaik Rasool**

*Asst. Professor*

**Mr. Mohammed Abdul Aziz**

*Asst. Professor*

**Mr. Rela Raghuram**

*Asst. Professor*

**Mr. Nandam Venu**

*Asst. Professor*

**Mr. Nandivada V D P Murthy**

*Asst. Professor*

**Mr. Goli Surendar Reddy**

*Asst. Professor*

**Mr. B. China Kotaiah**

*Asst. Professor*

**Mr. D. Ashok Kumar**

*Asst. Professor*

**Mr. D. Lakshmi Murthy**

*Asst. Professor*

**Mr. Danduboina Shavalaiah**

*Asst. Professor*

**Mr. Geethala Ramana Reddy**

*Asst. Professor*

**Mr. Kotagunta Raveendra**

*Asst. Professor*

**Mr. Tholla Mallikarjuna**

*Asst. Professor*

**Mr. N. Rama Krishna**

*Asst. Professor*

### TARGET PARTICIPANTS

- ❖ Faculty members from Engineering colleges & science colleges.
- ❖ Research Scholars.
- ❖ Delegates from Industry/other Organization.

### HOW TO APPLY

The Registration form in the prescribed proforma duly signed by the Head of the Institution / Organization should reach the convener on or before **24.1.2017** along with the registration fee in the form of DD drawn in favour of "**Ramachandra College of Engineering**", payable at Eluru.

## A TWO DAY NATIONAL LEVEL WORKSHOP ON

## “Research Methodologies”



on  
**3<sup>rd</sup> & 4<sup>th</sup> FEBRUARY 2017**

**ORGANIZED BY**

**DEPARTMENT OF  
ELECTRONICS AND COMMUNICATION  
ENGINEERING**

**RAMACHANDRA  
COLLEGE OF ENGINEERING**

**NAAC B++, ISO 9001:2008 Certified,**

**Graded 'AA' by A.P Knowledge Mission, Govt. of A.P**

NH-5, Near Kalaparru Toll Plaza,  
Vatluru (V), Pedapadu (M), **ELURU**.  
West Godavari (Dist.), Andhra Pradesh-534 007  
Ph: +91-8812- 215150, 215151, 215152



### **ABOUT THE COLLEGE**

**RAMACHANDRA COLLEGE OF ENGINEERING** is a vibrant, forward looking Institution offering a high quality learning experience with a welcoming and friendly environment. **RCE** is approved by AICTE and affiliated to JNTUK Kakinada. RCE aims at establishing academic credibility as an Institution offering dynamic UG, PG Engineering & MBA programs with Industry Interface, Research and Consultancy. RCE has a team of highly experienced, well qualified and dedicated faculty members in all disciplines. The Institution is continuously striving for excellence by adopting changes to improve the systems, practices and performance.

### **ABOUT ECE DEPARTMENT**

The Department of Electronics and Communication Engineering came into being in 2008 with an intake of 60 students. It has grown considerably to 180. The department has full-fledged and state of art, latest equipment to suit the needs of student community. The Department has several advanced and well equipped laboratories catering to the needs of students. The Department has received sophisticated state-of-the-art software such as MULTISIM, Xilinx ISE, MATLAB, Mentor Graphics etc., The Department Student Club, HUNT with an aim to promote student activities by conducting technical symposia, technical work shops, arranging guest lectures by eminent personalities in the respective fields

### **ABOUT “Research Methodologies”**

Research is a key feature of academic and industrial world. It plays an important role in the growth of a nation. The nations which had invested in research have flourished well and provided higher standards of living to its citizens. Therefore, in order to carry out research in an effective way, the researchers must be well versed with research methodology and design of experiments. This workshop aims to provide a platform to acquaint, improve, update and plan to do research in the area of Engineering and Science. It broadly encompasses the entire research process from problem definition to report writing and publishing a research paper in the relevant field.

### **TOPICS COVERED**

- ❖ Overview of Research
- ❖ Methodologies of Research
- ❖ Literature Review
- ❖ Defining Research Problem
- ❖ Research Process
- ❖ Planning of research activities
- ❖ Research Paper Writing and many more.....

### **IMPORTANT DATES**

Last date for Registration : 24.1.2017  
Intimation to Participants : 28.1.2017  
Early Bird **Rs. 400/-** : 18.1.2017

### **REGISTRATION FEE : Rs. 500/-**

The registration fee covers the cost of registration kit, participation in all the activities of the workshop, lunch, intersession snacks & tea.

### **OBJECTIVE**

The Workshop aims to acquaint the understanding of Research Methodologies in order to improve the quality of Research and writing thesis.

### **ELIGIBILITY**

This program is open to all the branches of Engineering departments PG students, faculty members, Research Scholars and Engineers from industries.

### **RESOURCE PERSONS**

Eminent Professor from IITs & NITs

### **ADDRESS FOR COMMUNICATION**

**Prof. Md. Baig Mohammad** +91-9493632971  
mohammadbaig33@rcee.ac.in

**Mr. J. Prasanth Kumar** +91-9618525850  
prasanthkumarjsir@gmail.com

**For detailed information go through college web site: [www.rcee.ac.in](http://www.rcee.ac.in)**

### **REGISTRATION FORM**

#### **A TWO DAY NATIONAL LEVEL WORKSHOP ON**

#### **“Research Methodologies”**

**1. Name of the Participant:**

**2. Designation:**

**3. Name of the Organization:**

**4. Address for Communication:**

**5. Phone Number with STD Code :**

**6. Mobile :**

**7. e-mail:**

**8. Registration Fee Details. Amount \_\_\_\_\_**

**DD No. \_\_\_\_\_ Date \_\_\_\_\_**

**Name of the Bank \_\_\_\_\_**

**Signature of the Participant**

#### **PERMITTING AUTHORITY**

**Mr./Ms. \_\_\_\_\_**  
**employed as \_\_\_\_\_ in our**  
**college / organization is permitted to attend the National**  
**level workshop.**

**Place:**

**Date:**

**Signature of permitting authority with seal**



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NH-5 Bypass Road, Vaturlu (V), ELURU - 534 007, A. P.



**B++ Grade**

## Department of Electronics and Communication Engineering

The following is the List of Faculty attended for Research methodologies workshops for A.Y 2016-17.

S.No	Name of the Faculty	Designation	Signature
1	Dr. U. Yedukondalu	Professor & HoD	
2	Dr. Jagan Mohan Rao S.	Professor	
3	Dr. Dola Sanjay S.	Professor	
4	Mr. Md. Baig Mohammad	Assoc. Prof.	
5	Mr. T. Rambabu	Assoc. Prof.	
6	Mr. J. Prasanth Kumar	Assoc. Prof.	
7	Mr. D. Sridhar	Assoc. Prof.	
8	Mr. R. Radha Krishna	Assoc. Prof.	
9	Mr. Md. Ali Baig	Asst. Prof.	
10	Mr. D. Venkanna Babu	Asst. Prof.	
11	Mr. D. Ram Babu	Asst. Prof.	
12	Mr. A. Rajesh Naidu	Asst. Prof.	
13	Mr. R. Durga Prasad	Asst. Prof.	
14	Mr. M. Rama Krishna	Asst. Prof.	
15	Mrs. Y. Lavanya	Asst. Prof.	
16	Mr. NVDP Murthy	Asst. Prof.	
17	Mr. N. Rama Krishna	Asst. Prof.	
18	Mr. R.L.R. Lokesh Babu	Asst. Prof.	
19	Mr. M. Ravi	Asst. Prof.	
20	Mr. G. Rajesh Babu	Asst. Prof.	
21	Mrs. Tenalirupa	Asst. Prof.	
22	Mr. B. China kotaiah	Asst. Prof.	
23	Mr. R. Raghu Ram	Asst. Prof.	
24	Mr. Venu Nandam	Asst. Prof.	
25	Ms. Ch. Naga Lakshmi	Asst. Prof.	

Principal





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**B++ GRADE**

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This is to certify that Dr./Mr./Ms. U. Yedukondalu of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*U. Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
**Principal**  
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This is to certify that Dr./Mr./Ms. Jagan Mohan Rao S. of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
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*Udara Yedukondalu*  
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Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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*Mohammad Baig*  
Coordinator

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
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in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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This is to certify that Dr./Mr./Ms. T. Rambabu of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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*Udara Yedukondalu*  
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This is to certify that Dr./Mr./Ms. J. Prasanth Kumar of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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This is to certify that Dr./Mr./Ms. D. VenkannaBabu of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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Ramachandra College of Engineering has participated  
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This is to certify that Dr./Mr./Ms. Md. Ali Baig of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
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This is to certify that Dr./Mr./Ms. A. Rajesh Naidu of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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This is to certify that Dr./Mr./Ms. R. Durga Prasad of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
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This is to certify that Dr./Mr./Ms. P.Neelima of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
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*Dr. Dola Sanjay S.*  
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This is to certify that Dr./Mr./Ms. K. Swathi of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

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**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
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*Dr. Dola Sanjay S.*  
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This is to certify that Dr./Mr./Ms. Aziz Md.Abdul of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
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This is to certify that Dr./Mr./Ms. M. Rama Krishna of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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**Coordinator**

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(Dr. Dola Sanjay S.)





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Web Site: [www.rcee.ac.in](http://www.rcee.ac.in), 08812-215150 -152, Fax: 08812-215005

An ISO 9001: 2008 certified & 'AA' rated Institution by APKM in AP



**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. R.L.R. Lokesh Babu of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
**Principal**  
(Dr. Dola Sanjay S.)





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**B++ GRADE**

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This is to certify that Dr./Mr./Ms. NVDP Murthy of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
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**B++ GRADE**

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This is to certify that Dr./Mr./Ms. D. Shavalaiah of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. K. Raveendra of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
**Principal**  
(Dr. Dola Sanjay S.)





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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. S.C.Penchalaiah of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
**Principal**  
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## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. T. Mallikarjuna of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
**Principal**  
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## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. Y.Chiranjeevi of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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Communication Engineering, Ramachandra College of Engineering, Eluru.**

Mohammad Baig  
Coordinator

(Mr. Md. Baig Mohammad)

Convener & HoD  
(Mr. Udara Yedukondalu)

Principal  
(Dr. Dola Sanjay S.)





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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. R .Raghu Ram of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
**Principal**  
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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. B. China kotaiah of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. Geethala Ramana Reddy of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. G.Surendra Reddy of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

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**B++ GRADE**

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This is to certify that Dr./Mr./Ms. P. SatishBabu of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
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*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
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*Dr. Dola Sanjay S.*  
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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. M. VenkataAmarnath of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. A. Babul Reddy of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. N. Venu of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
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Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
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**B++ GRADE**

## A TWO DAY NATIONAL LEVEL WORKSHOP on "Research Methodologies" CERTIFICATE

This is to certify that Dr./Mr./Ms. D.Ashokkumar of  
Ramachandra College of Engineering has participated  
in a Two day National Level Workshop on "**RESEARCH METHODOLOGIES**", during  
**3<sup>rd</sup> & 4<sup>th</sup> February 2017**, organized by the **Department of Electronics and  
Communication Engineering, Ramachandra College of Engineering, Eluru.**

*Mohammad Baig*  
**Coordinator**

(Mr. Md. Baig Mohammad)

*Udara Yedukondalu*  
**Convener & HoD**  
(Mr. Udara Yedukondalu)

*Dr. Dola Sanjay S.*  
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Format:9014/0

## Workshop Report

Organized by : Ramachandra College of Engineering  
Name of the Speaker : Mr. Govinda Rao  
Designation : Asst. Manager, Microlink peripheral Controls (P) Ltd.,  
Topic : Workshop on Fundamental concepts on embedded systems  
Venue : EEE e-Class room, C-block  
Date & Time : 13.07.2016 & 11.00AM

Branch	Year	Semester	No of Students Attended
ECE	IV	I	71
Total No of Students Attended			/1

### Profile of the Speaker

Mr. Govinda Rao is known consultant for Embedded systems. He has over 12 years of experience in this industry.

### Report

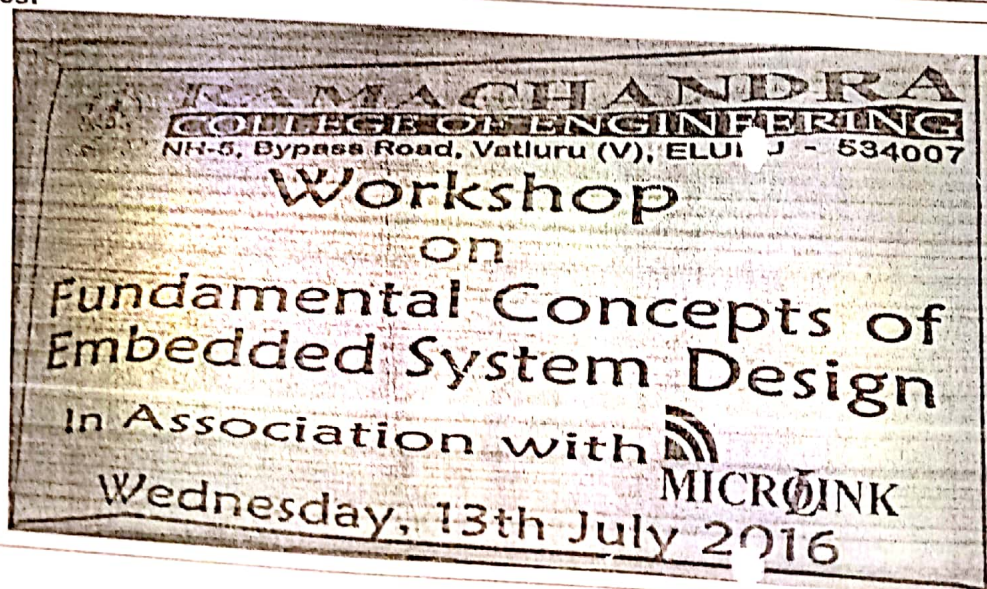
#### 1. Report in brief by Organizer / Coordinator / Convener:

The main objective of the workshop is to make the students have basic knowledge on Embedded system design. One day work shop is organized for the Departments of ECE & EEE on 13-07-2016 on Fundamental Concepts of Embedded System Design. A total of 71 students have attended the workshop.

The workshop is divided into 2-sessions; the first session is totally concentrated in making the students get strong at basics related to the Voltage, potential difference, current, Resistor, Capacitor, and Ohms Law & Fundamentals about op-amps along with practical applications with help of LED.

The second session is totally concentrated on Designing, writing a code & dumping it on Micro-Controllers using Embedded 'C' programming.

#### 2. Photos:

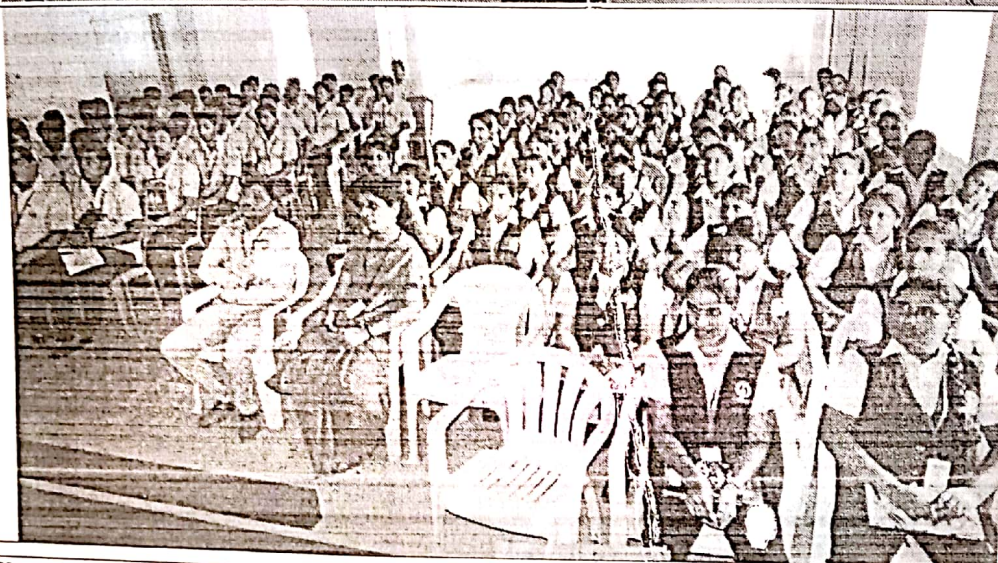
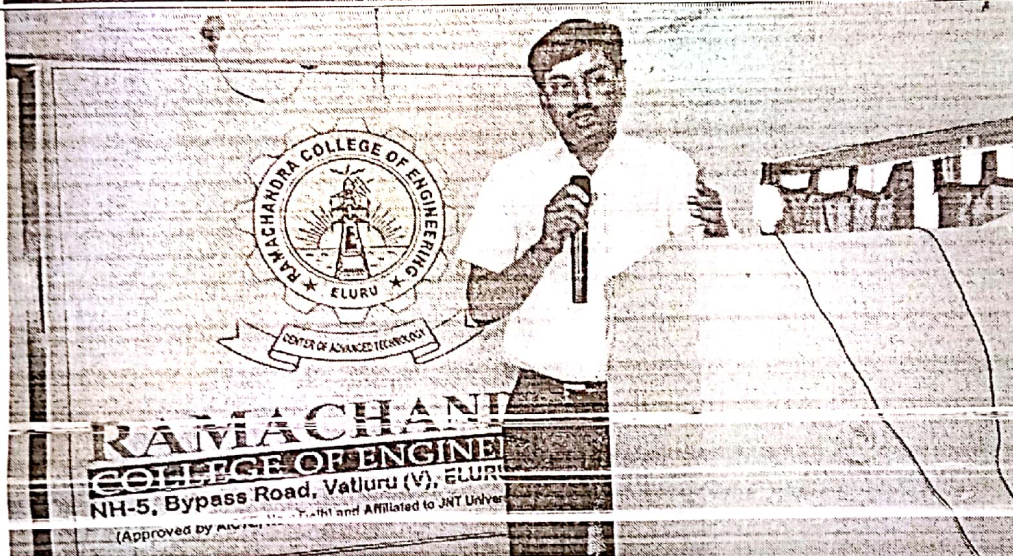






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**3. Feedback from students:** Students' have got good knowledge in introductory part of Embedded Systems. Interaction of the mentor is quite good with the students.

**4. Remarks from Resource Person:** Very good opportunities are available in the field of Embedded systems. Students' are good and pay complete attention on my lecture.

*[Handwritten signature]*





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NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

Letter from HOD to the principal

From:  
Prof. U. Yedukondalu  
Dept of ECE  
RCE,  
Eluru.

Place: Eluru,  
Date: 06-07-2016


To:  
The Principal,  
RCE,  
Eluru.

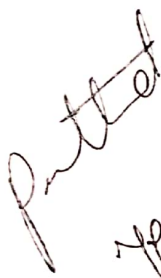
Sub: Requisition for permission to conduct workshop-Reg

Respected sir,

We the Department of ECE going to conduct Workshop on "Fundamental concepts on embedded system" on 13<sup>th</sup> July of 2016. In this context, I request you to grant me permission for the same.

Thanking you sir

  
Yours sincerely,

  
14/7/16  
a/s





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## **Circular**

Date: 06-07-2018

All the students IV year are hereby informed that, Department of ECE is planning to organize a Workshop on "Fundamental concepts in Embedded Systems" on 17<sup>th</sup> July of 2018. In this context all are requested to attend the program without fail.

Venue: ECE Seminar Hall

  
H.O.D.

Date: 15-July-2016

# Certificate of Participation

*This is to certify that*

*Dr. U. Yedukondalu*

*Has successfully completed Three Days Workshop on  
Embedded Systems Designing*

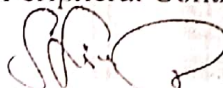
*Conducted from 13<sup>th</sup> to 15<sup>th</sup> July 2016*

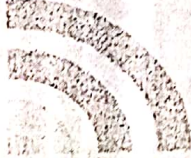
*At Ramachandira College of Engineering, Eluru, Andhra Pradesh.*

## Topics Covered in the Workshop:

- Introduction to Embedded Systems
- Introduction to Hardware Design using simple Ohm's Law
- Circuit Designing Techniques & Drivers Designing
- Development of concept from paper to circuit
- Analysis and selection of components
- Programming Concepts & Chip Copying Procedure

For Microlink Peripheral Controls (P) Ltd.,

  
Authorised Signatory

  
**MICROLINK**







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NAAC



NATIONAL ASSESSMENT AND  
ACCREDITATION COUNCIL

**B++ Grade**

## Department of Electronics and Communication Engineering

The following is the List of Faculty attended for Embedded Systems workshops for A.Y 2016-17.

S.No	Name of the Faculty	Designation	Signature
1	Dr. U. Yedukondalu	Professor & HoD	
2	Dr. Jagan Mohan Rao S.	Professor	
3	Dr. Dola Sanjay S.	Professor	
4	Mr. Md. Baig Mohammad	Assoc. Prof.	
5	Mr. T. Rambabu	Assoc. Prof.	
6	Mr. J. Prasanth Kumar	Assoc. Prof.	
7	Mr. D. Sridhar	Assoc. Prof.	
8	Mr. A. Rajesh Naidu	Asst. Prof.	
9	Mr. R. Durga Prasad	Asst. Prof.	
10	Mrs. P. Neelima	Asst. Prof.	
11	Ms. K. Swathi	Asst. Prof.	
12	Mr. Aziz Md. Abdul	Asst. Prof.	
13	Mr. M. Rama Krishna	Asst. Prof.	
14	Mr. R.L.R. Lokesh Babu	Asst. Prof.	
15	Mr. NVDP Murthy	Asst. Prof.	
16	Mr. D. Shavalaiah	Asst. Prof.	
17	Mr. K. Raveendra	Asst. Prof.	
18	Mr. S.C. Penchalaiah	Asst. Prof.	
19	Mr. T. Mallikarjuna	Asst. Prof.	
20	Mr. Y. Chiranjeevi	Asst. Prof.	
21	Mr. R. Raghu Ram	Asst. Prof.	
22	Mr. B. China Kotaiah	Asst. Prof.	
23	Mr. Geethala Ramana Reddy	Asst. Prof.	

Principal



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Format:9014/0

## WORKSHOP REPORT

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Mr P. Chakradhar

**Designation** : CRT Trainer

**Topic** : Individual & Team Work, Leadership Capability

**Venue** : A-302

S.NO	DATE	SESSION	Faculty Attended (%)
1	18-07-2016 TO 19-07-2016	9.30AM -12.30PM	14

### Report

#### **1. Report in brief by Organizer / Coordinator / Convener:**

Mr P.Chakradhar addressed the faculty related to professional **Individual and Team Work, Leadership Capabilites** and given awareness on Individual and Team Work. He said that as it is central point to the engineering field to successes. And also he told According to research published in Harvard Business Review, team members collaborate more easily if they perceive themselves alike.

The greater the diversity of background and experience, the less likely the team members is to share knowledge or show other collaborative behaviors. Speaker concluded with the following statement *"Coming together is a beginning; keeping together is progress; working together is success."* – Henry Ford on effective teamwork in the workplace.

#### **2. Photos:**







### **3. Feedback from faculty:**

Faculty are undergone through the interactive session and inspired by the speaker to self develop the team work and Leadership Qualities.

### **4. Remarks from Resource Person:**

He asked to practice these regularly by the faculty for the sake of Organization as well as for the benefit of the faculty.

  
(Mr. K. GOPALA REDDY)  
HOD:CSE

  
PRINCIPAL



# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Mr P. Chakradhar

**Designation** : CRT Trainer

**Topic** : Individual & Team Work, Leadership Capability

**Venue** : A-302

S.NO	NAME OF THE FACULTY	SIGNATURE
1	Dr. B Raja Srinivasa Reddy	
2	Dr. A Daveedu Raju	
3	Dr. K S N Prasad	
4	Mr. K Kiran	
5	Mr. K Praveen kumar	
6	Mr. A Bhargavi	
7	Mr. K Viswa Prasad	
8	Mr. K Lakshmanudu	
9	Mr. K N V Devendra Kumar	
10	Ms. V Sai Bhanu	
11	Mr. K.V Varaprasad	
12	Ms. K.Prasad	
13	Mr. P Bhargava	
14	Mr M Murali Krishna	



Eluru,  
30-05-2015

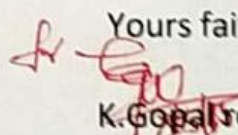
To  
The Principal  
Ramachandra College of Engineering  
Eluru

Sir,

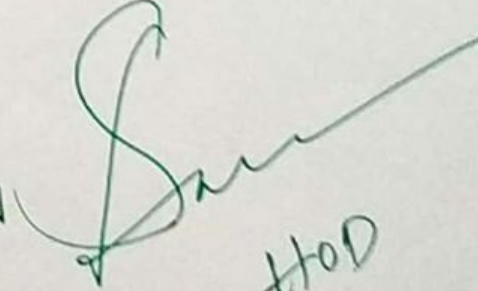
**Sub :** Permission to conduct Workshop for Technical Staff – Reg.

I, K.Gopal Reddy working as Professor&HOD of CSE Department planning to conduct "**C Programming**" training by K.Kiran for Technical Staff in our College on 01-06-2015. Hence, I request you to grant me permission to conduct the program.

Thanking you, Sir

Yours faithfully,  
  
K. Gopal Reddy

HOD-CSE

for  
  
HOD



30-05-2015

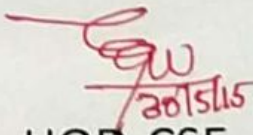
Circular

Rcee/Cse/technical/skill

This is to inform that CSE Department is going to organize skill upgradation in C programming on 01-06-2015. In this regard, all the technical staff should attend without fail.

Venue:DBMS lab

Time:9:30a.m

  
HOD-CSE





# RAMACHANDRA COLLEGE OF ENGINEERING

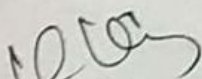
Approved By AICTE, NEW DELHI, Affiliated to JNTUK-Kakinada. NH-5 By-Pass Road, Vatthuru(V), Eluru, 534007 AP

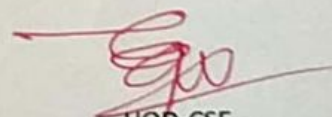
Date: 30-05-2015

Department Of Computer Science and Engineering

Attendance Sheet For Add On Programs

S.NO	NAME OF TECHNICAL STAFF	SIGNATURE
1	Mr. CH VENKATESH	CH Venkatesh
2	Mr. Y AMARNATH	Y. Amarnath
3	Ms. M NAGA SARITHA	M. Naga Saritha
4	Ms. J MANJULA	Manjula
5	Mr. GVENKATA SAI KRISHNA	G. Venkata Sai Krishna
6	Ms. T V PRASANNA	T.V. prasanna
7	Mr. P KISHORE	P. Kishore
8	Mr. R. SATEESH	R. Sateesh

  
FACULTY

  
HOD-CSE

### Technical staff Skills Report

Organized by : Computer Science and Engineering  
Name of the Speaker : K.Kiran  
Designation : Assit.professor-RCE  
Course : C Programming  
Venue : Computer Lab2/DBMS Lab

S.NO	DATE	SESSION
1	01-06-2015	9.30 A.M -4.40 PM

### Report

Report in brief by Organizer / Coordinator / Convener:

Speaker Addressed about Strong Number Programs, Arrays Programs, Series Programs

Photos:





Eluru,

09-11-2015

To  
The Principal  
Ramachandra College of Engineering  
Eluru

Sir,

**Sub :** Permission to conduct Workshop for Technical Staff – Reg.

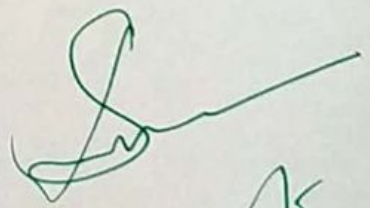
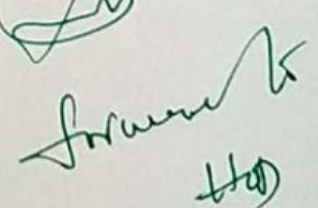
I, K.Gopal Reddy working as Professor&HOD of CSE Department planning to conduct “ **Java Programming**” training by M S Radha M M for Technical Staff in our College on 12-11-2015. Hence, I request you to grant me permission to conduct the program.

Thanking you, Sir

Yours faithfully,

  
K.Gopal reddy

HOD-CSE

for   
  
HOD



9-11-2015

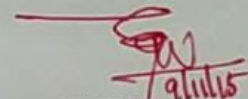
Circular

Rcee/Cse/technical/skill

This is to inform that CSE Department is going to organize skill upgradation in Java programming on 12-11-2015. In this regard, all the technical staff should attend without fail.

Venue:DS lab

Time:9:30a.m

  
HOD-CSE





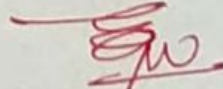
Date: 12-11-2015

Department Of Computer Science and Engineering

Attendance Sheet For Add On Programs

S.NO	NAME OF TECHNICAL STAFF	SIGNATURE
1	Mr. CH VENKATESH	CH. Venkatesh
2	Mr. Y AMARNATH	Y. Amarnath
3	Ms. M NAGA SARITHA	M. Naga Saritha
4	Ms. J MANJULA	Manjula
5	Mr. GVENKATA SAI KRISHNA	G. Venkata Sai Krishna
6	Ms. T V PRASANNA	T.V. Prasanna
7	Mr. P KISHORE	P. Kishore
8	Mr. R. SATEESH	R. Sateesh

H. S. Reddy  
FACULTY

  
HOD-CSE

Date: 12-11-2015

## JAVA PROGRAMMING

### TODAY DAIRY

S.NO	TOPIC COVERED
1	Inheritance
2	Types in inheritance
3	Example programs

H.S.Redd

FACULTY

for (B) polward  
CSE-HOD



### Technical staff Skills Report

**Organized by** : Computer Science and Engineering  
**Name of the Speaker** : M S Radha M M  
**Designation** : Assit.professor-RCE  
**Course** : Java Programming  
**Venue** : Computer Lab5/DS Lab

S.NO	DATE	SESSION
1	12-11-2015	9.30 A.M -4.40 PM

#### **Report**

##### **1. Report in brief by Organizer / Coordinator / Convener:**

The Speaker Addressed about Inheritance & Its Types And Example Programs

##### **2. Photos:**






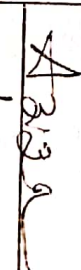







*P. S. Radha*  
Coordinator

*[Signature]*  
HOD

ISO 9001: 2015 certified & NAAAC accredited Institution  
 Accredited

## Department of Electronics and Communication Engineering

### Lab Orientation Program for the A. Y. 2015-16 - Odd Semester

S.No	Name of the Faculty Member	Dates	Time	Name of the lab	Remarks	Signature
1	Mr. D. VenkannaBabu	14-05-2015, 15-05-2015.	9am-12pm	DLD LAB	12 Experiments are conducted and the results are verified	
2	Mr. A. Rajesh Naidu	14-05-2015, 15-05-2015.	1pm-4pm	DSID & DICA LAB	12 Experiments are conducted and the results are verified	
3	Mr. D. Rambabu					
4	Mrs. P. Neelima					
5	Ms. K. Swathi	16-05-2015, 18-05-2015.	9am-12pm	EDC LAB	10 Experiments are conducted and the results are verified	
6	Mr. R. Durga Prasad					
7	Mr. Aziz Md. Abdul					
8	Ms. K. Rajitha Devi	16-05-2015, 18-05-2015.	9am-12pm	EDC LAB	10 Experiments are conducted and the results are verified	
9	Mr. R.L.R. LokeshBabu					
10	Mr. NVDP Murthy					
11	Mr. D. Shavalaiah	16-05-2015, 18-05-2015	1pm-4pm	LICA LAB	12 Experiments are conducted and the results are verified	



D. Akshini sundy		10-05-2015	10-05-2015	results are verified	
14	Mr. K. Paveendra	19-05-2015, 20-05-2015.	9am-12pm	MWE LAB	12 Experiments are conducted and the results are verified
15	Mr. T. Mallikarjuna				
16	Mr. Y. Chiranjeevi	19-05-2015, 20-05-2015.	1pm-4pm	PDC LAB	13 Experiments are conducted and the results are verified
17	Mr. S.C. Penchalaiah				
18	Mr. B. Venkatesh	21-05-2015, 22-05-2015.	9am-12pm	DSP LAB	10 Experiments are conducted and the results are verified
19	Mr. B. China kotaiiah				
20	Mr. G. Ramana Reddy				

Mr. K. Paveendra  
 T. Mallikarjuna  
 Y. Chiranjeevi  
 S.C. Penchalaiah  
 B. Venkatesh  
 B. China kotaiiah  
 G. Ramana Reddy



# RAMACHANDRA COLLEGE OF ENGINEERING

NH-5 Bypass Road, Vathuru (V), Eluru - 534007, W. G. Dist., A.P.  
(Approved by AICTE, New Delhi, Permanently affiliated to JNTUK: Kakimandla)  
NAAAC Accredited with B+++, ISO 9001:2015 Certified

## Department of ECE Orientation classes for A Y 2015-2016(ODD SEM)

S.No	Date	Time	Lecture By	Topic	Signature
1	5.5.2015	10:00 AM	Mr. K. Balaji	Orientation on Digital IC applications	K. Balaji
2		11:00 AM	K. Sai Sudheer	Orientation on Half adder and full adder	K. Sai Sudheer
3	6.5.2015	10:00 AM	K. Ravi Kiran	Introduction to Chip fabrication	K. Ravi Kiran
4		11:00 AM	P. Gopi Krishna	Orientation on Dsp Processors	P. Gopi Krishna
5	7.5.2015	10:00 AM	K. Ravi Kiran	Introduction to Pulse and digital circuits	K. Ravi Kiran
6		11:00 AM	P. Gopi Krishna	OSI Layers and TCP IP	P. Gopi Krishna
7	8.5.2015	10:00 AM	Md. Ali Baig	Basics Of Signals and systems	Md. Ali Baig
8		11:00 AM	A. Rajesh Naidu	Orientation on Intensity modulation	A. Rajesh Naidu
9	9.5.2015	10:00 AM	Mr. K. Balaji	Orientation on Transponder	K. Balaji
10		11:00 AM	K. Sai Sudheer	Introduction to Digital Design	K. Sai Sudheer
11	10.5.2015	10:00 AM	K. Ravi Kiran	Orientation on SSBSC DSBSC Techniques	K. Ravi Kiran
12		11:00 AM	P. Gopi Krishna	Core of the Embedded systems	P. Gopi Krishna
13	11.5.2015	10:00 AM	Md. Ali Baig	Basics Of Antennas	Md. Ali Baig



14	11:00 AM	A. Rajesh Naidu	Probability theory and stochastic process	A. Rajesh Naidu
15	10:00 AM	Mr. K. Balaji	Ethics and human values	K. Balaji
	11:00 AM	K. Sai Sudheer	Orientation on Network security	K. Sai Sudheer
			Orientation to transponder	K. Ravikiran
18	11:00 AM	P. Gopi Krishna	Introduction to IC Design	P. Gopi Krishna
19	10:00 AM	Mid. Ali Baig	Orientation on Asics Cots processors	Mid. Ali Baig
20	11:00 AM	A. Rajesh Naidu	Introduction to microprocessors and controllers	A. Rajesh Naidu
21	10:00 AM	Mr. K. Balaji	Electronic switching systems	K. Balaji
22	11:00 AM	K. Sai Sudheer	Introduction to computer networks	K. Sai Sudheer
23	10:00 AM	K. Ravi Kiran	Introduction to computer Architecture	K. Ravi Kiran
24	11:00 AM	P. Gopi Krishna	Introduction to Image Processing	P. Gopi Krishna

*Sai*



**MACHANDRA**  
**JEEGEOR ENGINEERING**  
Vijaya Road, Vatturu (V), Eluru - 534007, W. G. Dist., A.P.  
Accredited with B++1, ISO 9001:2015 Certified

Attendance Sheet

1	K. Ravi Kiran	Pall
2	P. Gopi Krishna	Geopla
3	Mr. K. Balaji	Belagi
4	K. Sai Sudhan	Sai Sudhan
5	K. Ravi Kiran	Geopla
6	P. Gopi Krishna	Geopla
7	Mr. K. Balaji	P. Geopla
8	A. Raju	Mr. K. Balaji
9	Mr. K. Balaji	A. Raju
10	K. Sai Sudhan	K. Balaji
11	K. Ravi Kiran	K. Sai Sudhan
12	P. Gopi Krishna	K. Ravi Kiran
13	Mr. K. Balaji	P. Gopi Krishna
14	A. Raju	Mr. K. Balaji
15	Mr. K. Balaji	A. Raju
16	K. Sai Sudhan	K. Balaji
17	K. Ravi Kiran	K. Sai Sudhan
18	P. Gopi Krishna	K. Ravi Kiran
19	Mr. K. Balaji	P. Gopi Krishna
20	A. Raju	Mr. K. Balaji
21	Mr. K. Balaji	A. Raju
22	K. Sai Sudhan	K. Balaji
23	Mr. K. Balaji	K. Sai Sudhan
24	K. Sai Sudhan	Mr. K. Balaji





# RAMACHANDRA COLLEGE OF ENGINEERING

NH-5 Bypass Road, Vaturu (V), Eluru - 534007, W. G. Dist., A.P.  
(Approved by AICTE, New Delhi, Permanently affiliated to JNTUK: Kakinada)  
NAAAC Accredited with B++ , ISO 9001:2015 Certified

## Department of ECE Orientation classes for A Y 2015-2016(EVEN SEM)

S.No	Date	Time	Lecture By	Topic	Signature
1	6.10.2015	10:00 AM	Mr. K. Balaji	Introduction to Pulse and Digital Circuits	K. Balaji
2		11:00 AM	K. Sai Sudheer	Orientation on CMOS Design	K. Sai Sudheer
3	7.10.2015	10:00 AM	K. Ravi Kiran	Orientation On Satellite communications	K. Ravikiran
4		11:00 AM	P. Gopi Krishna	Design of Digital Filters	P. Gopikrishna
5	8.10.2015	10:00 AM	K. Ravi Kiran	Orientation on Routing Protocols	K. Ravikiran
6		11:00 AM	P. Gopi Krishna	Orientation on Digital Filter Applications	P. Gopikrishna
7	9.10.2015	10:00 AM	Md. Ali Baig	Ac Bridge measurement	Md. Ali Baig
8		11:00 AM	A. Rajesh Naidu	Introduction on Electronic measurement and Instrumentation	A. Rajesh Naidu
9	10.10.2015	10:00 AM	Mr. K. Balaji	Introduction on Wireless sensor networks	K. Balaji
10		11:00 AM	K. Sai Sudheer	Introduction to Discrete fourier transforms	K. Sai Sudheer
11	11.10.2015	10:00 AM	K. Ravi Kiran	Orientation on MOSFETS	K. Ravikiran

12		11:00 AM	P. Gopi Krishna	Orientation On Frequency Reuse	P. Gopi Krishna
13	12.10.2015	10:00 AM	Md. Ali Baig	Orientation on IC Designing	Md. Ali Baig
14		11:00 AM	A. Rajesh Naidu	Orientation on computer architecture	A. Rajesh Naidu
15	13.10.2015	11:00 AM	K. Sai Sudheer	Orientation on DSP Processors	K. Sai Sudheer
16		11:00 AM	K. Sai Sudheer	Orientation control systems	K. Sai Sudheer
17	14.10.2015	10:00 AM	K. Ravi Kiran	Orientation on Feedback Amplifiers	K. Ravi Kiran
18		11:00 AM	P. Gopi Krishna	Orientation on Electromagnetic waves	P. Gopi Krishna
19	15.10.2015	10:00 AM	Md. Ali Baig	Introduction to control systems and types	Md. Ali Baig
20		11:00 AM	A. Rajesh Naidu	Architecture Of DSP Processors	A. Rajesh Naidu
21	16.10.2015	10:00 AM	Mr. K. Balaji	Orientation on Linear Circuits	K. Balaji
22		11:00 AM	K. Sai Sudheer	Orientation on DSP Processors and its types	K. Sai Sudheer
23	17.10.2015	10:00 AM	K. Ravi Kiran	Introduction to linear ic applications	K. Ravi Kiran
24		11:00 AM	P. Gopi Krishna	Introduction to Verilog	P. Gopi Krishna

*P. Gopi Krishna*





# AMACHANDRA COLLEGE OF ENGINEERING

Bypass Road, Vatluru (V), Eluru - 534007, W. G. Dist., A.P.  
(Approved by AICTE, New Delhi, Permanently affiliated to JNTUK, Kakinada)  
AAC Accredited with B++, ISO 9001:2015 Certified

## Attendance Sheet

1	Mr. K. Balaji	K. Balaji
2	K. Sai Sudhe	K. Sai Sudhe
3	K. Ravi Kiran	K. Ravi Kiran
4	P. Gopi Krish	P. Gopi Krish
5	K. Ravi Kiran	K. Ravi Kiran
6	P. Gopi Krish	P. Gopi Krish
7	Md. Ali Baig	Md. Ali Baig
8	A. Rajesh Naidu	A. Rajesh Naidu
9	Mr. K. Balaji	K. Balaji
10	K. Sai Sudhe	K. Sai Sudhe
11	K. Ravi Kiran	K. Ravi Kiran
12	P. Gopi Krish	P. Gopi Krish
13	Md. Ali Baig	Md. Ali Baig
14	A. Rajesh Naidu	A. Rajesh Naidu
15	Mr. K. Balaji	K. Balaji
16	K. Sai Sudhe	K. Sai Sudhe
17	K. Ravi Kiran	K. Ravi Kiran
18	P. Gopi Krish	P. Gopi Krish
19	Md. Ali Baig	Md. Ali Baig
20	A. Rajesh Naidu	A. Rajesh Naidu
21	Mr. K. Balaji	K. Balaji
22	K. Sai Sudhe	K. Sai Sudhe
23	K. Ravi Kiran	K. Ravi Kiran
24	P. Gopi Krish	P. Gopi Krish



### **WORKSHOP REPORT**

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Mr D Anil Kumar, Shreya's Data insights

**Designation** : Industry Expert

**Topic** : Apps development using Android

**Venue** : A-302

S.NO	DATE	SESSION	Faculty Attended
1	04/03/2016 to 05/03/2016	9.30AM -12.30PM	8

### **Report**

#### **1. Report in brief by Organizer / Coordinator / Convener:**

Mr **D Anil Kumar**, This Domain enables learners to successfully apply core Java programming languages features & software patterns needed to develop maintainable mobile apps comprised of core Android components, as well as fundamental Java I/O & persistence mechanisms. Learners who successfully complete this Domain will be well-prepared to master the more advanced material in the subsequent "Mobile Cloud Computing with Android" Specialization.

#### **2. Photos:**



#### **3. Feedback from Faculty:**

faculty are undergone through the interactive session and inspired by the speaker to self develop the Various android apps.



**4. Remarks from Resource Person:**

He asked to practice these regularly by faculty for the sake of Organization as well as for the benefit of faculty. Resource person has proclaimed the importance of self updating and development of app development for getting the god job.



(Mr. K. GOPALA REDDY)  
HOD:CSE



PRINCIPAL



# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Mr D Anil Kumar, Shreya's Data insights

**Designation** : Industry Expert

**Topic** : Apps development using Android

**Venue** : A-302

S.NO	NAME OF THE FACULTY	SIGNATURE
1	Dr. B V V S Prasad	
2	Dr. A Daveedu Raju	
3	Dr. K S N Prasad	
4	Mr. K Kiran	
5	Mr. P Chakradhar	
6	Mr. K Gopala Reddy	
7	Ms. S Nagavali	
8	Mr. P Sudhakar	
9	Mr. O Shiv Bhagawan	
10	Mr. B Yedukondalu	





**WORKSHOP REPORT**

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Mr R Satish

**Designation** : Asst Prof.

**Topic** : 3D Animation

**Venue** : A-302

S.NO	DATE	SESSION	Faculty Attended (%)
1	20/11/2015 to 21/11/2015	9.30AM -12.30PM	10

**Report**

**1. Report in brief by Organizer / Coordinator / Convener:**

**Mr R Satish** addressed the faculty related to Animation as Animation is all about generating a chain of drawings or pictures taken by way of a simulation procedure for creating movement. It is a type of optical illusion through which we are able to see still images or drawings moving. The procedure involves the manifestation of motion as a result of displaying still pictures or photographs one after the other at the rate of 24 pictures per second. A video program or motion picture is considered one of the most common methods of presenting animation. However, there are various other forms that can be used for presenting animation. Here, we will try having an idea about 3D Animation and the difference between 3D and 2D Animation.

He also showed the different videos with multiple 3d effects to practically to the faculty and faculty, really they are very inspirational videos, faculty are eagerly wait for the more practical videos. Resource person also advised to visit the some sites, and his blog.

## 2. Photos:



## 3. Feedback from faculty:

Faculty practically saw the various 3d effects in the video clips and enjoyed, a lot and faculty also requested me to arrange an another practical session

## 4. Remarks from Resource Person:

He advised to encourage and facilitate the lab sessions to the faculty that they practice and learn the animations, and he told to upgrade the systems and loaded with the software's. he also referred some tools to both faculty and faculty to which are open source.

  
(Mr. K. GOPALA REDDY)  
HOD:CSE

  
PRINCIPAL





**RAMACHANDRA  
COLLEGE OF ENGINEERING**  
(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

**WORKSHOP REPORT**

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Mr R Satish

**Designation** : Asst Prof.

**Topic** : 3D Animation

**Venue** : A-302

S.NO	NAME OF THE FACULTY	SIGNATURE
1	Dr. B V V S Prasad	
2	Dr. A Daveedu Raju	
3	Dr. K S N Prasad	
4	Mr. K Viswa Prasad	
5	Mr. P Bhargava	
6	Mr. A Bhargavi	
7	Mr. Deevena Raju	
8	Mr M Navya Sri	
9	Mr M Murali Krishna	
10	Mr D Uday Kumar	

Eluru,

10-11-2014

To  
The Principal  
Ramachandra College of Engineering  
Eluru


Sir,

**Sub :** Permission to conduct Workshop for Technical Staff – Reg.

I, P M Prasuna working as Professor&HOD of CSE Department planning to conduct “ **DBMS** ” training by CH.Raghu Kumar for Technical Staff in our College on 12-11-2014. Hence, I request you to grant me permission to conduct the program.

Thanking you, Sir

Yours faithfully,

  
P M Prasuna  
HOD-CSE

✓  
10/11/14  
For wavy  
HOD



Date: 12-11/2014

DBMS

TODAY DAIRY

S.NO	TOPIC COVERED
1	Crete tables (employee ,dept)
2	Operations on tables

9/11/2014  
FACULTY

P.M. Prasad  
CSE-HOD



# RAMACHANDRA COLLEGE OF ENGINEERING

Approved By AICTE, NEW DELHI, Affiliated to JNTUK-Kakinada. NH-5 By-Pass Road, Vatluru(V), Eluru, 534007 A.P

Date: 12-11-2010

Department Of Computer Science and Engineering

Attendance Sheet For Add On Programs

S.NO	NAME OF TECHNICAL STAFF	SIGNATURE
1	Mr. G Venkata Sai Krishna	G. Venkata Sai Krishna
2	Ms. T V Prasanna	T.V. Prasanna
3	Mr. P Kishore	P. Kishore
4	Mr. R. Sateesh	R. Sateesh

FACULTY

P.M. Prasanna.  
CSE-HOD



Use the following data for Employee and Department tables.

EMPNO	} PRIMARY KEY      FOREIGN KEY {	DEPTNO
ENAME		DNAME
JOB		LOC
MGR		
HIREDATE		
SAL		
COMM		
DEPTNO		

**EMP TABLE**

REFERENCING TABLE: **DEPT TABLE**

**Sample data:**

EMPNO	ENAME	JOB	MGR	HIREDATE	SAL	COMM	DEPTNO
7369	SMITH	CLERK	7902	17-Dec-80	800		20
7499	ALLEN	SALESMAN	7698	20-Feb-81	1600	300	30
7521	WARD	SALESMAN	7698	22-Feb-81	1250	500	30
7566	JONES	MANAGER	7839	2-Apr-81	2975		20
7654	MARTIN	SALESMAN	7698	28-Sep-81	1250	1400	30
7698	BLAKE	MANAGER	7839	1-May-81	2850		30
7782	RAJU	MANAGER	7839	9-Jun-81	2450		10
7788	SCOTT	ANALYST	7566	19-Apr-87	4000		20
7844	TURNER	SALESMAN	7698	8-Sep-81	1500	0	30
7876	ADAMS	CLERK	7788	23-May-87	1100		20
7900	JAMES	CLERK	7698	3-Dec-81	950		30
7902	FORD	ANALYST	7566	3-Dec-81	4000		20
7934	MILLER	CLERK	7782	23-Jan-82	1300		10

DEPTNO	DNAME	LOC
10	ACCOUNTING	NEW YORK
20	RESEARCH	DALLAS
30	SALES	CHICAGO
40	OPERATIONS	BOSTON

DEPTNO	DEPTNAME	LOCATION	DESIGNATION
9	accounting	hyderabad	manager
10	research	chennai	professor
11	sales	banglore	salesman
12	operations	mumbai	operator
9	accounting	chennai	manager

c) List the records of dept table grouped by deptno.

SQL> select deptno,deptname from department group by deptno,deptname;

DEPTNO DEPTNAME

```

9 accounting
12 operations
10 research
11 sales

```

d) Update the record where deptno is 9.

SQL> update department set designation='accountant' where deptno=9;

2 rows updated.

SQL> select \* from employee;

DEPTNO	DEPTNAME	LOCATION	DESIGNATION
9	accounting	hyderabad	accountant
10	research	chennai	professor
11	sales	banglore	salesman
12	operations	mumbai	operator
9	accounting	chennai	accountant

e) Delete any column data from the table.

SQL> alter table department drop(designation);

Table altered.

SQL> select \* from department;

DEPTNO	DEPTNAME	LOCATION
9	accounting	hyderabad
10	research	chennai
11	sales	banglore
12	operations	mumbai
9	accounting	Chennai





Date: 2/6/2014

Circular

Rcee/Cse/technical/skill

This is to inform that CSE Department is going to organize skill upgradation in C programming on 04-06-2014. In this regard, all the technical staff should attend without fail.

Venue: computer lab1

Time: 9:10 a.m

P.M. Prasanna.  
2/6/14  
HOD-CSE

u 2/6/14  
For work  
HOD

Eluru,

02-05-2014

To  
The Principal  
Ramachandra College of Engineering  
Eluru

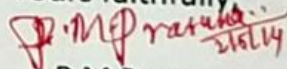
Sir,

**Sub :** Permission to conduct Workshop for Technical Staff – Reg.

I, P M Prasuna working as Professor&HOD of CSE Department planning to conduct " **C Programming**" training by P. Chakradhar for Technical Staff in our College on ~~02-06~~ 21/06/2014. Hence, I request you to grant me permission to conduct the program.

Thanking you, Sir

Yours faithfully,

  
P M Prasuna  
HOD-CSE





date: 02-16-2014

### Attendance Sheet For Add On Programs

S.NO	NAME OF TECHNICAL STAFF	SIGNATURE
1	Mr. G Venkata Sai Krishna	G. Venkata Sai Krishna
2	Ms. T V Prasanna	T.V. Prasanna
3	Mr. P Kishore	P. Kishore
4	Mr. R. Sateesh	R. Sateesh

P. M. Prabhu.  
CSE-HOD

**CSE-HOD**

*[Signature]*  
FACULTY 04/06/14

### Technical staff Skills Report

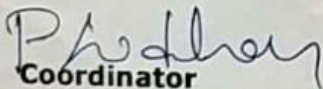
Organized by : Computer Science and Engineering  
Name of the Speaker : P.Chakradhar  
Designation : Assit.professor-RCE  
Course :C programming  
Venue : Computer Lab2/DBMS Lab

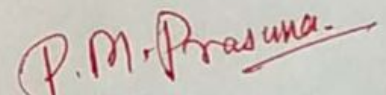
S.NO	DATE	SESSION
1	04-06-2014	9.30 A.M -4.40 PM

#### **Report**

1. Report in brief by Organizer / Coordinator / Convener:






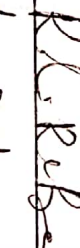




The Speaker Addressed about structure, unions, enum, typedef.

  
Coordinator

  
HOD



**Lab Orientation Program for the A. Y. 2014-15 - Odd Semester**

S.N	Name of the Faculty Member	Dates	Time	Name of the lab	Remarks	Signature
0						
1	Mrs. P. Neelima	19-05-2014, 20-05-2014.	9am-12pm	DLD LAB	12 Experiments are conducted and the results are verified	
2	Ms. K. Swathi					
3	Mr. R. Durga Prasad					
4	Mr. Aziz Md. Abdul	19-05-2014, 20-05-2014.	1pm-4pm	DSB & DICA LAB	12 Experiments are conducted and the results are verified	
5	Ms. K. Rajitha Devi					
6	Mr. R.L.R. Lokesh Babu					
7	Mr. M. Rama Krishna					
8	Mr. D. Lakshmi murthy	21-05-2014, 22-05-2014.	9am-12pm	EDC LAB	10 Experiments are conducted and the results are verified	
9	Mr. K. Ravendra					
10	Mr. B. Venkatesh					

D. Akshini sundy		10-05-2015	10-05-2015	results are verified	
14	Mr. K. Paveendra	19-05-2015, 20-05-2015.	9am-12pm	MWE LAB	12 Experiments are conducted and the results are verified
15	Mr. T. Mallikarjuna				
16	Mr. Y. Chiranjeevi	19-05-2015, 20-05-2015.	1pm-4pm	PDC LAB	13 Experiments are conducted and the results are verified
17	Mr. S.C. Penchalaiah				
18	Mr. B. Venkatesh	21-05-2015, 22-05-2015.	9am-12pm	DSP LAB	10 Experiments are conducted and the results are verified
19	Mr. B. China kotaiiah				
20	Mr. G. Ramana Reddy				

Mr. K. Paveendra  
 T. Mallikarjuna  
 Y. Chiranjeevi  
 S.C. Penchalaiah  
 B. Venkatesh  
 B. China kotaiiah  
 G. Ramana Reddy





# RAMACHANDRA COLLEGE OF ENGINEERING

NH-5 Bypass Road, Vaturu (V), Eluru - 534007, W.G. Dist., A.P.  
(Autonomous Institute Since 2008, Previously affiliated to JNTUK: Kakinada)

Sl. No.	Date	Time	Lecture By	Topic		Signature
1	2.5.2014	10:00 AM	Md. Ali Baig	Basics Of Antennas		<i>Ali</i>
2		11:00 AM	A. Rajesh Naidu	Probability theory and stochastic process		<i>A. Rajesh Naidu</i>
3	3.5.2014	10:00 AM	Mr. K. Balaji	Ethics and human values		<i>Balaji</i>
4		11:00 AM	K. Sai Sudheer	Orientation on Network security		<i>Sudheer</i>
5	4.5.2014	10:00 AM	K. Ravi Kiran	Introduction to transponder		<i>Kiran</i>
6		11:00 AM	P. Gopi Krishna	Introduction to IC Design		<i>Gopi</i>
7	5.5.2014	10:00 AM	Md. Ali Baig	Orientation on Asics Cots processors		<i>Ali</i>
8		11:00 AM	A. Rajesh Naidu	Introduction to microprocessors and controllers		<i>A. Rajesh Naidu</i>
9	6.5.2014	10:00 AM	Mr. K. Balaji	Electronic switching systems		<i>Balaji</i>
10		11:00 AM	K. Sai Sudheer	Introduction to computer networks		<i>Sudheer</i>
11	7.5.2014	10:00 AM	K. Ravi Kiran	Introduction to computer Architecture		<i>Kiran</i>
12		11:00 AM	P. Gopi Krishna	Introduction to Image Processing		<i>Gopi</i>
13	8.5.2014	10:00 AM	Md. Ali Baig	Orientation on Digital IC applications		<i>Ali</i>

14	0.5.2014	11:00 AM	A. Rajesh Naidu	Orientation on Half adder and full adder	AKS
15	0.5.2014	10:00 AM	Mr. K. Balaji	Introduction to Chip fabrication	K. Balaji
				Introduction on Deep Processors	Genes
				Layers and Materials	Genes
19	11.5.2014	10:00 AM	Mr. Md. Ali Baig	Basics Of Signals and systems	Genes
20		11:00 AM	A. Rajesh Naidu	Orientation on Intensity modulation	AKS
21	2.5.2014	10:00 AM	Mr. K. Balaji	Orientation on Transponder	Genes
22		11:00 AM	K. Sai Sudheer	Introduction to Digital Design	Genes
23	12.5.2014	10:00 AM	K. Ravi Kiran	Orientation on SSBSO DSBSO Techniques	K. Ravi
24		11:00 AM	P. Gopi Krishna	Core of the Embedded systems	P. Gopi

Signature  
HOD





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**COLLEGE OF ENGINEERING**  
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(Approved by AICTE, New Delhi, Permanently affiliated to JNTUK, Kakinada)  
NAAC Accredited with B++, ISO 9001:2015 Certified

Attendance Sheet

1	Md. Ali Baig	A Baig
2	A. Rajesh Naidu	A Rajesh Naidu
3	Mr. K. Balaji	B Balaji
4	K. Sai Sudheer	Sai Sudheer
5	K. Ravi Kiran	Ravi Kiran
6	P. Gopi Krishna	Gopi Krishna
7	Md. Ali Baig	A Baig
8	A. Rajesh Naidu	A Rajesh Naidu
9	Mr. K. Balaji	B Balaji
10	K. Sai Sudheer	Sai Sudheer
11	K. Ravi Kiran	Ravi Kiran
12	P. Gopi Krishna	Gopi Krishna
13	Md. Ali Baig	A Baig
14	A. Rajesh Naidu	A Rajesh Naidu
15	Mr. K. Balaji	B Balaji
16	K. Sai Sudheer	Sai Sudheer
17	K. Ravi Kiran	Ravi Kiran
18	P. Gopi Krishna	Gopi Krishna
19	Md. Ali Baig	A Baig
20	A. Rajesh Naidu	A Rajesh Naidu
21	Mr. K. Balaji	B Balaji
22	K. Sai Sudheer	Sai Sudheer
23	K. Ravi Kiran	Ravi Kiran
24	P. Gopi Krishna	Gopi Krishna



**RAMACHANDRA COLLEGE OF ENGINEERING**  
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 ISO 9001:2015 Certified

Department of ECE Orientation classes for A Y 2014-2015(EVEN SEM)				
S.No	Date	Time	Lecture By	Topic
1	12.10.2014	10:00 AM	Md. Ali Baig	Orientation on IC Designing
2	13.10.2014	11:00 AM	A. Rajesh Naidu	Orientation on computer architecture
3		10:00 AM	Mr. K. Balaji	Orientation on DSP Processors
4		11:00 AM	K. Sai Sudheer	Orientation control systems
5	14.10.2014	10:00 AM	K. Ravi Kiran	Orientation on Feedback Amplifiers
6		11:00 AM	P. Gopi Krishna	Orientation on Electromagnetic waves
7		10:00 AM	Md. Ali Baig	Introduction to control systems and types
8	15.10.2014	11:00 AM	A. Rajesh Naidu	Architecture Of DSP Processors
9		10:00 AM	Mr. K. Balaji	Orientation on linear Circuits
10		11:00 AM	K. Sai Sudheer	Orientation on DSP Processors and its types
11	17.10.2014	10:00 AM	Mr. K. Balaji	Introduction to linear ic applications
12		11:00 AM	K. Sai Sudheer	Introduction to Verilog



13	18.10.2014	10:00 AM	K. Ravi Kiran	Introduction to Pulse and Digital Circuits	Pavithra
14		11:00 AM	P. Gopi Krishna	Orientat on on CMOS Design	Gopini
17	20.10.2014		Mr. K. Ravi Kiran	Orientation On Satellite communications	Balaji
18		11:00 AM	P. Gopi Krishna	Orientation on Routing Protocols	Endur
19	21.10.2014	10:00 AM	Md. Ali Baig	Ac Bridge measurement	Gopini
20		11:00 AM	A. Rajesh Naidu	Introduction on Electronic measurement and Instrumentation	Balaji
21	22.10.2014	10:00 AM	Mr. K. Balaji	Introdu ction on Wireless sensor networks	Endur
22		11:00 AM	K. Sai Sudheer	Introduction to Dicrete fourier transforms	Balaji
23	23.10.2014	10:00 AM	K. Ravi Kiran	Orientation on MOSFETS	Pavithra
24		11:00 AM	P. Gopi Krishna	Orientation On Frequency Reuse	Gopini

Signature  
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# RAMACHANDRA COLLEGE OF ENGINEERING

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(Approved by AICTE, New Delhi, Permanently affiliated to JNTUK: Kakinada)  
NAAC Accredited with B++, ISO 9001:2015 Certified

## Attendance Sheet

1	Mr. K. Balaji	K. Balaji
2	K. Sai Sudheer	K. Sai Sudheer
3	K. Ravi Kiran	K. Ravi Kiran
4	P. Gopi Krishna	P. Gopi Krishna
5	K. Ravi Kiran	K. Ravi Kiran
6	P. Gopi Krishna	P. Gopi Krishna
7	Md. Ali Baig	Md. Ali Baig
8	A. Rajesh Naidu	A. Rajesh Naidu
9	Mr. K. Balaji	K. Balaji
10	K. Sai Sudheer	K. Sai Sudheer
11	K. Ravi Kiran	K. Ravi Kiran
12	P. Gopi Krishna	P. Gopi Krishna
13	Md. Ali Baig	Md. Ali Baig
14	A. Rajesh Naidu	A. Rajesh Naidu
15	Mr. K. Balaji	K. Balaji
16	K. Sai Sudheer	K. Sai Sudheer
17	K. Ravi Kiran	K. Ravi Kiran
18	P. Gopi Krishna	P. Gopi Krishna
19	Md. Ali Baig	Md. Ali Baig
20	A. Rajesh Naidu	A. Rajesh Naidu
21	Mr. K. Balaji	K. Balaji
22	K. Sai Sudheer	K. Sai Sudheer
23	K. Ravi Kiran	K. Ravi Kiran
24	P. Gopi Krishna	P. Gopi Krishna

HOD





### **WORKSHOP REPORT**

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Mr k kiran

**Designation** : Professor

**Topic** : Software Engineering and Testing

**Venue** : A-302

S.NO	DATE	SESSION	Faculty Attended (%)
1	23-01-2015 to 24-01-2015	9.30AM -12.30PM	10

### **Report**

#### **1. Report in brief by Organizer / Coordinator / Convener:**

Mr K.Kiran addressed the faculty about software engineering and testing . **Software engineering** is a detailed study of engineering to the design, development and maintenance of software. Software engineering was introduced to address the issues of low-quality software projects. Problems arise when software generally exceeds timelines, budgets, and reduced levels of quality.

**Software testing** is an investigation conducted to provide stakeholders with information about the quality of the software product or service under test.<sup>1</sup> Software testing can also provide an objective, independent view of the software to allow the business to appreciate and understand the risks of software implementation. Test techniques include the process of executing a program or application with the intent of finding software bugs (errors or other defects), and verifying that the software product is fit for use.

Software testing methods are traditionally divided into white- and black-box testing. These two approaches are used to describe the point of view that the tester takes when designing test cases.

#### **2. Photos:**



**3. Feedback from faculty :**

Faculty understood the concept of software engineering and testing, how it is useful in software life cycle. Appreciated about the speaker lecture

**4. Remarks from Resource Person:**

Speaker appreciated the faculty for their attention towards his lecture and enlightened the faculty about various job opportunities regarding software engineering and testing



Coordinator



PRINCIPAL





# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

Organized by : Computer Science and Engineering

Name of the Speaker : Mr k kiran

Designation : Professor

Topic : Software Engineering and Testing

Venue : A-302

S.NO	NAME OF THE FACULTY	SIGNATURE
1	Dr A Daveedu Raju	
2	Dr K S N Prasad	
3	Ms. P Manikya Prasuna	
4	Mr. K Kiran	
5	Mr. K Gopala Reddy	
6	Mr. P Chakradhar	
7	Mr. D Ravi Chandran	
8	Mr. P Sudhakar	
9	Ms. S Nagavali	
10	Ms. A Bhargavai	



**WORKSHOP REPORT**

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Mr R Satish

**Designation** : Asst Professor

**Topic** : Android Framework

**Venue** : A-302

S.NO	DATE	SESSION	Faculty Attended (%)
1	24/10/2014 to 25/10/2014	9.30AM -12.30PM	10

**Report**

**2. Report in brief by Organizer / Coordinator / Convener:**

The Speaker addressed the gathering of faculty about The main aim of frameworks is to increase productivity by reducing efforts which eventually saves lot of time for developers to resolve any other important issues in the app or game. These frameworks provides inbuilt tools for developers to work instantly on difficult and lengthy part of coding.

In today's rapid development world, almost all programming languages have frameworks to help the developers. With instant growth of mobile devices a lot of frameworks are also increased whether it be for Android, Windows or iOS.

He also listed the top most android frame works

1. Corona SDK, 2. Phonegap, 3. Xamarin, 4. Sencha Touch, 5. Appcelerator,
6. Basic4Android, 7. JQuery Mobile, 8. Dojo Mobile, 9. Sproutcore, 10. Theappbuilder



11. DHTMLX Touch, 12. Mo Sync SDK.

With almost 3 million apps on Google Play, the Android operating system dominates the mobile landscape. Individuals, small businesses, and large enterprises work hard to establish a strong mobile presence and grab their share of the market. However, not everyone has the expertise and resources required to build an app from scratch using native tools.

## 2. Photos:



## 3. Feedback from faculty:

Faculty got the overview about android framework and mobilized to penetrate into the various opportunities to develop the android apps. Faculty also noted some of the open source frameworks for developing the use app for society.

## 4. Remarks from Resource Person:

Speaker appreciated the faculty for their participation in the events held. They paid full attention to His lecture. Speaker also allowed the faculty to contact with him if any doubt about the android frame work .

  
Coordinator

  
PRINCIPAL



# RAMACHANDRA COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

Organized by : Computer Science and Engineering

Name of the Speaker : Mr R Satish

Designation : Asst Professor

Topic : Android Framework

Venue : A-302

S.NO	NAME OF THE FACULTY	SIGNATURE
1	T Hima Bindhu	
2	Dr A Daveedu Raju	
3	Ms. P Manikya Prasuna	
4	Mr. K Kiran	
5	Mr. K Gopala Reddy	
6	Mr. P Chakradhar	
7	Mr. D Ravi Chandran	
8	Mr. P Sudhakar	
9	Ms. S Nagavali	
10	Mr. B Yedukondalu	





# **RAMACHANDRA**

## **COLLEGE OF ENGINEERING**

(Approved by AICTE, New Delhi, Affiliated to JNTUK: Kakinada)  
NH-5 Bypass Road, Vatluru (V), ELURU – 534 007, A. P.

Format:9014/0

### **WORKSHOP REPORT**

**Organized by** : Computer Science and Engineering

**Name of the Speaker** : Dr JSVN Mani Kumar,

**Designation** : Prof ACE Engg. College

**Topic** : Cybercrime and Ethical Hacking

**Venue** : A-302

S.NO	DATE	SESSION	Faculty Attended (%)
1	5/09/2014 to 6/09/2014	9.30AM -12.30PM	10

#### **Report**

##### **1. Report in brief by Organizer / Coordinator / Convener:**

**Dr JSVN Mani Kumar** Addressed the faculty related to Ethical Hacking-it aides identifying weakness in computer systems and/or computer networks and coming with countermeasures that protect the weaknesses. Ethical hackers must abide certain rules.

He also spoke on the use of computers and networks to perform illegal activities such as spreading computer viruses, online bullying, performing unauthorized electronic fund transfers, etc. Most cyber crimes are committed through the internet. Some cyber crimes can also be carried out using Mobile phones via SMS and online chatting applications

He stated the different types of crimes and its consequences related to the Punishment and prism.

##### **2. Photos:**



**3. Feedback from faculty:**

Faculty Understood the major threats in computer usage and necessary steps to protect from the these attacks.

**4. Remarks from Resource Person:**

He asked faculty to learn the security tips and apply regularly to avoid big hazards. He also specified various mechanism to ensure the security and recovery from various threats. He also asked them to be regularly in touch with the developments of industry.



Coordinator



PRINCIPAL





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NH-5 Bypass Road, Vatluru (V), ELURU - 534 007, A. P.

Organized by : Computer Science and Engineering

Name of the Speaker : Dr JSVN Mani Kumar,

Designation : Prof ACE Engg. College

Topic : Cybercrime and Ethical Hacking

Venue : A-302

S.NO	NAME OF THE FACULTY	SIGNATURE
1	T Hima Bindhu K.V. Varaprasad	K.V.V.
2	Dr A Daveedu Raju	
3	Dr K S N Prasad	K.S.N.
4	Ms. P Manikya Prasuna	P.M.P.
5	Mr. K Kiran	K.K.
6	Mr. K Gopala Reddy	K.G.R.
7	Mr. P Chakradhar	P.C.
8	Mr. D Ravi Chandran	D.R.C.
9	Mr. P Sudhakar	P.S.
10	T Hima Bindhu	T.H.B.